

2N6284 (NPN); 2N6286, 2N6287 (PNP)

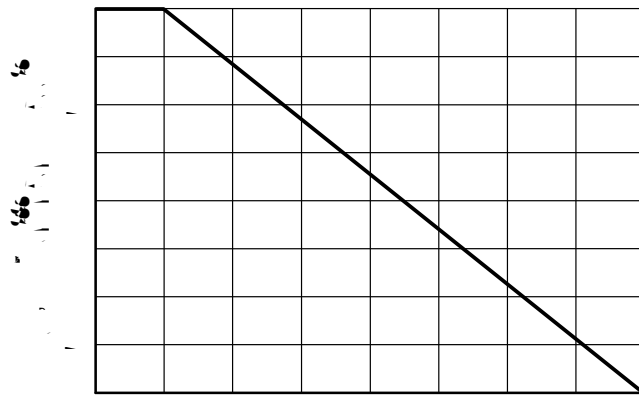


Figure 1. Power Derating

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Min	Max	Unit
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OFF CHARACTERISTICS

2N6284 (NPN); 2N6286, 2N6287 (PNP)

ACTIVE-REGION SAFE OPERATING AREA

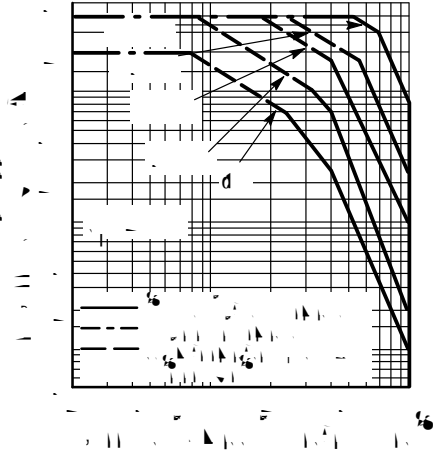


Figure 5. 2N6284, 2N6287

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e. the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 5 is based on $T_{J(pk)} = 200\text{ C}$; T_C is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} < 200\text{ C}$. $T_{J(pk)}$ may be calculated from the data in Figure 4. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

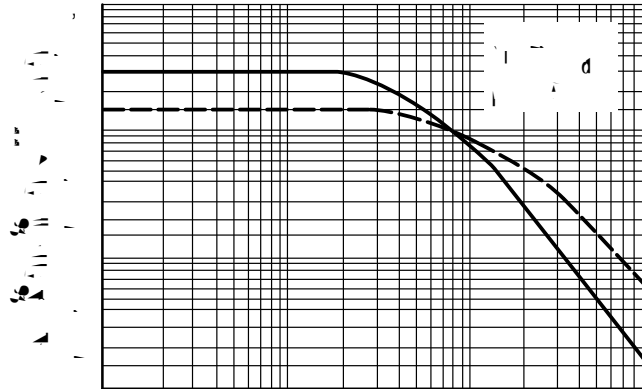


Figure 6. Small-Signal Current Gain

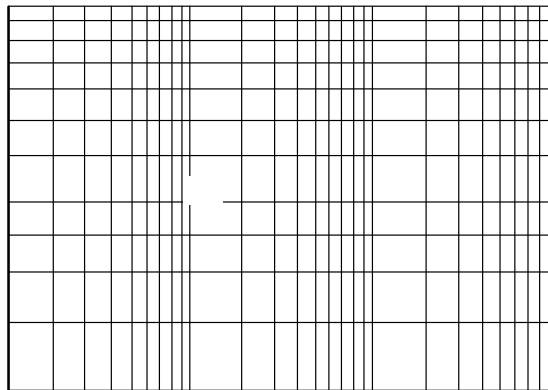


Figure 7. Capacitance

2N6284 (NPN); 2N6286, 2N6287 (PNP)

NPN
2N6284

PNP
2N6287

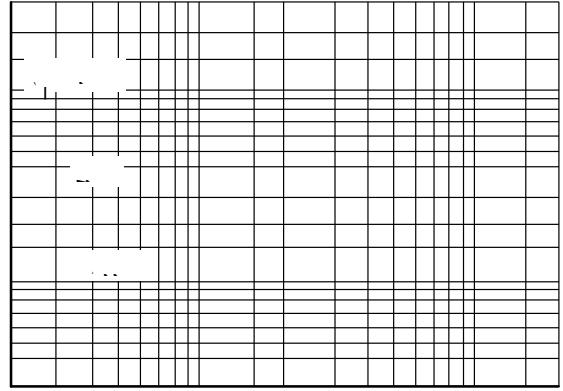
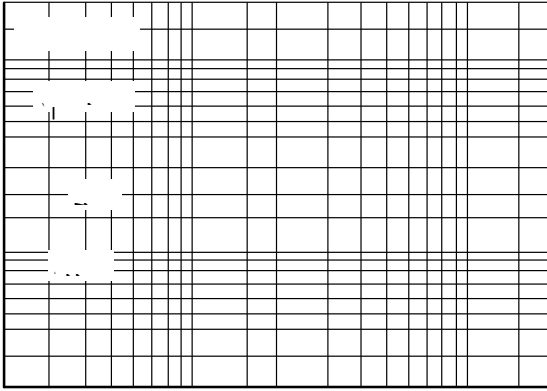


Figure 8. DC Current Gain

2N6284 (NPN); 2N6286, 2N6287 (PNP)

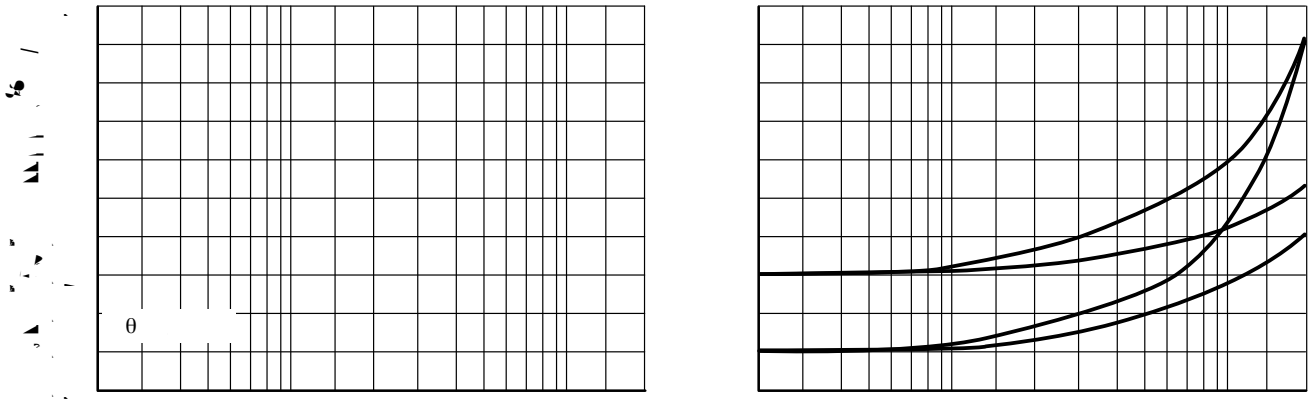
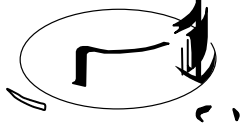


Figure 11. Temperature Coefficients

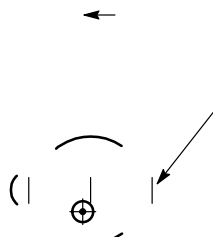
E6: 1. A E 2. E E CASE: C EC	E7: 1. A DE 2. E CASE: CA DE	E8: 1. CA DE #1 2. CA DE #2 CASE: A DE	E9: 1. A DE #1 2. A DE #2 CASE: CA DE
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TO-204 (TO-3)



S A 1:1

E8
 1. D E A D E A C E A
 14.5, 1982.
 2. C D E : C
 3. A E A D E A CA ED
 EFE E CED -204AA E A



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