

General Description

The AC138/ACT138 is a high speed 1 of 8 decoder/demultiplexer. This device is ideally suited for high speed bipolar memory chip select address decoding. The multiple input enables allow parallel expansion to a 1 of 24 decoder using just three AC138/ACT138 devices or a 1 of 32 decoder using four AC138/ACT138 devices and one inverter

Features

I_{CC} Reduced by 50%
Demultiplexing Capability
Multiple Input Enable for Easy Expansion
Active LOW Mutually Exclusive Outputs
Outputs Source/Sink 24 mA
ACT138 Has TTL Compatible Inputs
These are Pb Free Devices

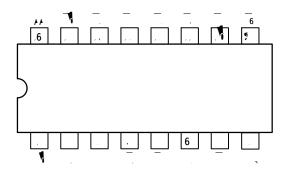


Figure 1. Pinout: 16-Lead Packages Conductors
(Top View)

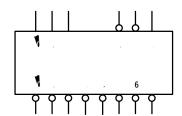
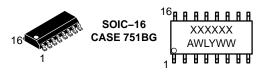


Figure 2. Logic Symbol

MARKING DIAGRAMS





XXX = Specific Device Code
A = Assembly Location
WL or L = Wafer Lot
Y = Year
WW or W = Work Week
G or = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information on page 8 of this data sheet.

PIN DESCRIPTIONS

PIN	FUNCTION		
A ₀ , A ₁	Address Inputs		
Ē	Enable Inputs		

Ε

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Rating	Unit
V _{CC}	Supply Voltage	-0.5 to +6.5	V
I _{IK}	DC Input Diode Current $V_{I} = -0.5 \text{ V}$ $V_{I} = V_{CC} + 0.5 \text{ V}$	-20 +20	mA
VI	_	-	="

DC ELECTRICAL CHARACTERISTICS FOR AC

				$T_A = +25^{\circ}C$ $T_A = -40^{\circ}C$ to +8		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	>
Symbol	Parameter	V _{CC} (V)	Conditions	Тур	G	uaranteed Limits	Unit
V_{IH}			•	-	-		-

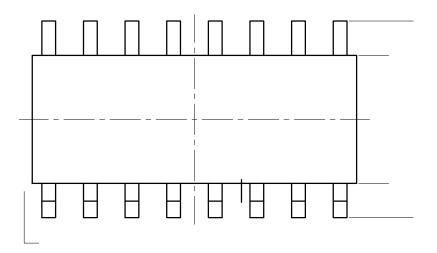
DC ELECTRICAL CHARACTERISTICS FOR ACT

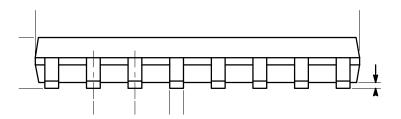
				T _A = +	-25°C	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	
Symbol	Parameter	V _{CC} (V)	Conditions	Тур	Gı	uaranteed Limits	Unit
V _{IH}	Minimum HIGH Level	4.5	V _{OUT} 0.1 V or V _{CC} 0.1 V	1.5	2.0	2.0	V
	Input Voltage	5.5		1.5	2.0	2.0	
V _{IL}	'	•	•	•	•	<u>-</u>	•

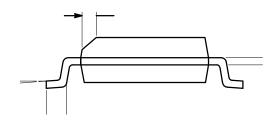


SOIC-16, 150 mils CASE 751BG ISSUE O

DATE 19 DEC 2008



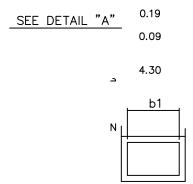


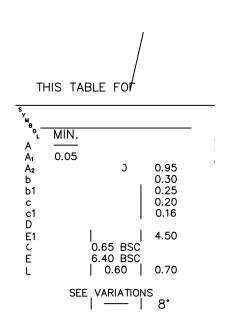


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DESCRIPTION:	SOIC-16, 150 mils		PAGE 1 OF 1

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SIDE VIEW

