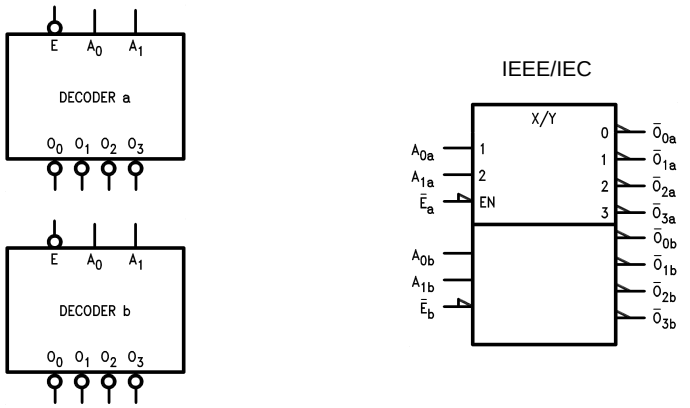
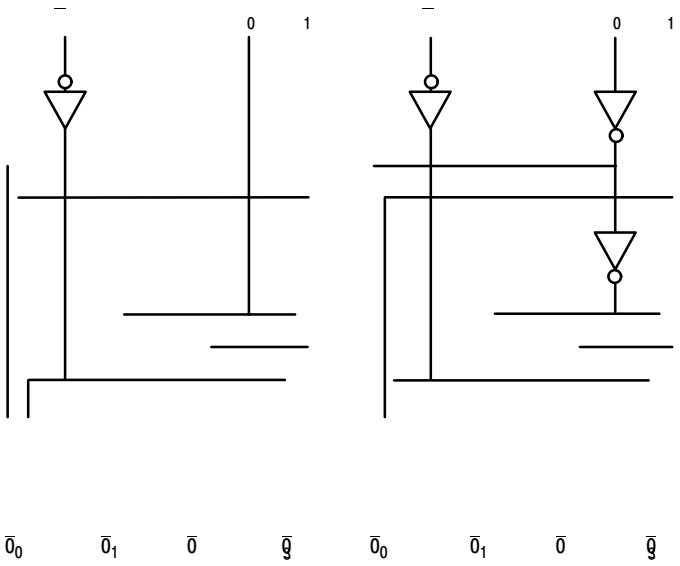


Figure 1. Logic Symbols



NOTE: This diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

Figure 2. Logic Diagram

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Rating	Unit
V_{CC}	Supply Voltage	−0.5 to +6.5	V
I_{IK}	DC Input Diode Current V_I = −0.5 V V_I = V_{CC} + 0.5 V	 −20 +20	mA
V_I			

DC ELECTRICAL CHARACTERISTICS FOR AC

Symbol	Parameter	V_{CC} (V)	Conditions	T_A = +25°C	T_A = −40°C to +85°C		Unit
				Typ	Guaranteed Limits		
V_{IH}	Minimum HIGH Level Input Voltage	3.0	V_{OUT} = 0.1 V or V_{CC} – 0.1 V	1.5	2.1	2.1	V
		4.5		2.25	3.15	3.15	
		5.5		2.75	3.85	3.85	
V_{IL}	Maximum LOW Level Input Voltage	3.0	V_{OUT} = 0.1 V or V_{CC} – 0.1 V	1.5	0.9	0.9	V
		4.5		2.25	1.35	1.35	
		5.5		2.75	1.65	1.65	
V_{OH}	Minimum HIGH Level Output Voltage	3.0	I_{OUT} = –50 μA	2.99	2.9	2.9	V
		4.5		4.49	4.4	4.4	
		5.5		5.49	5.4	5.4	
		3.0	V_{IN} V_{IL} or V_{IH}, I_{OH} = –12 mA		2.56	2.46	
		4.5	V_{IN} V_{IL} or V_{IH}, I_{OH} = –24 mA		3.86	3.76	
		5.5	V_{IN} V_{IL} or V_{IH}, I_{OH} = –24 mA (Note 1)		4.86	4.76	
V_{OL}	Maximum LOW Level Output Voltage	3.0	I_{OUT} = 50 μA	0.002	0.1	0.1	V
		4.5		0.001	0.1	0.1	
		5.5		0.001	0.1	0.1	
		3.0	V_{IN} V_{IL} or V_{IH}, I_{OL} = 12 mA		0.36	0.44	
		4.5	V_{IN} V_{IL}				

DC ELECTRICAL CHARACTERISTICS FOR ACT

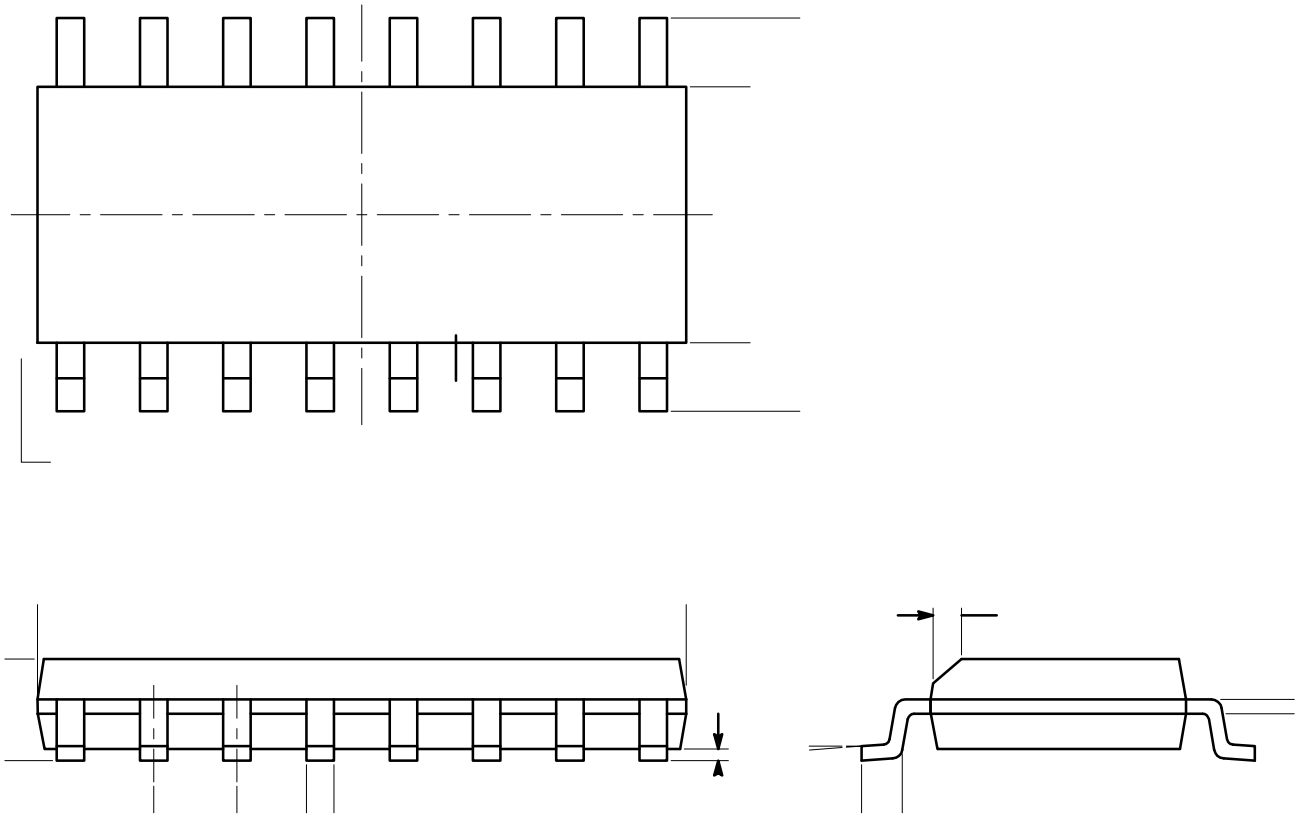
Symbol	Parameter	V_{CC} (V)	Conditions	T_A = +25°C	T_A = −40°C to +85°C		Unit
				Typ	Guaranteed Limits		
V_{IH}	Minimum HIGH Level Input Voltage	4.5	V_{OUT} 0.1 V or V_{CC} 0.1 V	1.5	2.0	2.0	V
		5.5		1.5	2.0	2.0	
V_{IL}	Maximum LOW Level Input Voltage	4.5	V_{OUT} 0.1 V or V_{CC} 0.1 V	1.5	0.8	0.8	V
		5.5		1.5	0.8	0.8	
V_{OH}	Minimum HIGH Level Output Voltage	4.5	I_{OUT} 50 μA	4.49	4.4	4.4	V
		5.5		5.49	5.4	5.4	
		4.5	V_{IN} V_{IL} or V_{IH}. I_{OH} 24 mA		3.86	3.76	
		5.5	V_{IN} V_{IL} or V_{IH}. I_{OH} 24 mA (Note 4)		4.86	4.76	

AC ELECTRICAL CHARACTERISTICS FOR AC

Symbol	Parameter	V_{CC} (V) (Note 6)	T_A = +25°C, C_L = 50 pF			T_A = −40°C to +85°C, C_L = 50 pF		Unit
			Min	Typ	Max	Min	Max	
t_{PLH}	Propagation Delay, A_n to O							

SOIC−16, 150 mils
CASE 751BG
ISSUE O

DATE 19 DEC 2006



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DESCRIPTION:	**SOIC−16, 150 mils**		**PAGE 1 OF 1**

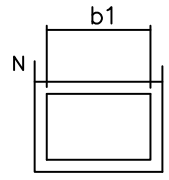
TSSOP 16
CASE 948AH
ISSUE O

DATE 19 SEP 2008

SEE DETAIL "A"

0.19
0.09

4.30



THIS TABLE FOR

SYMBOL	MIN.		
A			
A_1	0.05		
A_2			0.95
b			0.30
b1			0.25
c			0.20
c1			0.16
D			
E1			4.50
e		0.65 BSC	
E		6.40 BSC	
L		0.60	0.70
	SEE VARIATIONS		
		—	8°