

74AC245, 74ACT245

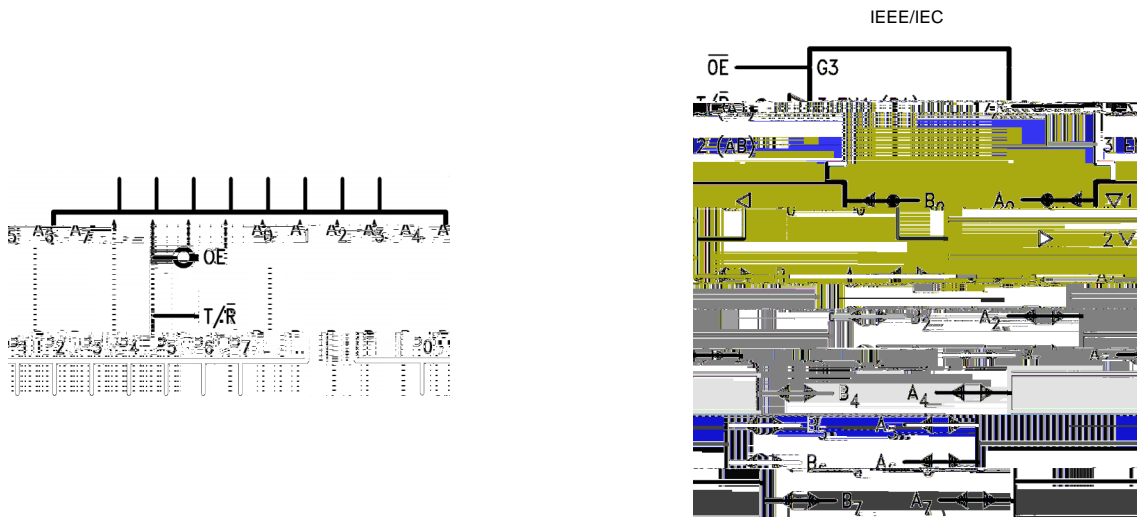


Figure 2. Logic Symbol

PIN DESCRIPTIONS

Pin Name	Description
\overline{OE}	Output Enable Input
T/R	Transmit / Receive Input
A ₀ -A ₇	Side A 3-STATE Inputs or 3-STATE Outputs
B ₀ -B ₇	Side B 3-STATE Inputs or 3-STATE Outputs

TRUTH TABLE

Input		Output
\overline{OE}	TR	
L	L	Bus B Data to Bus A
L	H	Bus A Data to Bus B
H	X	HIGH-Z State

H = HIGH Voltage Level
 L = LOW Voltage Level
 X = Immaterial

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Rating	Unit
V _{CC}	Supply Voltage	-0.5 to +6.5	V
I _{IK}	DC Input Diode Current V _I = -0.5 V	-20	mA
	V _I = V _{CC} + 0.5	+20	mA
V _I	DC Input Voltage	-0.5 to V _{CC} + 0.5	V
I _{OK}	DC Output Diode Current V _O = -0.5 V	-20	mA
	V _O = V _{CC} + 0.5 V	+20	mA
V _O	DC Output Voltage	-0.5 to V _{CC} + 0.5	V
I _O	DC Output Source or Sink Current	±50	mA
I _{CC} or I _{GND}	DC V _{CC} or Ground Current per Output Pin	±50	mA
T _{STG}	Storage Temperature	-65 to +150	°C
T _J	Junction Temperature	140	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

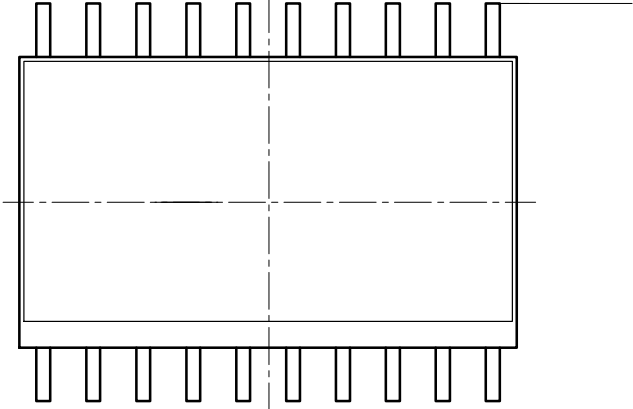
74AC245, 74ACT245

AC ELECTRICAL CHARACTERISTICS FOR ACT

Symbol	Parameter	V _{CC} (V) Note 7)	T _A = +25°C C _L = 50 pF			T _A = -40°C to +85°C C _L = 50 pF		Unit
			Min.	Typ.	Max.	Min.	Max.	
t _{PLH}	Propagation Delay, A _n to B _n or B _n to A _n	5.0 or B						

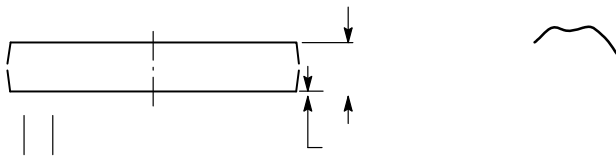
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CASE 751BJ
ISSUE O

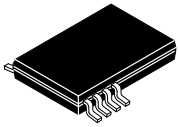
DATE 19 DEC 2008



SOIC-20 WB
CASE 751D-05
ISSUE H

DATE 22 APR 2015





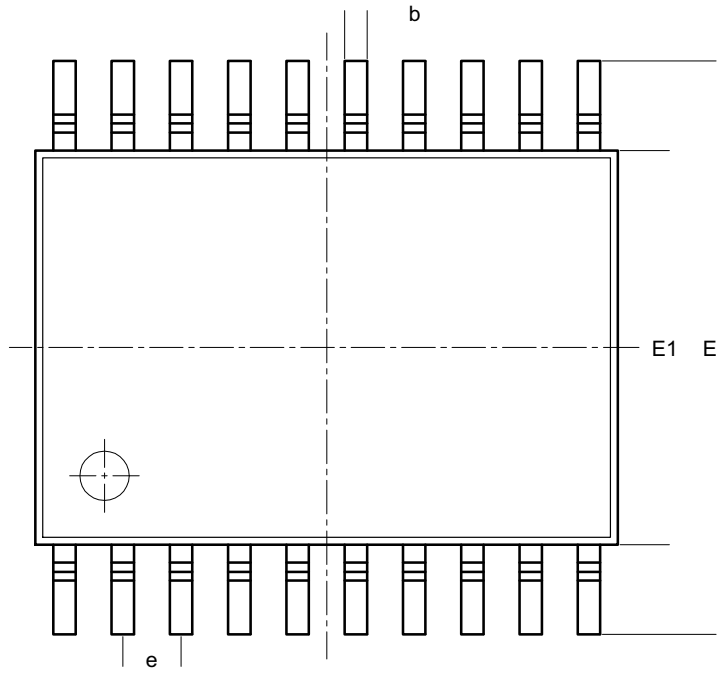
SCALE 2:1

TSSOP-20 WB
CASE 948E
ISSUE D

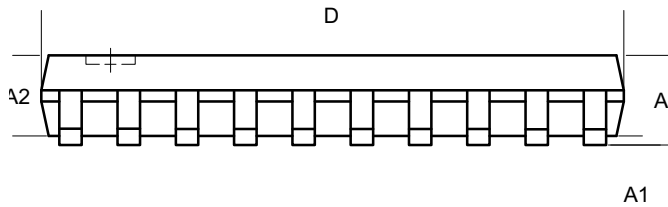
DATE 17 FEB 2016

TSSOP20, 4.4x6.5
CASE 948AQ
ISSUE A

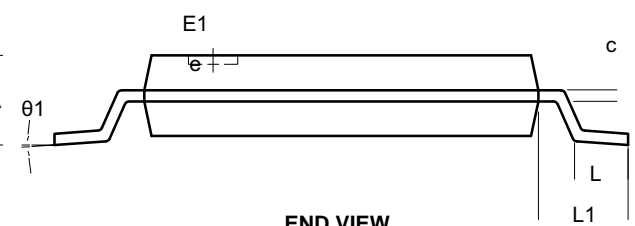
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TOP VIEW



SIDE VIEW



END VIEW

SYMBOL	MIN	NOM	MAX
A			
A1			
A2			
b			
c			
D			
E			
E1			
e			
L1E1A2			
θ			

c

D

E

E1

c

θ 1

L

L1

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