Lo -Vol age 16-Bi BidiNec ional Tran cei er i h 3.6 V Toleran Inp amd O p 74ALVC16245

General Description

The ALVC16245 contains sixteen non–inverting bidirectional buffers with 3–STATE outputs and is intended for bus oriented applications. The device is byte controlled. Each byte has separate 3–STATE control inputs which can be shorted together for full 16–bit operation. The T/\overline{R} inputs determine the direction of data flow through the device. The \overline{OE} inputs disable both the A and B ports by placing them in a high impedance state.

The 74ALVC16245 is designed for low voltage (1.65 V to 3.6 V) V_{CC} applications with I/O capability up to 3.6 V.

The 74ALVC16245 is fabricated with an advanced CMOS technology to achieve high speed operation while maintaining low CMOS power dissipation.

Features

• 1.65 V

DISCONTINUED



TSSOP48 12.5x6.1 CASE 948BQ

ISSUE O

DATE 30 SEP 2016

A. CONFORMS TO JEDEC REGISTRATION MO-153, VARIATION ED, DATE 4/97.

B. DIMENSIONS ARE IN MILLIMETERS.

D. DIM

