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EMI D O C T R

Pin Configuration

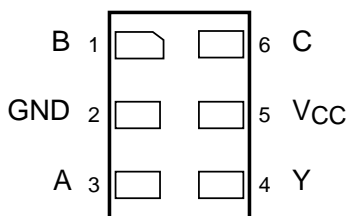


Figure 1. MicroPak™ (Top Through View)

Pin Definitions

| Pin # | Name | Description |
|-------|-----------------|---------------------|
| 1 | B | Data Input |
| 2 | GND | Ground |
| 3 | A | Data Input |
| 4 | Y | Output (Open Drain) |
| 5 | V _{CC} | Supply Voltage |
| 6 | C | Data Input |

Function Table

| Inputs | | | Y=Output |
|--------|---|---|------------------|
| C | B | A | |
| L | L | L | L |
| L | L | H | L |
| L | H | L | H ⁽¹⁾ |
| L | H | H | H ⁽¹⁾ |
| H | L | L | L |
| H | L | H | H ⁽¹⁾ |
| H | H | L | L |
| H | H | H | H ⁽¹⁾ |

H = HIGH Logic Level

L = LOW Logic Level

Note:

1. High impedance output state, open drain.

Function Selection Table

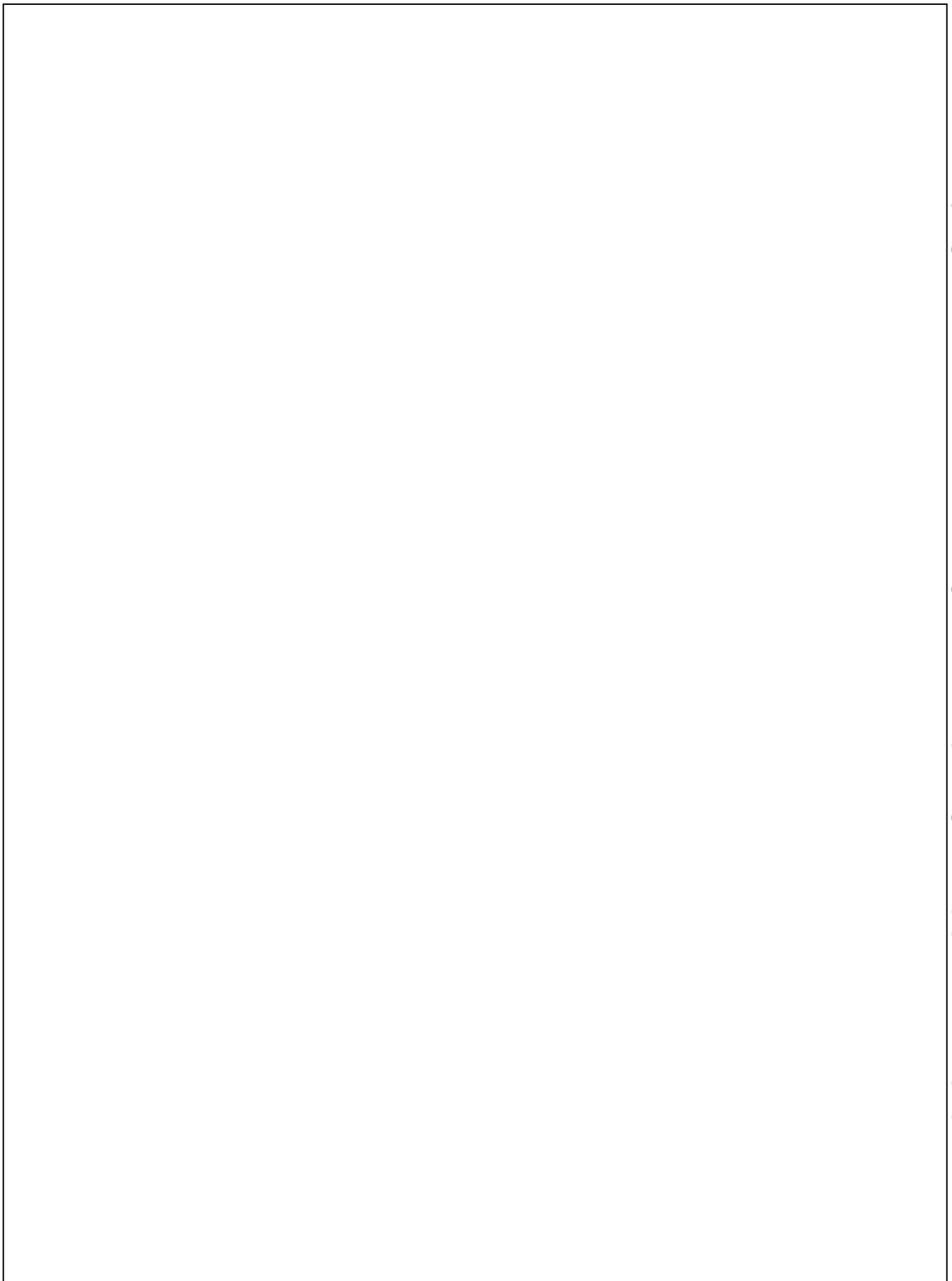
| 2-Input Logic Function | Connection Configuration |
|---|--------------------------|
| 2-to-1 MUX | Figure 2 |
| 2-Input AND Gate | Figure 3 |
| 2-Input OR Gate with One Inverted Input | Figure 4 |
| 2-Input NAND Gate with One Inverted Input | Figure 4 |
| 2-Input AND Gate with One Inverted Input | Figure 5 |
| 2-Input NOR Gate with One Inverted Input | Figure 5 |
| 2-Input OR Gate | Figure 6 |
| Inverted | Figure 7 |
| Buffer | Figure 8 |

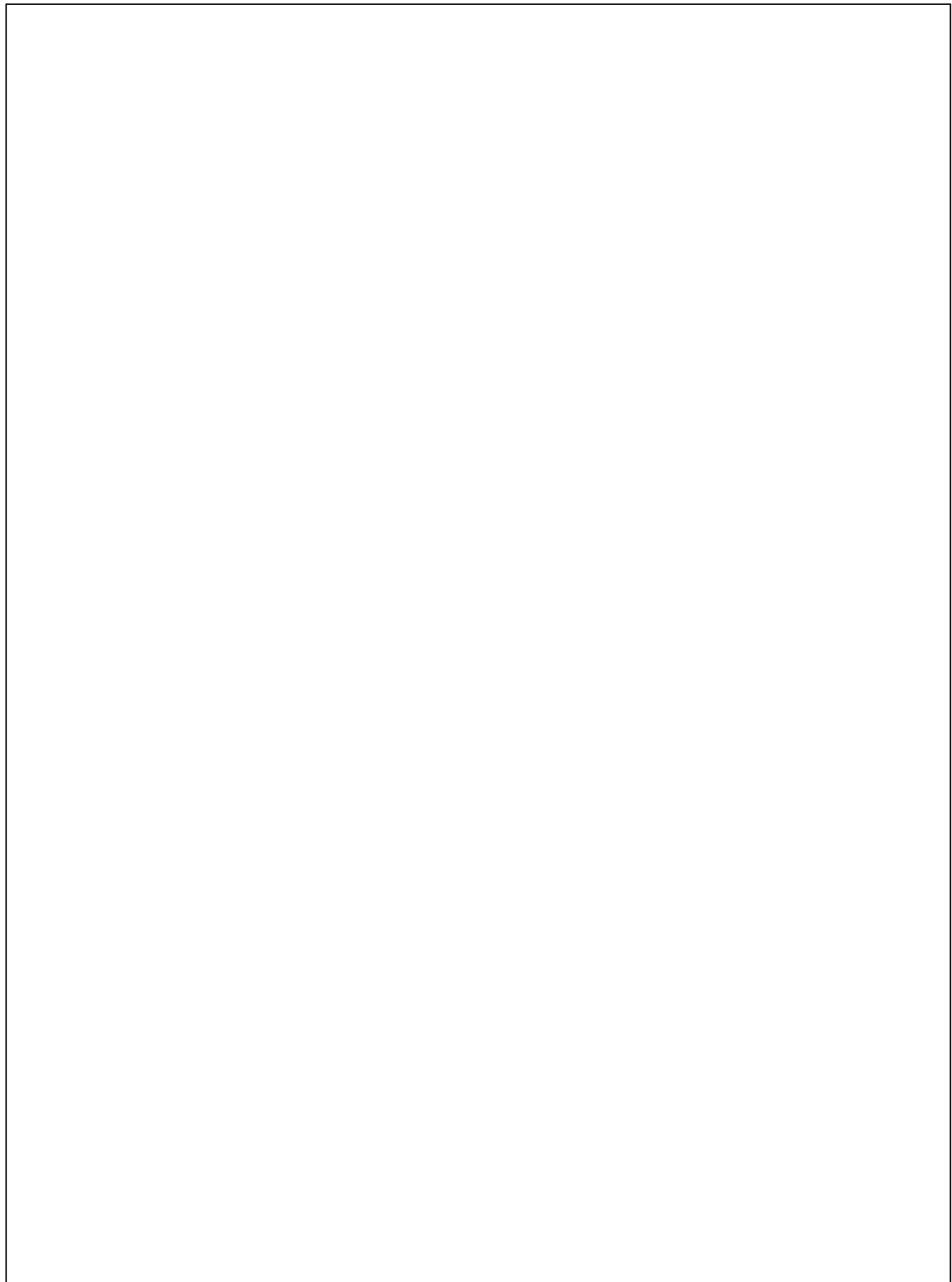
Logic Configurations

Absolute Maximum Ratings

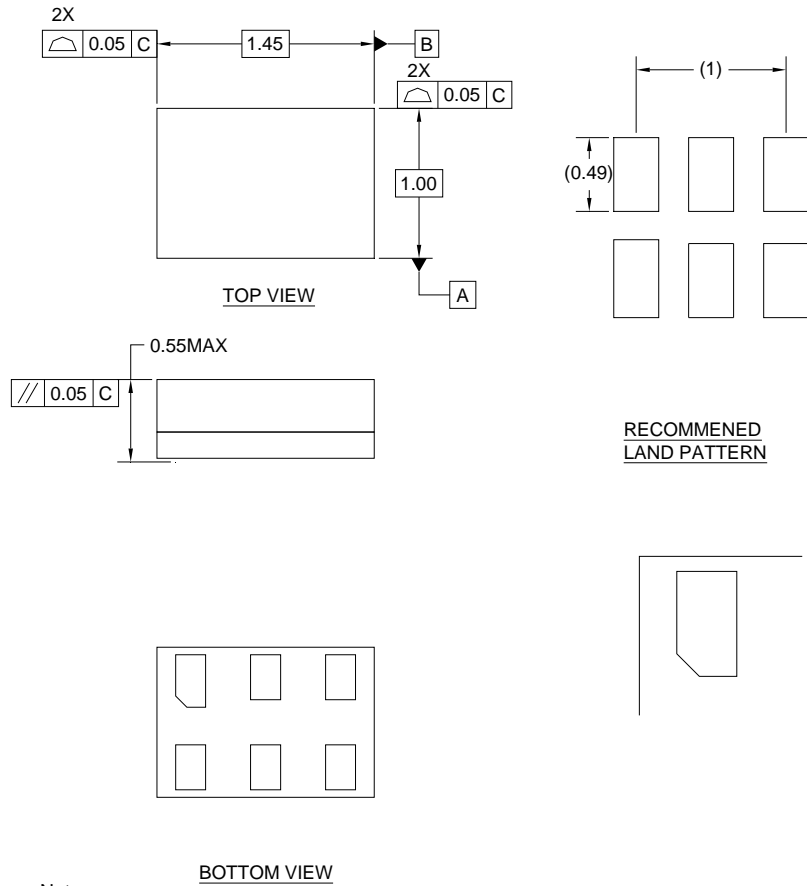
Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

| Symbol | Parameter | Min. | Max. | Unit |
|---------------|----------------|------|------|------|
| V_{CC} V | Supply Voltage | -0.5 | 4.6 | V |





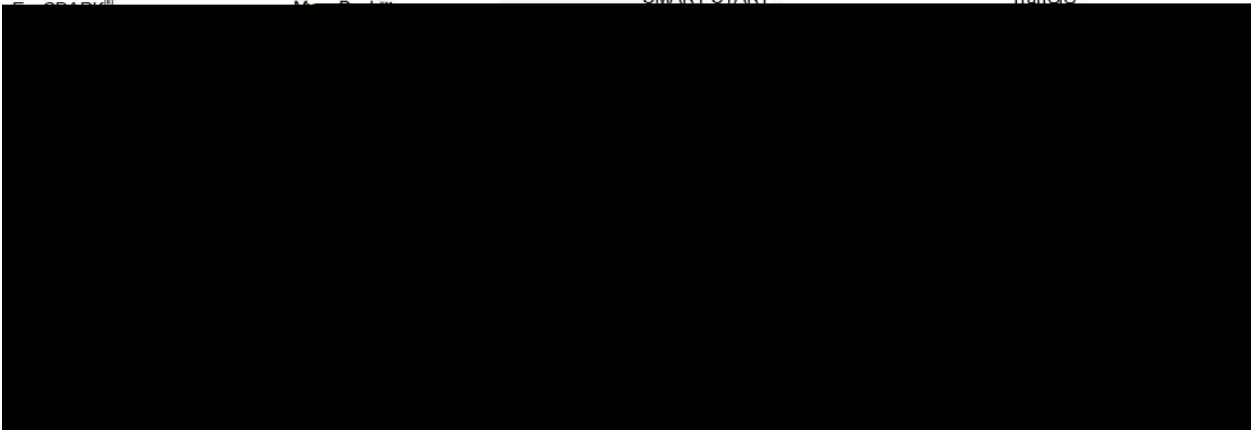
Physical Dimensions




Notes:

1. CONFORMS TO JEDEC STANDARD M0-252 VARIATION UAAD
2. DIMENSIONS ARE IN MILLIMETERS
3. DRAWING CONFORMS TO ASME Y14.5M-1994
4. FILENAME AND REVISION: MAC06AREV4





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