L V g Q 2-I M x

With 5 V Tolerant Inputs

74LCX157

The LCX157 is a high-speed quad 2-input multiplexer. Four bits of data from two sources can be selected using the common Select and Enable inputs. The four outputs present the selected data in the true (non-inverted) form.

The LCX157 can also be used as a function generator.

The 74LCX157 is fabricated with advanced CMOS technology to achieve high speed operation while maintaining CMOS low power dissipation.

Features

- 5 V Tolerant Inputs
- 1.65 V to 5.5 V, V_{CC}

$\overline{E}$ $_{0b}$ I_{1b} I_{0c} I_{1c} I_{0d} I_{1d}

S

Z_a Z_b Z_c Z_d

IEEE/IEC

$\overline{E}$ EN

S G1

I_{0a} $\overline{1}$ MX

I_{1a} 1 Z_a

I_{0b}

I_{1b} Z_b

I_{0d}

I_{1d} Z_d

I_{0c}

I_{1c} Z_c

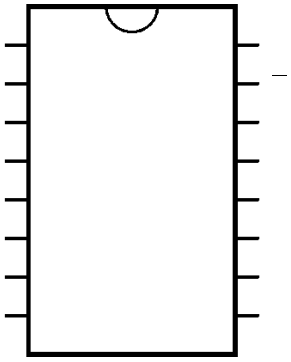

Figure 2. Connection Diagram

PIN DESCRIPTIONS

Pin Names	Description

Functional Description

ABSOLUTE MAXIMUM RATINGS

Symbol

DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Conditions	V_{CC} (V)	T_A = −40°C to +85°C		T_A = −40°C to +125°C		Unit
				Min	Max	Min	Max	
					–		–	
					–		–	
					–		–	
			–		–		–	
				–		–		
				–		–		
				–		–		
			–	–		–		
		– μ – – – –						

AC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Test Condition	V_{CC} (V)	T_A = −40°C to +85°C		T_A = −40°C to +125°C		Unit
				Min	Max	Min	Max	
				–		–		
				–		–		
				–		–		
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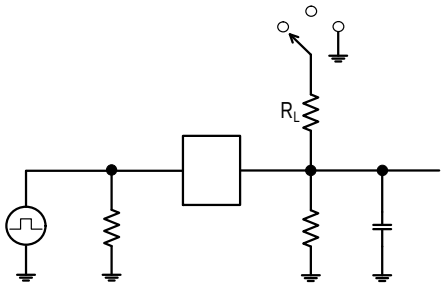
				–		–		
				–	–	–	–	

DYNAMIC SWITCHING CHARACTERISTICS

Symbol	Parameter	Conditions	V_{CC} (V)	T_A = 25°C	Unit
				Typical	
				–	
				–	

CAPACITANCE

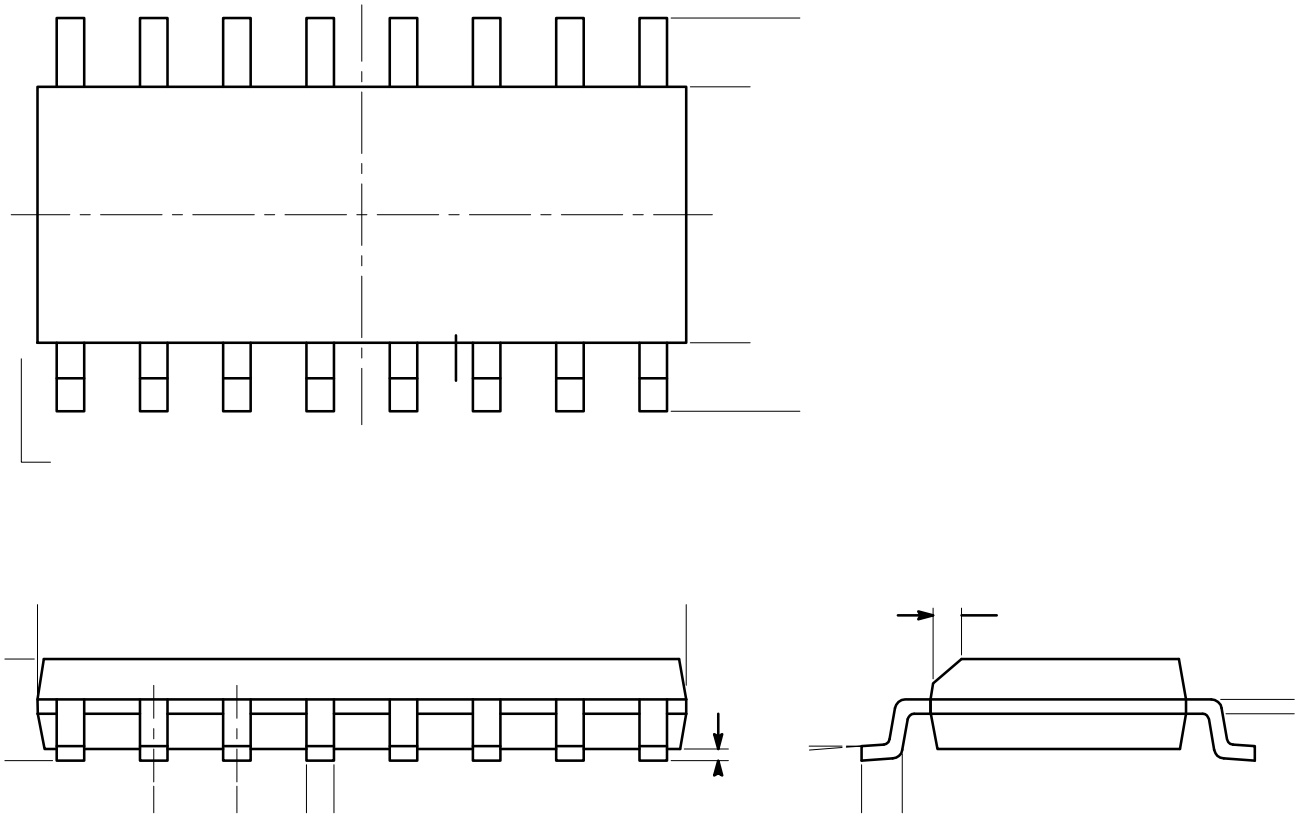
Symbol	Parameter	Condition	Typical	Units


R_L

74LCX157

SOIC−16, 150 mils
CASE 751BG
ISSUE O

DATE 19 DEC 2006



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DESCRIPTION:	**SOIC−16, 150 mils**		**PAGE 1 OF 1**

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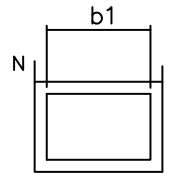
TSSOP 16
CASE 948AH
ISSUE O

DATE 19 SEP 2008

SEE DETAIL "A"

0.19
0.09

4.30



THIS TABLE FOR

SYMBOL	MIN.		
A			
A_1	0.05		
A_2			0.95
b			0.30
b1			0.25
c			0.20
c1			0.16
D			
E1			4.50
e		0.65 BSC	
E		6.40 BSC	
L		0.60	0.70
	SEE VARIATIONS		
		—	8°

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