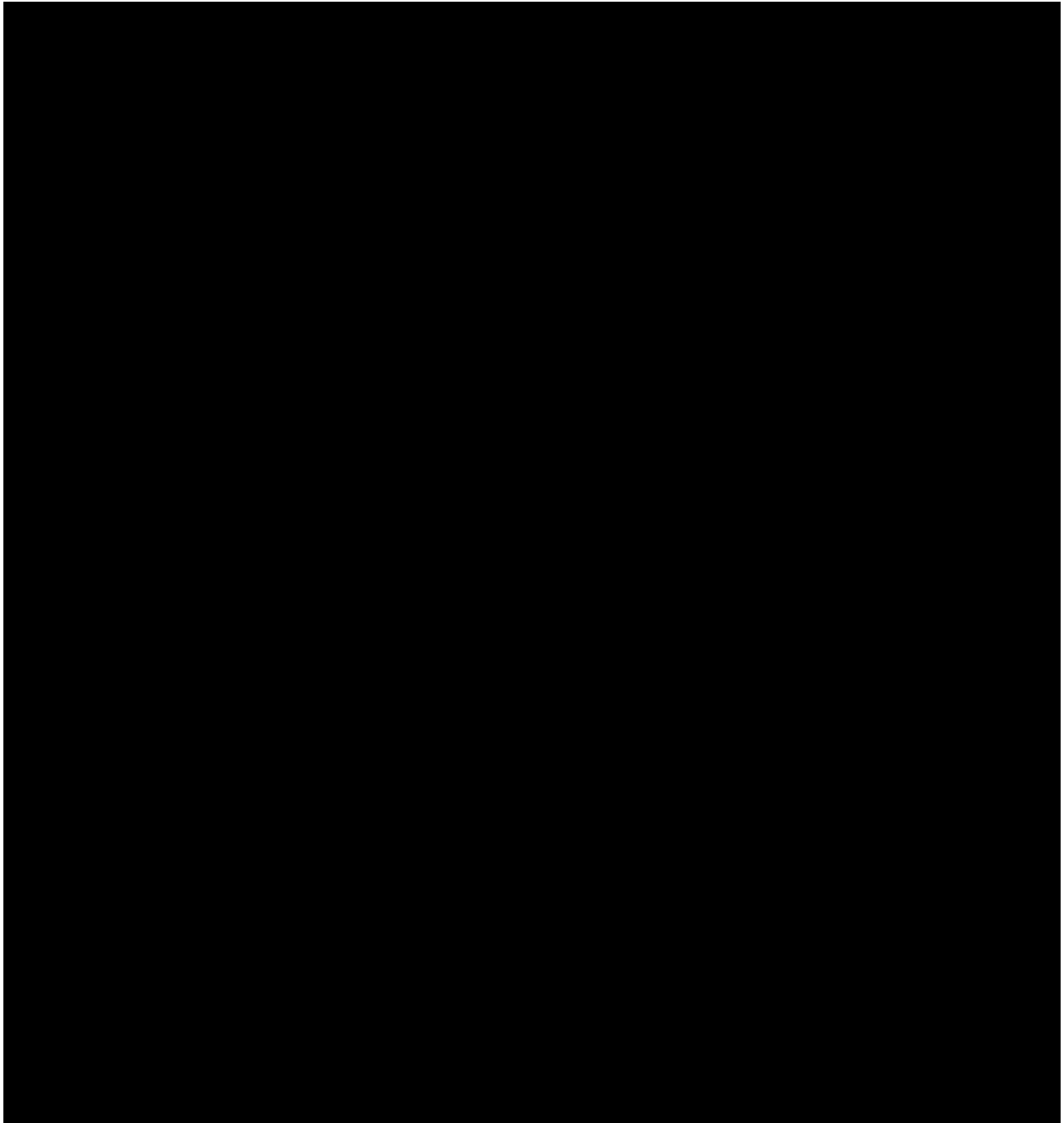


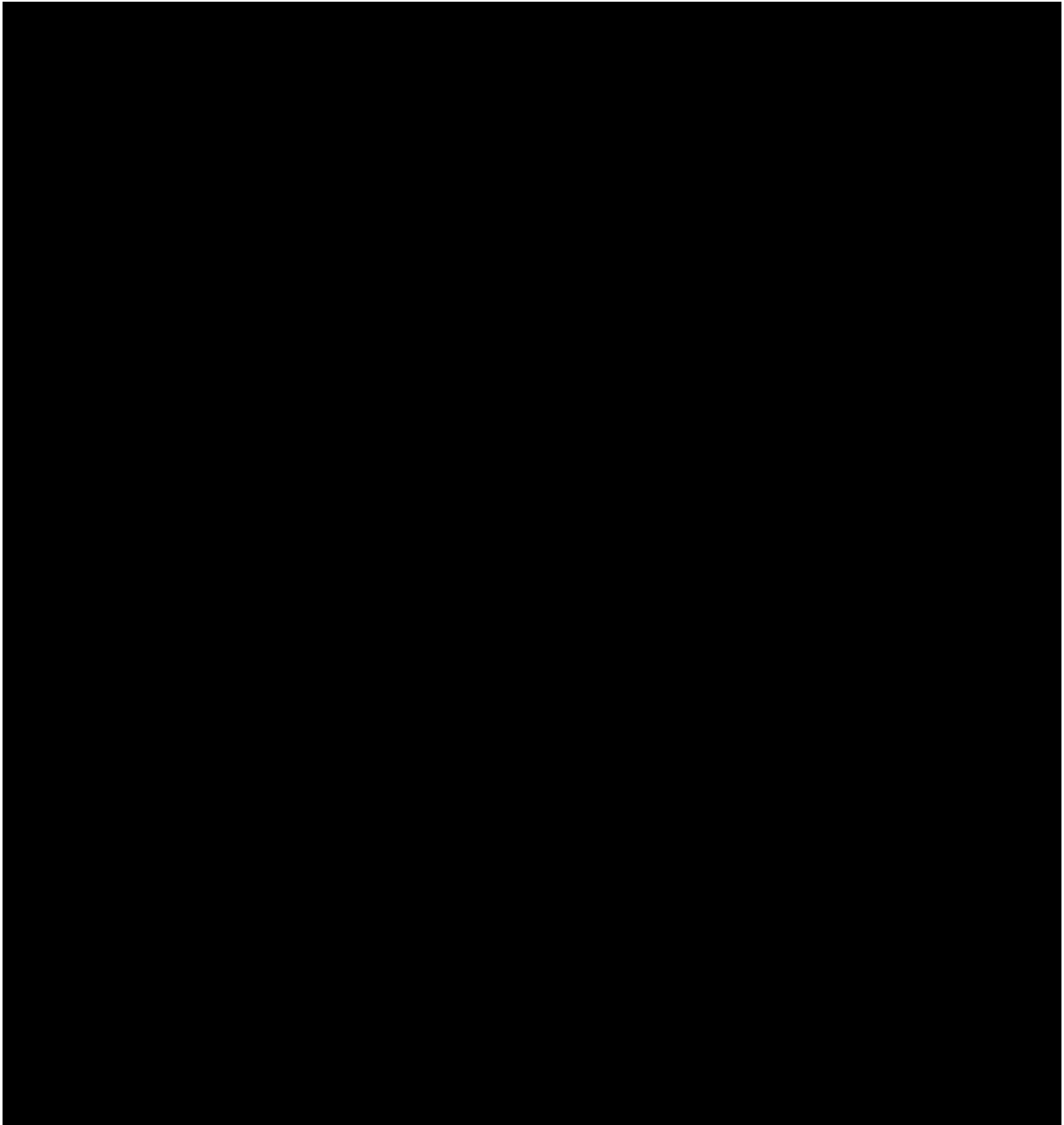
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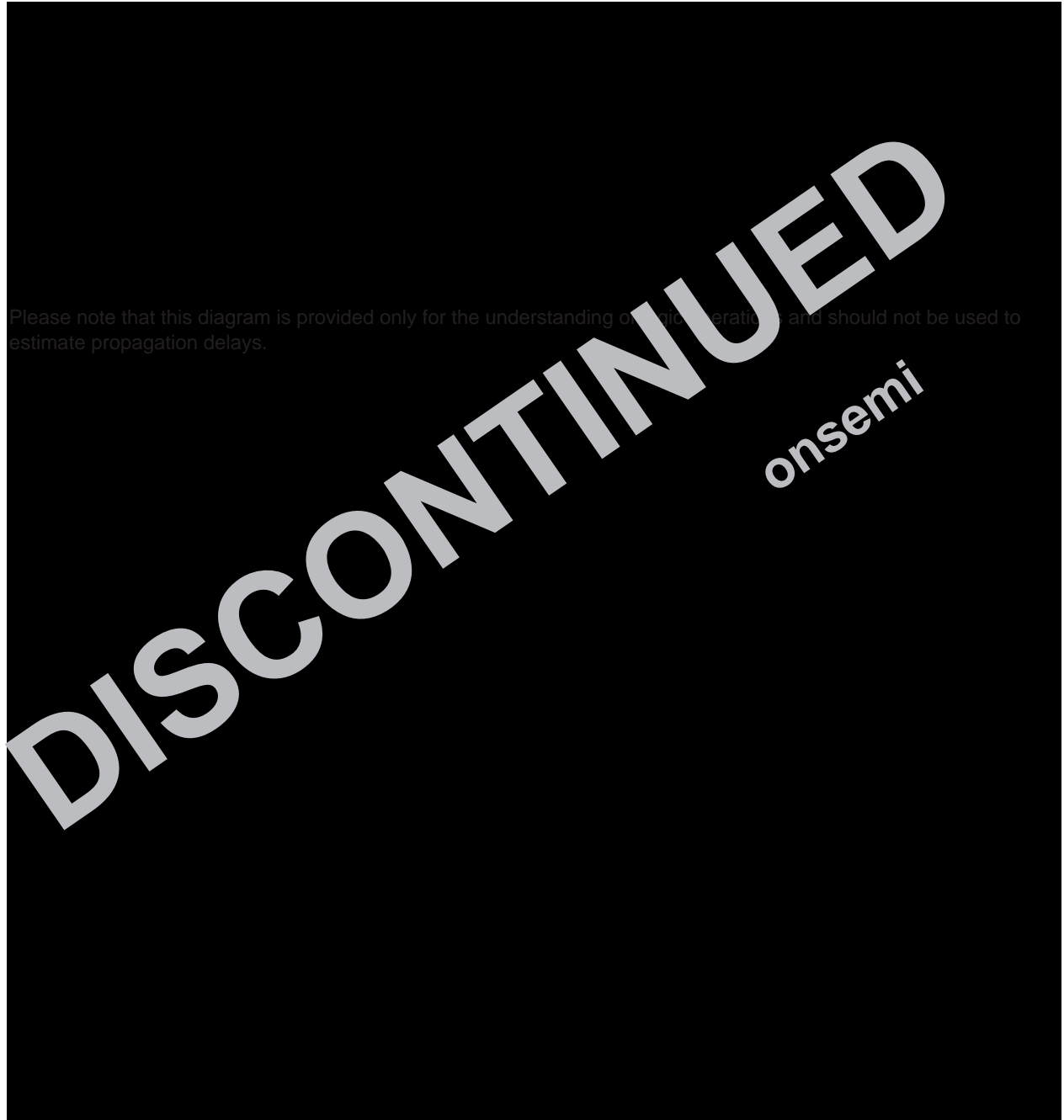
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Logic Diagram





## DC Electrical Characteristics

Symbol	Parameter	$V_{CC}$ (V)	Conditions	$T_A =$
[Redacted Table Content]				

**Notes:**

- 2. All typical values are at  $V_{CC} = 3.3V$ ,  $T_A = 25^{\circ}C$ .
- 3. Applies to bushold versions only (74LVTH373).
- 4. An external driver must source at least the specified current to switch from LOW-to-HIGH.
- 5. An external driver must sink at least the specified current to switch from HIGH-to-LOW.
- 6. This is the increase in supply current for each input that is at the specified voltage level rather than  $V_{CC}$  or GND.

Dynamic Switching Characteristics (7)

Symbol	Parameter	V <sub>CC</sub> (V)	Conditions C <sub>L</sub> = 50pF, R <sub>L</sub> = 500	T <sub>A</sub> = 25°C			Units
				Min.	Typ.	Max.	
V <sub>OLP</sub>	Quiet Output Maximum Dynamic V <sub>OL</sub>	3.3	(8)		0.8		V

V <sub>OLV</sub>	Quiet Output Minimum Dynamic V <sub>OL</sub>	3.3	(8)		0.8		V
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- Notes:  
 7. Characterized in SOIC package. Guaranteed parameter, but not tested.  
 8. Max number of outputs defined as (n). nD1 data inputs are driven 0V to 3V. Output under test held LOW.

AC Electrical Characteristics

Symbol	Parameter	T <sub>A</sub> = 25°C			T <sub>A</sub> = 0°C to +75°C		Units
		Min.	Typ.	Max.	Min.	Max.	
t <sub>PHL</sub>	Propagation Delay, D <sub>n</sub> to O <sub>n</sub>	1.5		4.5	1.5	5.0	ns
t <sub>PLH</sub>	Propagation Delay, LE to O <sub>n</sub>	1.7		4.5	1.7	5.0	ns
t <sub>PZH</sub>	Output Enable Time	1.3		4.8	1.3	5.5	ns
t <sub>PLZ</sub>	Output Disable Time	1.9		4.6	1.9	4.9	ns
t <sub>PHZ</sub>	Output Disable Time	1.9		4.6	1.9	4.9	ns
t <sub>W</sub>	LE Pulse Width	3.0			3.0		ns
t <sub>S</sub>	Setup Time, D <sub>n</sub> to LE	1.1			1.0		ns
t <sub>H</sub>	Hold Time, D <sub>n</sub> to LE	1.4			1.4		ns

- Note:  
 9. All typical values are at V<sub>CC</sub> = 3.3V, T<sub>A</sub> = 25°C.

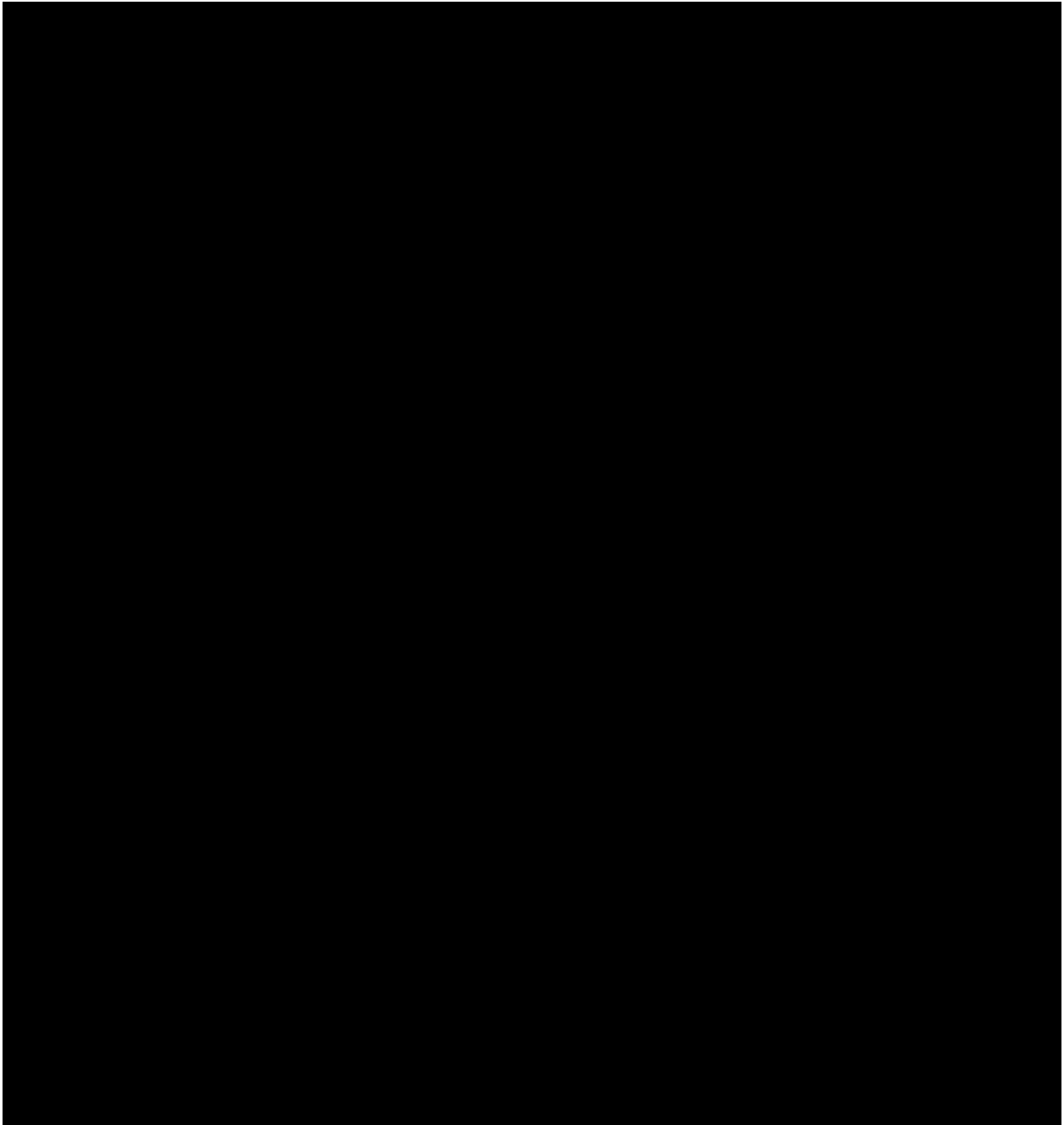
Capacitance (10)

Symbol	Parameter	Conditions	Typical	Units
C <sub>IN</sub>	t <sub>nTJ</sub> - 0 10 1_2 1 Td (1.1)T7Td 4.71 (1.3)- -16.086 -1.6 Td [(Min.)-200 8 433.9OPEN, 0 Td [( 0 0 1 9 0 0 9 444.352323			

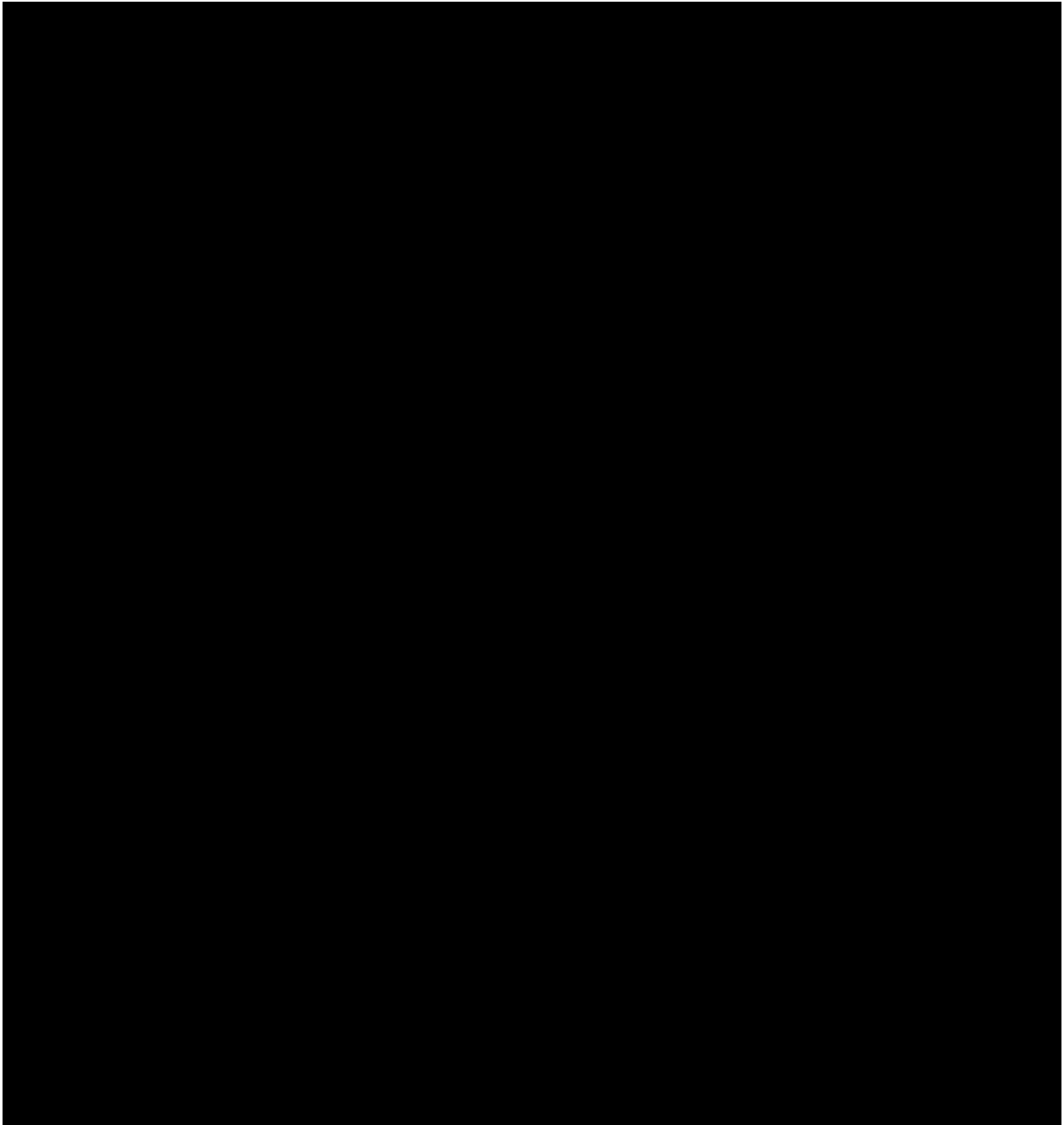
- Note:  
 10. Capacitance is measured at frequency f = 1MHz, per MIL-STD-883, Method 3012.

DISCONTINUED

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Physical Dimensions (Continued)



Figure 3. 20-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide

Pac

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