

74LVX138

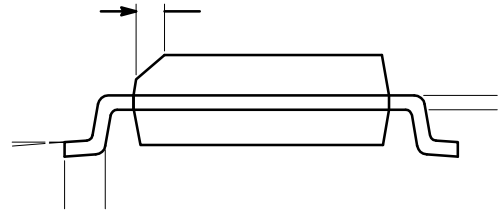
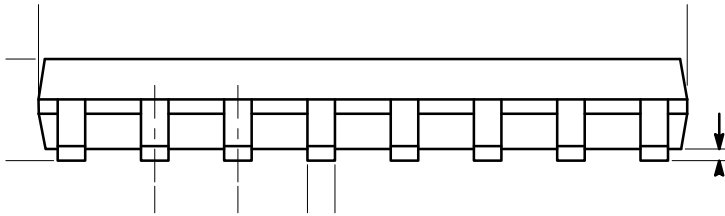
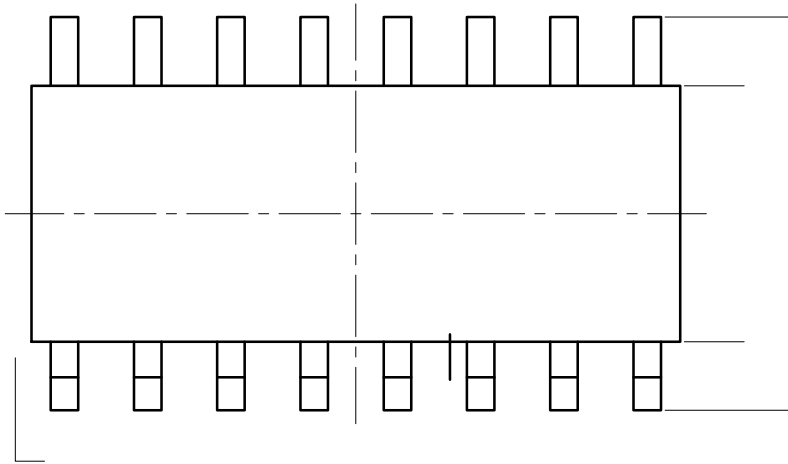
Functional Description

The LVX138 high-speed 1-of-8 decoder/demultiplexer accepts three binary weighted inputs (A_0, A_1, A_2) and, when enabled, provides eight mutually exclusive active-LOW outputs ($\overline{O}_0-\overline{O}_7$). The L

74LVX138

SOIC-16, 150 mils
CASE 751BG
ISSUE O

DATE 19 DEC 2008



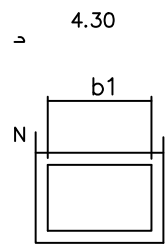
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DESCRIPTION:	SOIC-16, 150 mils	PAGE 1 OF 1

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TSSOP 16
CASE 948AH
ISSUE O

DATE 19 SEP 2008

SEE DETAIL "A" 0.19
0.09



THIS TABLE FOR

S Y M B O L	MIN.		
A			
A ₁	0.05		
A ₂		J	0.95
b			0.30
b ₁			0.25
c			0.20
c ₁			0.16
D			
E ₁			4.50
C		0.65 BSC	
E		6.40 BSC	
L		0.60	0.70

SEE VARIATIONS
| ——— | 8°

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