With 3.6 V ïTolerant Inputs and Outputs (3 ïState, Non ïInverting)

The 74VCX16373 is an advanced performance, ïnon inverting 16 ïbit transparent latch. It is designed for very high ïspeed, very low ïpower operation in 1.8 V, 2.5 V or 3.3 V systems. The VCX16373 is byte controlled, with each byte functioning identically, but independently. Each byte has separate Output Enable and Latch Enable inputs. These control pins can be tied together for full ïbit 16 operation.

When operating at 2.5 V (or 1.8 V) the part is designed to tolerate voltages it may encounter on either inputs or outputs when interfacing to 3.3 V busses. It is guaranteed to be overvoltage tolerant to 3.6 V.

The 74VCX16373 contains 16 ïD type latches with 3 ïstate 3.6 V ïtolerant outputs. When the Latch Enable (LEn) inputs are HIGH, data on the Dn inputs enters the latches. In this condition, the latches are transparent, (a latch output will change state each time its D input changes). When LE is LOW, the latch stores the information that was present on the D inputs a setup time preceding the HIGH ïto ïLOW transition of LE. The 3 ïstate outputs are controlled by the Output Enable ($\overline{OE}$) inputs. When $\overline{OE}$ is LOW, the outputs are enabled. When $\overline{OE}$ is HIGH, the standard outputs are in the high impedance state, but this does not interfere with new data entering into the latches.

Features

- Designed for Low Voltage Operation, V_{CC} = Tc en interfacing V_{CC}