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### Dual 2-to-4 Decoder/Demultiplexer

## 74VHC139

**General Description** 

The VHC139 is an advanced high speed CMOS Dual 2-to-4 Decoder/Demultiplexer fabricated with silicon gate CMOS technology. It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

The active LOW enable input can be used for gating or it can be used as a data input for demultiplexing applications. When the enable input is held HIGH, all four outputs are fixed at a HIGH logic level

#### 74VHC139

#### ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Ratings	Unit
V <sub>CC</sub>			





SOIC-16, 150 mils CASE 751BG ISSUE O

DATE 19 DEC 2008





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<sup>Ү</sup> мв			
°L	MIN.		
A A₁ A₂ b b1 c c1	0.05	د ا	0.95 0.30 0.25 0.20 0.16
E1 C		 0.65 BSC	4.50
E L		6.40 BSC   0.60	0.70
	SEE	VARIATION	s 8'

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