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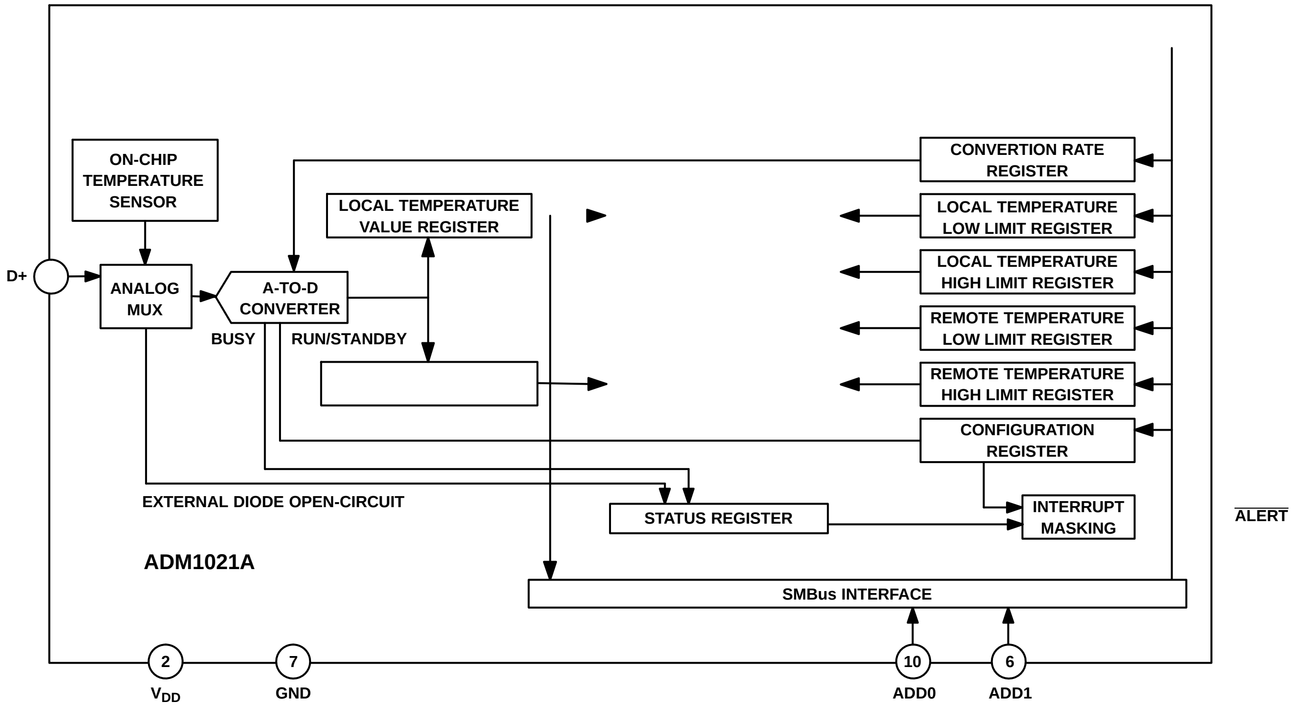

Figure 1. Functional Block Diagram

Table 4. ELECTRICAL CHARACTERISTICS

Parameter	Test Conditions/Comments	Min	Typ	Max	Unit

Functional Description

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Δ

Differences Between the ADM1021 and the ADM1021A

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Table 8. CONFIGURATION REGISTER BIT ASSIGNMENTS

Bit	Name	Function	Power-on Default

Table 11. DEVICE ADDRESSES

ADD0	ADD1	Device Address

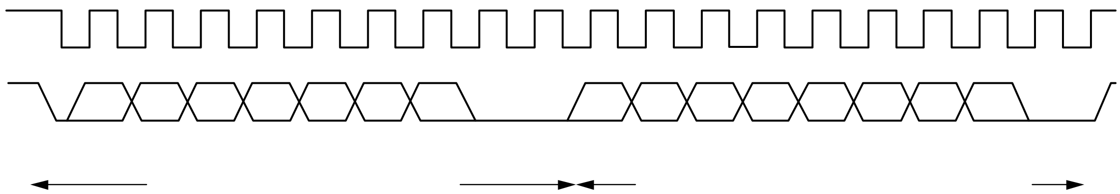

Figure 15. Writing to the Address Pointer Register Only

Layout Considerations

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Figure 18. Arrangement of Signal Tracks

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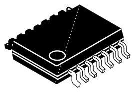
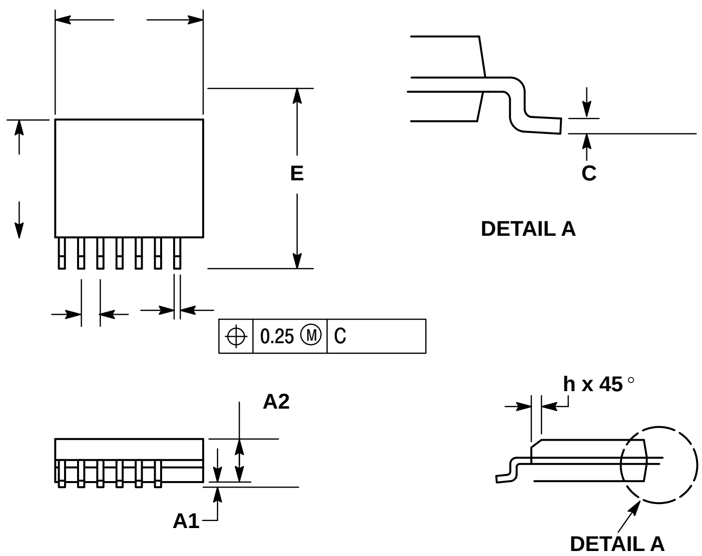


SCALE 2:1

QSOP16
CASE 492−01
ISSUE A

DATE 23 MAR 2011



NOTES:

	INCHES		
DIM	MIN	MA	
A	0.053	0.069	
A1	0.004	0.010	
	0.008	0.012	
	0.007	0.010	
	0.025 BSC		
	0.009	0.020	
L	0.016	0.050	
M	0	8	