



Dual Bootstrapped, 12 V MOSFET Driver with Output Disable

ADP3418

FEATURES

- All-in-one synchronous buck driver
- Bootstrapped high-side drive
- 1 PWM signal generates both drives
- Anticross-conduction protection circuitry



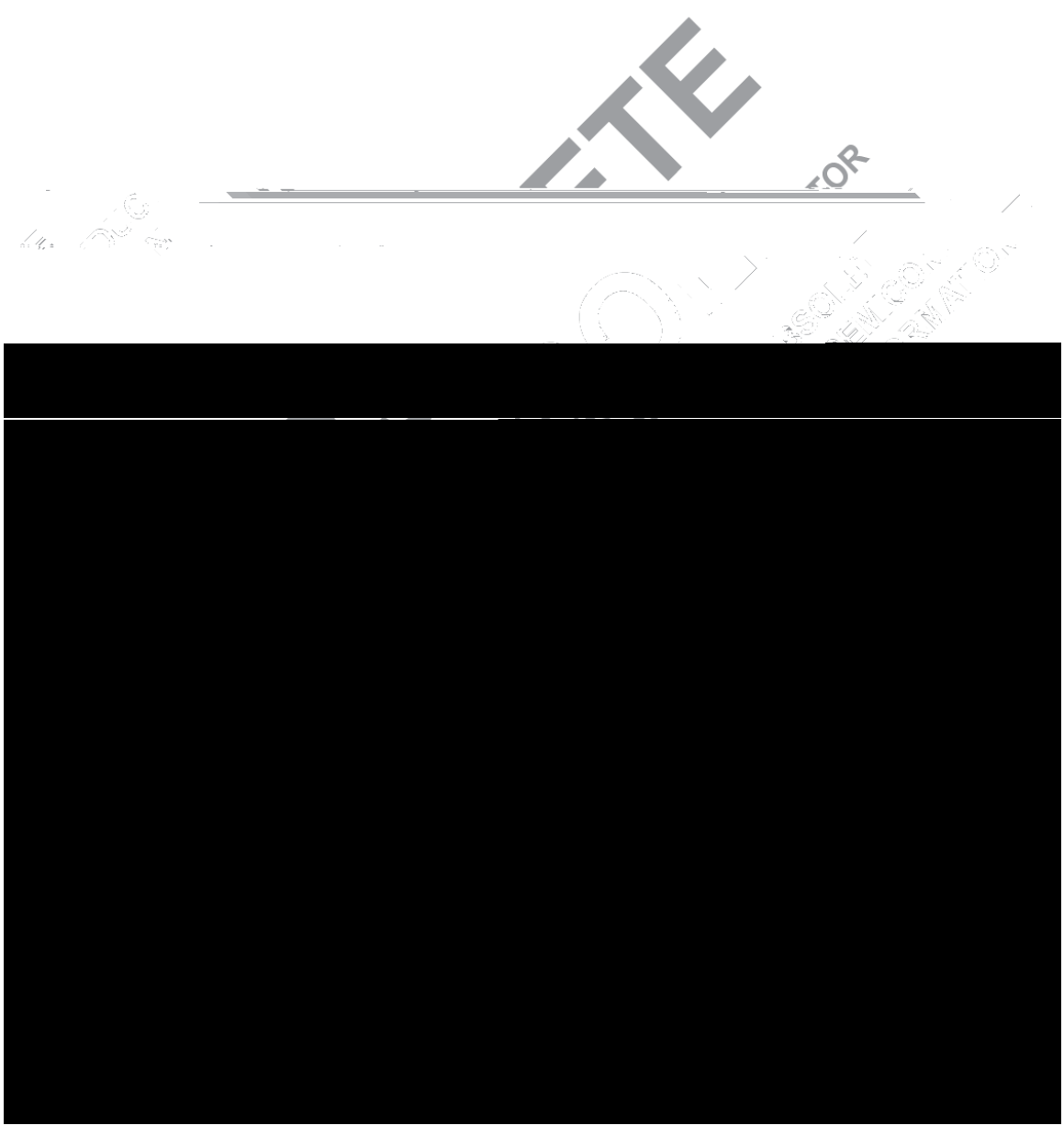
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SPECIFICATIONS¹

$V_{CC} = 12\text{ V}$, $BST = 4\text{ V to }26\text{ V}$, $T_A = 0^\circ\text{C to }85^\circ\text{C}$, unless otherwise noted.

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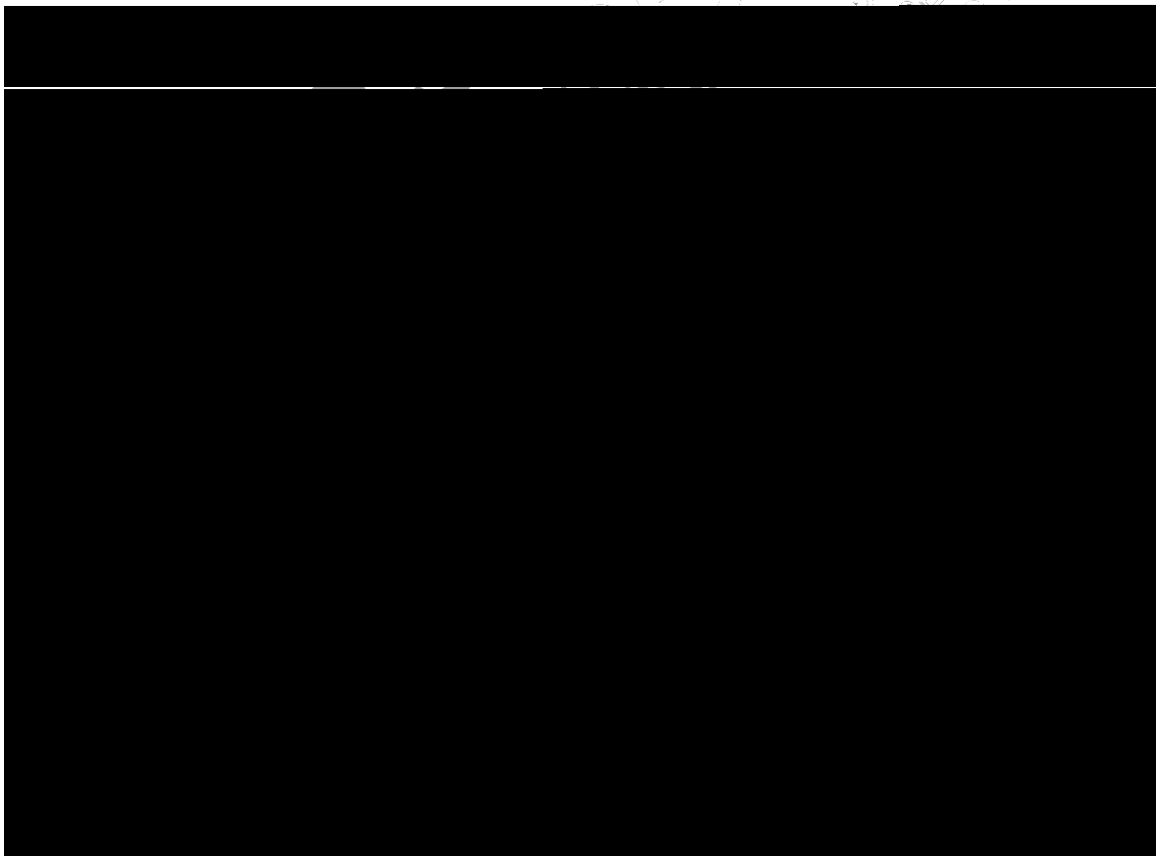
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
SUPPLY						
Supply Voltage Range	V_{CC}		4.15		13.2	V
Supply Current	I_{SYS}	$BST = 12\text{ V}$, $IN = 0\text{ V}$		3	6	mA
OD INPUT						
Input Voltage High			2.6			V
Input Voltage Low					0.8	V



PIN CONFIGURATION AND FUNCTION DESCRIPTIONS

BST	1	8	DRVH
IN	2	7	SW
	3	6	PGND
VCC	4	5	DRVL

03228-B-002



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TYPICAL PERFORMANCE CHARACTERISTICS

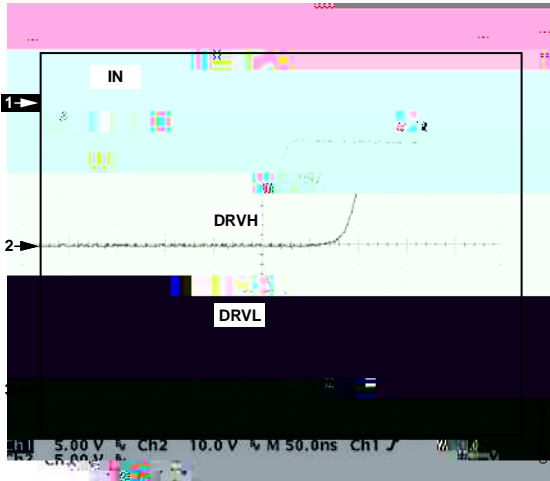


Figure 5. DRVH Rise and DRVL Fall Times

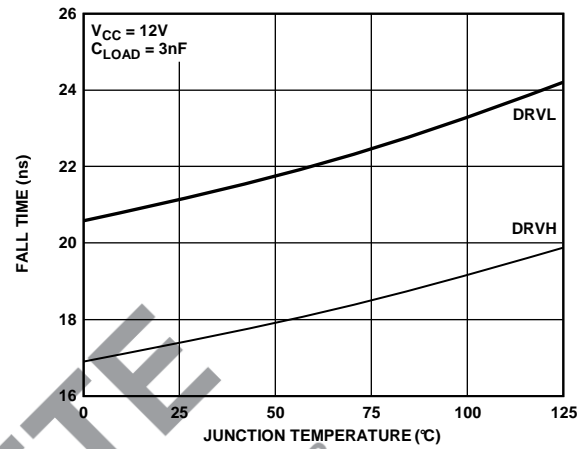


Figure 8. DRVH and DRVL Fall Times vs. Junction Temperature

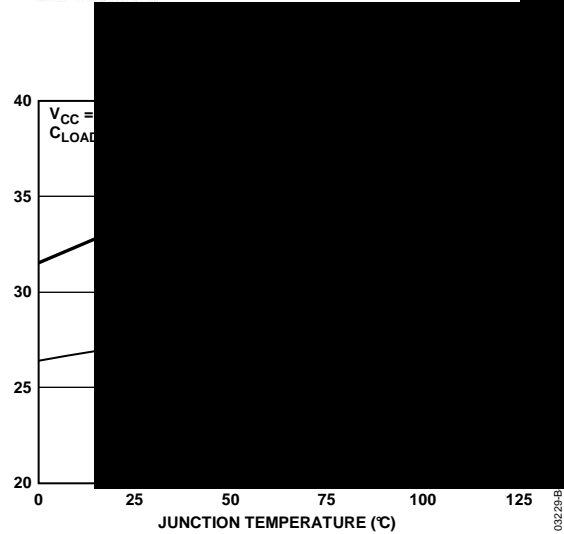
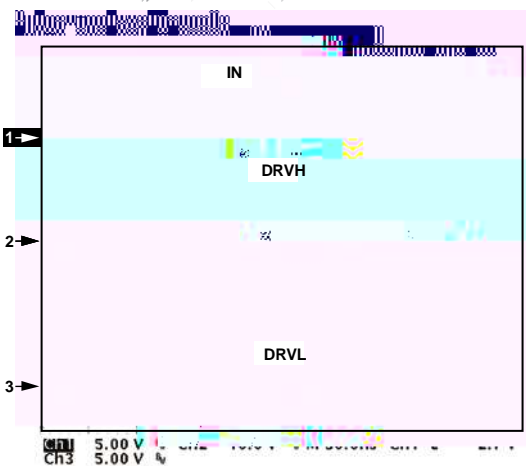


Figure 7. DRVH and DRVL Rise Times vs. Junction Temperature

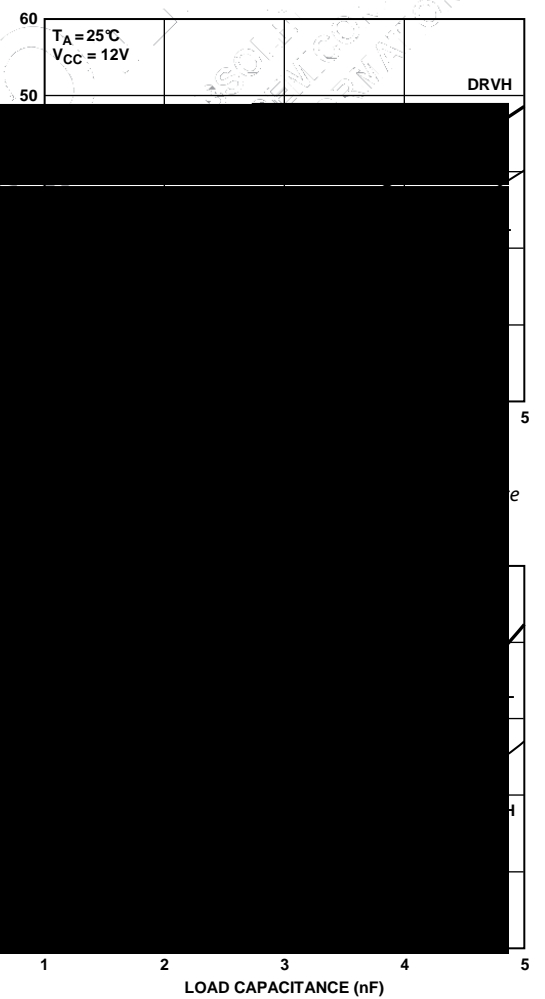


Figure 10. DRVH and DRVL Fall Times vs. Load Capacitance

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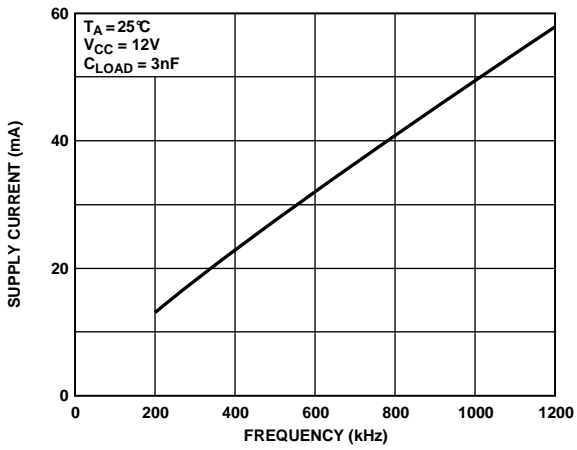
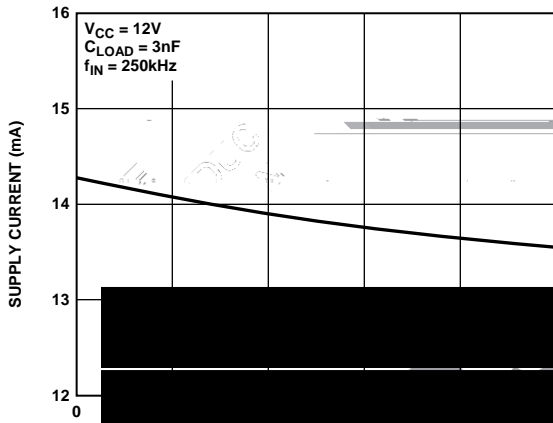
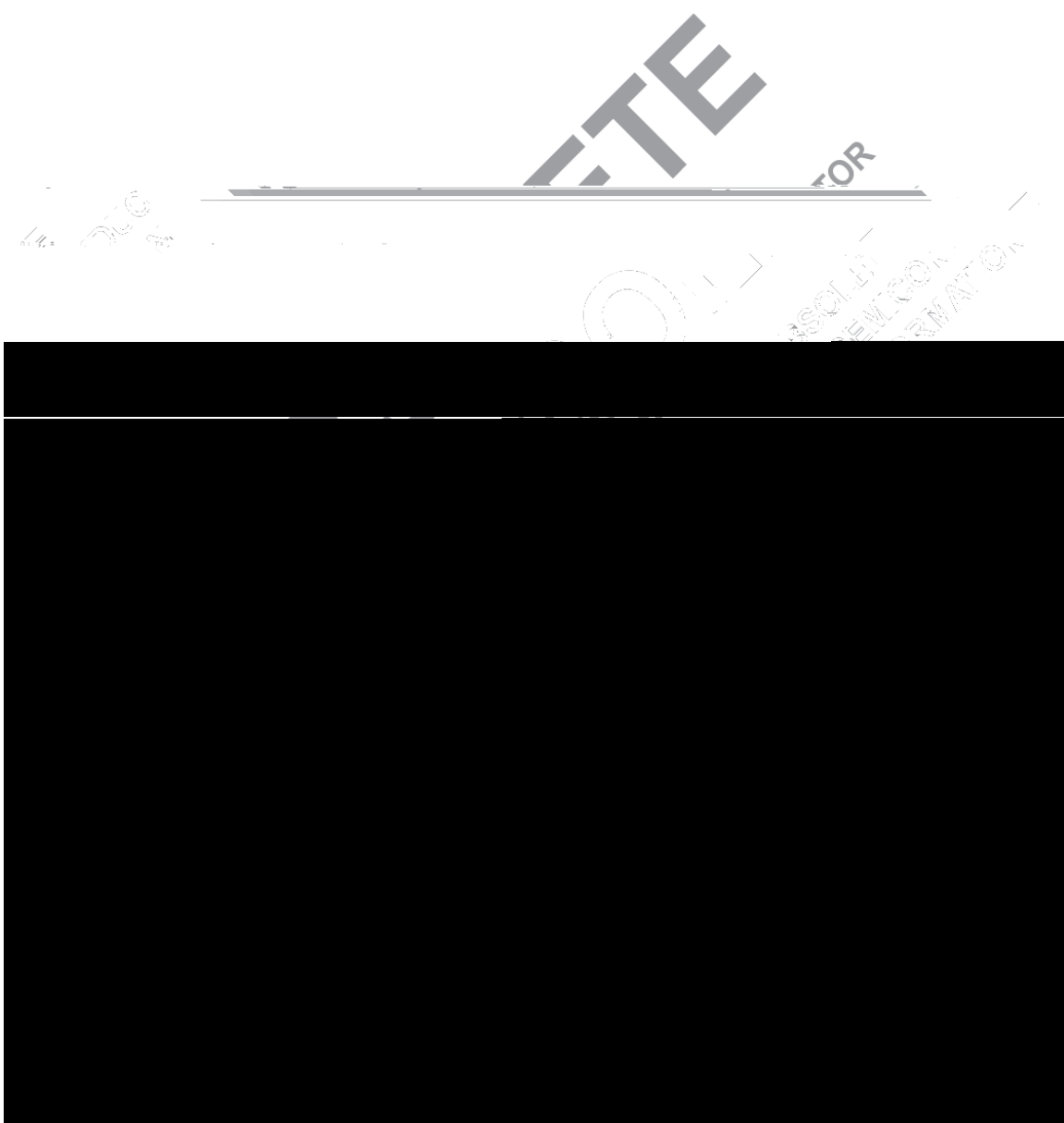


Figure 11. Supply Current vs. Frequency



THEORY OF OPERATION

The ADP3418 is a dual MOSFET driver optimized for driving two N-channel MOSFETs in a synchronous buck converter

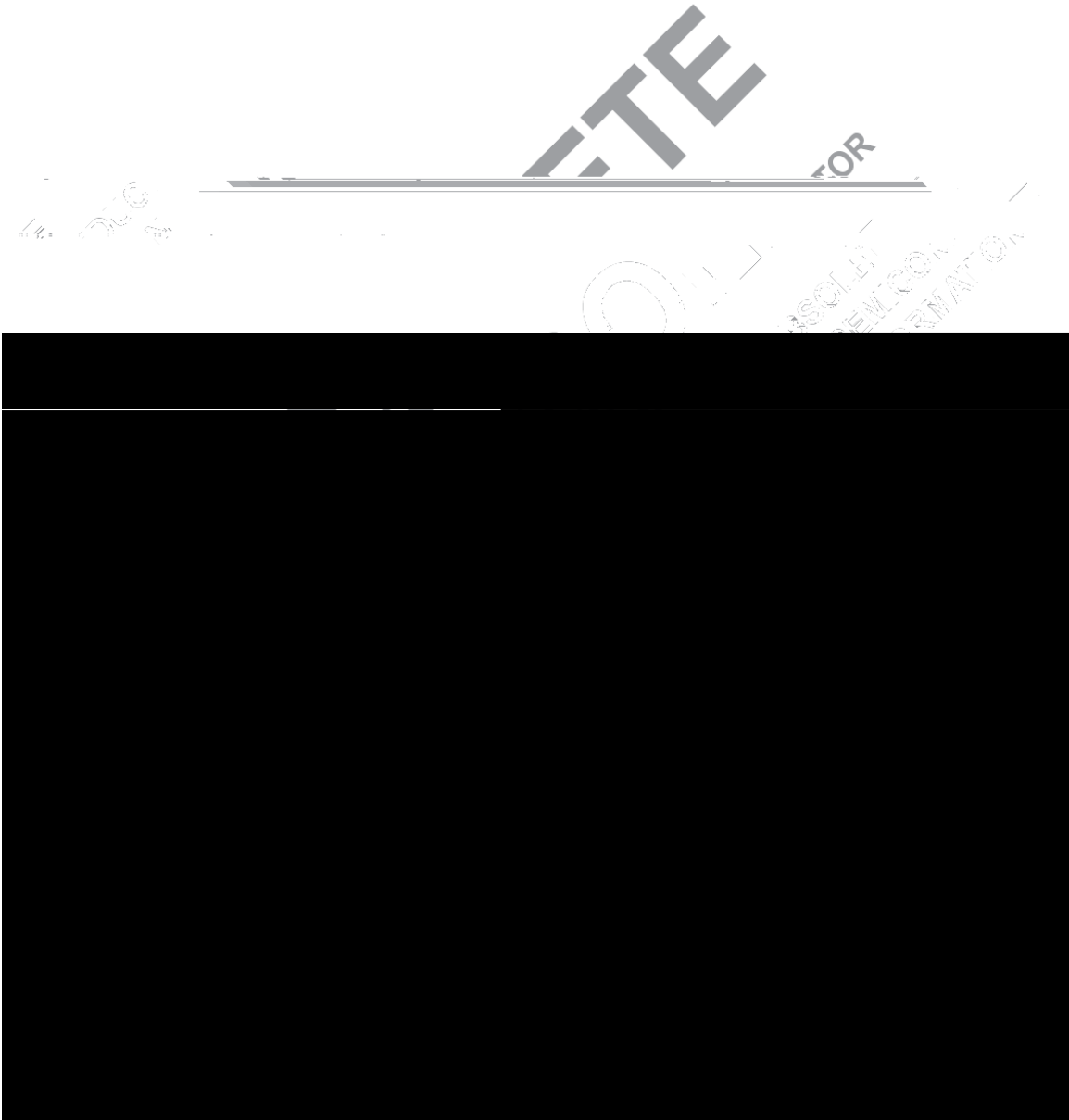


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APPLICATION INFORMATION

SUPPLY CAPACITOR SELECTION

For the supply input (V_{CC}) of the ADP3418, a local bypass capacitor is recommended to reduce the noise and to supply some of the peak currents drawn, such as a 4.7 μF ,



The MOSFET vendor should provide a maximum voltage slew rate at the drain current rating such that this can be designed around. Once this specification is had, the next step is to determine the maximum current expected to be seen in the MOSFET. This can be done by

$$I_{AX} = I_{DC} (p_{\text{high}} + p_{\text{low}}) + (C_{\text{CC}} -) \times \frac{D_{AX}}{f_{\text{AX}}} \quad (5)$$

where:

I_{AX} is determined for the VR controller being used with the driver. Note that this current is divided roughly equally between MOSFETs if more than one is used (assume a worst-case mismatch of 30% for design margin).

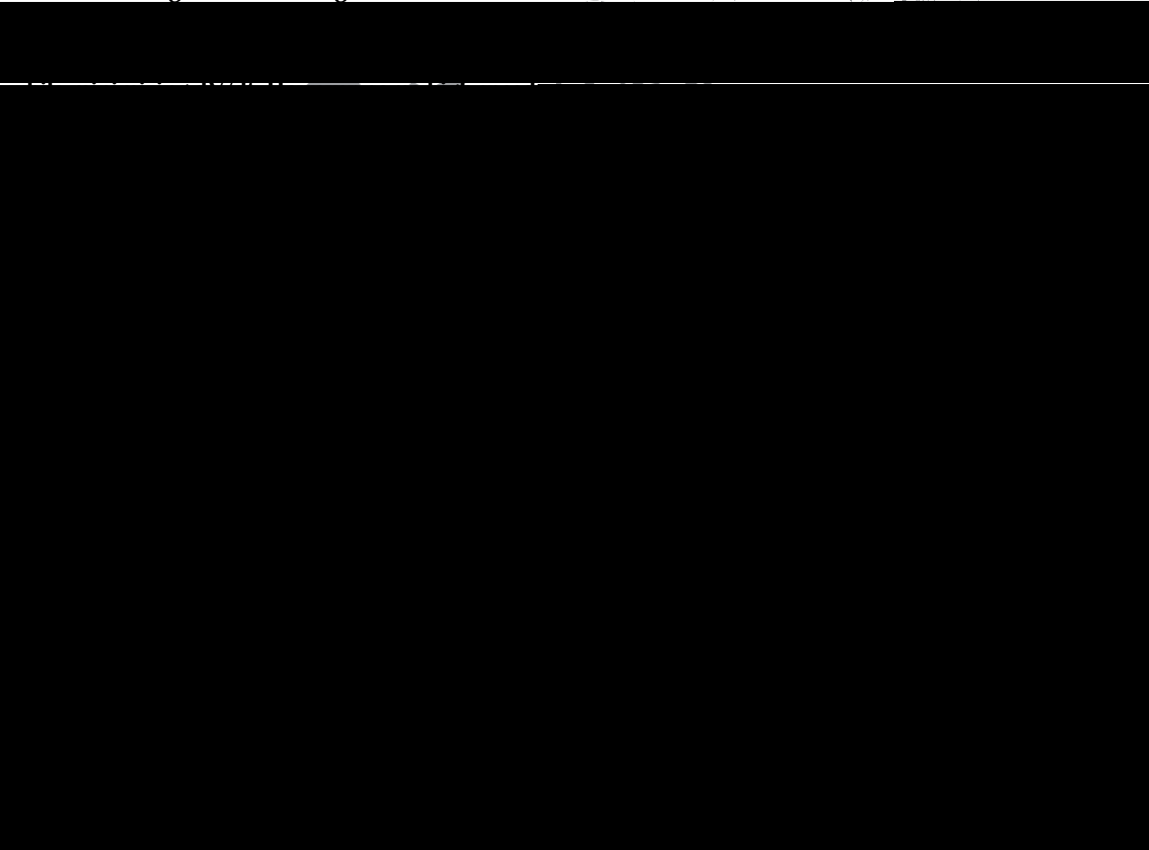
L is the output inductor value.

When producing the design, there is no exact method for calculating the dV/dt due to the parasitic effects in the external MOSFETs as well as the PCB. However, it can be measured to determine if it is safe. If it appears the dV/dt is too fast, an optional gate resistor can be added between DRVH and the high-side MOSFETs. This resistor slows down the dV/dt , but it also increases the switching losses in the high-side MOSFETs.

The ADP
impedance
efficiently
MOSFET
the current

LOW-SIDE

The low-side
resistance
large input
to make s



ADP3418

OUTLINE DIMENSIONS

