6-Channel LED Driver Tith I²C Interface

CAT3626

Description

The CAT3626 is a high efficiency 1x/1.5x fractional charge pump with programmable dimming current in six LED channels. To ensure uniform brightness in LCD backlight applications, each LED channel delivers an accurate regulated current.

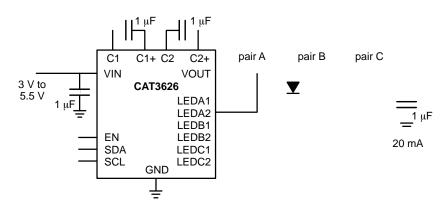
Low noise and input ripple is achieved by operating at a constant switching frequency of 1 MHz which allows the use of small external ceramic capacitors. The 1x/1.5x fractional charge pump supports a wide range of input voltages from 3 V to 5.5 V with efficiency up to 91%, and is ideal for Li–Ion battery powered devices.

The LED channels are configured into three independent pairs, each containing 2 matched channels. Each pair can be separately programmed from zero to 32 mA, in 0.5 mA resolution steps, using the I^2C serial interface. Any individual channel can be disabled while others remain active. When the enable input (EN) is low, the device is in shutdown mode drawing zero current.

The device is available in a 16-pad TQFN package with a max height of 0.8 mm.

Features

- Drives 6 LED Channels
- Independent Current on 3 Pairs of LEDs
- I²



| Table 4. A.C. CHARACTERISTICS For $3 V \le V_{IN} \le 5.5 V$, over full ambient temperature range | 40°C to +125°C |
|-----------------------------------------------------------------------------------------------------------|----------------|
| (over recommended operating conditions unless specified otherwise). | |

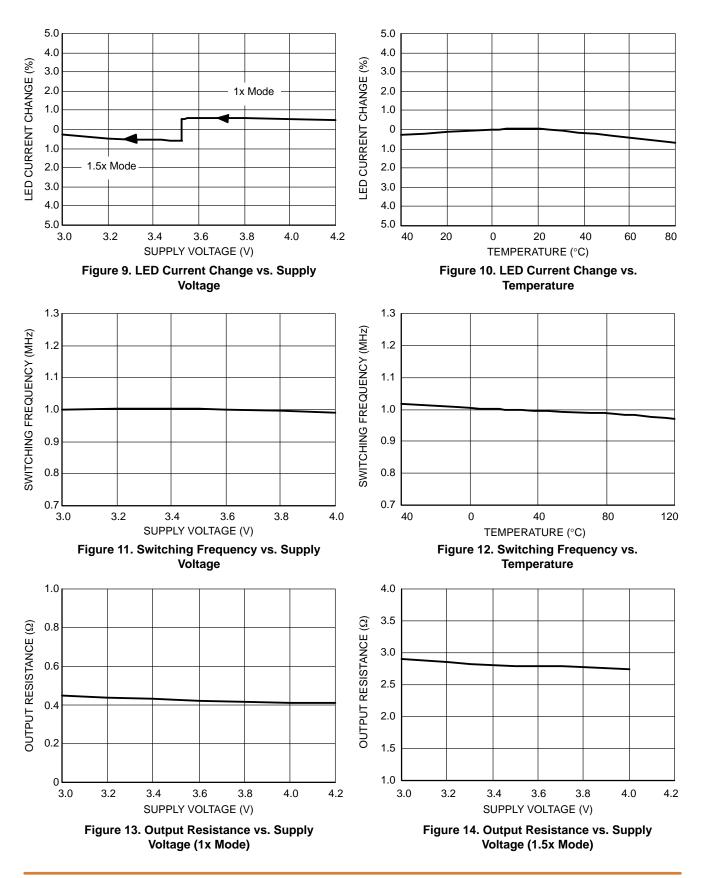
| Symbol | Parameter | Min | Тур | Max | Unit |
|------------------|-----------------|-----|-----|-----|------|
| f _{SCL} | Clock Frequency | | | | |

TYPICAL CHARACTERISTICS

 $(V_{\text{IN}} = 3.6 \text{ V}, \text{I}_{\text{OUT}} = 90 \text{ MA} \text{ (6 LEDS AT 15 MA)}, \text{ EN} = V_{\text{IN}}, \text{ } \text{C}_{\text{IN}} = \text{C}_{1} = \text{C}_{2} = \text{C}_{\text{OUT}} = 1 \text{ MF}, \text{ } \text{T}_{\text{AMB}} = 25^{\circ}\text{C}, \text{ } \text{UNLESS OTHERWISE}$

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(V_{IN} = 3.6 V, I_{OUT} = 90 MA (6 LEDS AT 15 MA), EN = V_{IN}, $C_{IN} = C_1 = C_2 = C_{OUT} = 1$ MF, $T_{AMB} = 25^{\circ}C$, UNLESS OTHERWISE SPECIFIED.)



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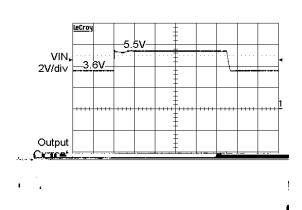


Figure 21. Line Transient Response in 1x Mode

BLOCK DIAGRAM

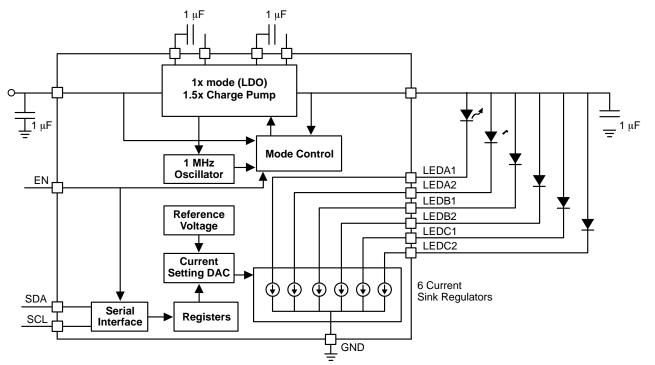


Figure 25. CAT3626 Functional Block Diagram

The Table 7 lists the various LED currents with the associated RegA, RegB, and RegC register values.

| Current | D7 | DC | DE | D 4 | Da | 50 | D4 | 50 |
|---------|----|----|----|------------|----|----|----|----|
| (MA) | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 |

I²C INTERFACE

The LED driver is interfaced through a 2-wire serial I^2C -bus in order to control the state and the current in each of the six LED channels. The SDA and SCL lines comply with the I^2C electrical specification and should be terminated with pull-up resistors. When the bus is not used, both lines are high. The device supports the maximum bus speed of 400 kbit/s. The serial bit sequence is shown below

for read and write operations into the registers. Read and write instructions are initiated by the master controller/CPU and acknowledged by the slave LED driver. The I²C address of the driver is internally fixed to the binary value 1100110. The protocol requires that the start bit and the device address are both repeated. For further details on the I²C protocol, please refer to the I²C–Bus Specification, document number 9398 393 40011, from Philips Semiconductors.

• Read operation:

| [| S | Slave address | W | А | Register address | А | S | Slave address | R | А | Data | A* | Р |
|---|---|---------------|---|---|------------------|---|---|---------------|---|---|------|----|---|
| | | | | | | | | | | | | | |

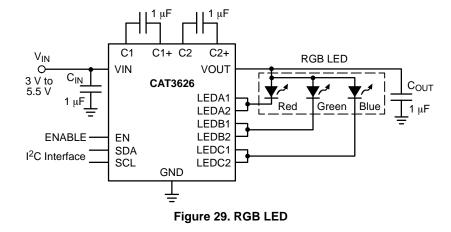
• Write operation:

| S | Slave address | W | А | Register address | А | Data | А | Р |
|---|---------------|---|----|------------------|---|------|---|---|
| | | | S: | Start condition | | | | |

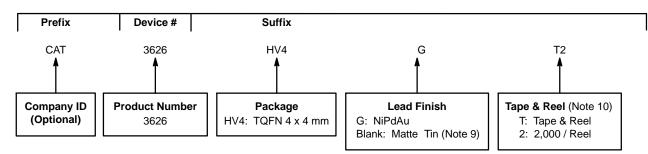
TYPICAL APPLICATION

The CAT3626 is ideal for driving RGB (red green blue) LEDs with common anode configuration. The individual LED currents associated with the red, green and blue LEDs are programmable independently through the I²C interface,

allowing to generate an accurate color mixing. Dimming while maintaining the same color can be done by reprogramming the RegEn register on and off with the appropriate duty cycle (PWM mode).



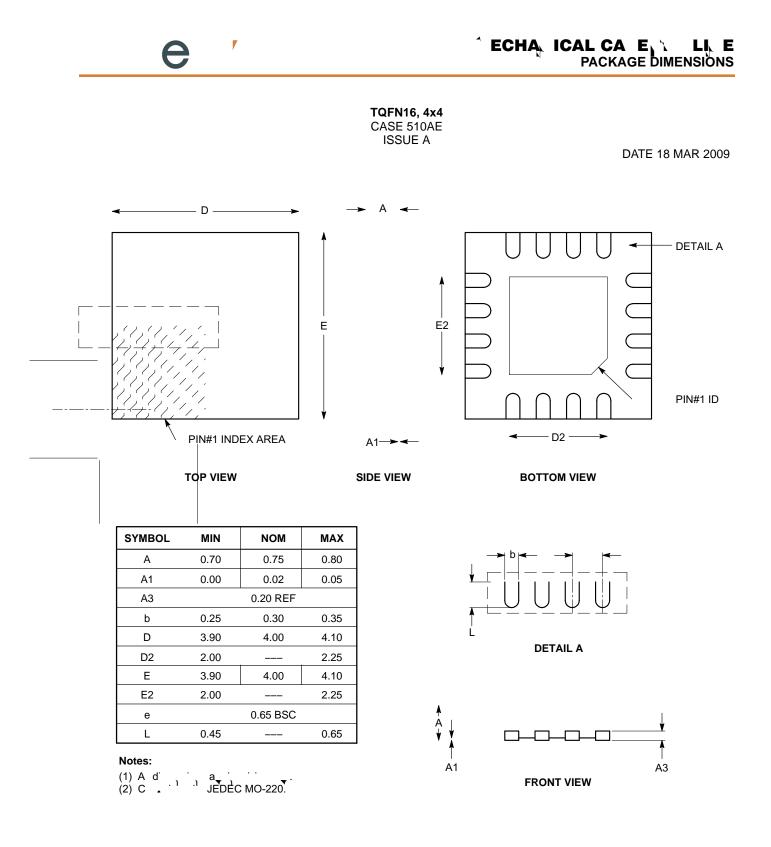
EXAMPLE OF ORDERING INFORMATION (NOTE 8)



6. All packages are RoHS compliant (Lead free, Halogen free).

The standard lea finish is NiPdAu. 7.

- 8. The device used in the above example is a CAT3626HV4 GT2 (TQFN, NiPdAu Plated Finish, Tape & Reel, 2,000/Reel).
- For Matte Tin package option, please contact your nearest onsemi Sales office.
 For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.



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