1220

4 -
profile Chip Scale Package.

Features

- Four and Eight Channels of ESD Protection
- OptiGuard™ Coated for Improved Reliability
- ±

CM1220

CM1220

Table 4. ELECTRICAL OPERATING CHARACTERISTICS (Note 1)

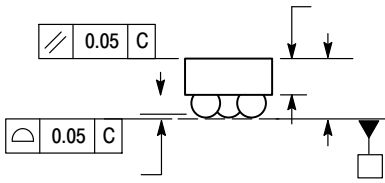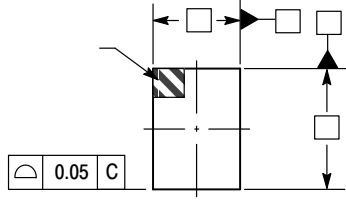| Symbol	Parameter	Conditions	Min	Typ	Max	Units
C_{DIODE}	Diode (Channel) Capacitance	At 2.5 VDC Reverse Bias, 1 MHz, 30 mVAC	11	14	17	pF
V_{DIODE}	Diode Standoff Voltage	I_{DIODE} = 10 μA		6.0		V
I_{LEAK}	Diode Leakage Current	V_{IN} = +3.3 V (reverse bias voltage)		0.1	1	μA
V_{SIG}	Signal Clamp Voltage Positive Clamp Negative Clamp	I_{DIODE} = 10 mA	5.6 −1.5	6.8 −0.8	9.0 −0.4	V
V_{ESD}	In−system ESD Withstand Voltage a) Human Body Model, MIL−STD−883, Method 3015 b) Contact Discharge per IEC 61000−4−2	(Note 2)	±30 ±15			

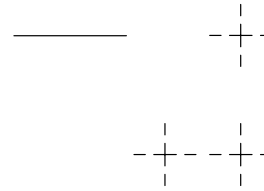## APPLICATION INFORMATION

WLCSP5, 0.96x1.33
CASE 567AY
ISSUE O

DATE 26 JUL 2010
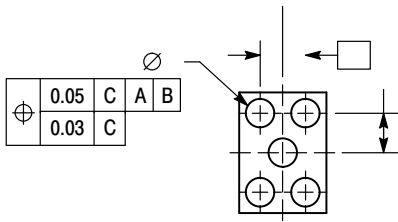
NOTES:
1. DIMENSIONING AND TOLERANCING PER
   ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
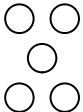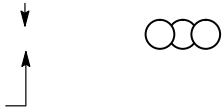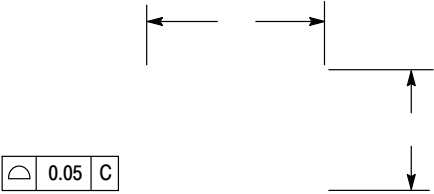3. COPLANARITY APPLIES TO SPHERICAL
   CROWNS OF SOLDER BALLS.

DIM	MILLIMETERS	
	MIN	MAX
A	0.56	0.72
A1	0.21	0.27

b	0.29	0.35
D	0.96 BSC	
E	1.33 BSC	
eD	0.50 BSC	
eE	0.435 BSC	

PIN A1
REFERENCE

2X △ 0.05 C

// 0.05 C

A2

A

△ 0.05 C

A1

C SEATING PLANE

5X ⌀ b

⊕ 0.05 C A B
  0.03 C

eD

C
B
A

1 2 3

WLCSP10, 1.96x1.33
CASE 567BL
ISSUE O

DATE 26 JUL 2010

NOTES:

| ⌒ | 0.05 | C |

SEATING
PLANE