



# CM1409

## LCD A C E D EMI F

### Product Description

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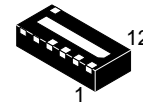
### Applications

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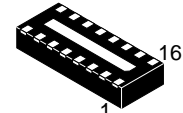


ON Semiconductor

<http://onsemi.com>

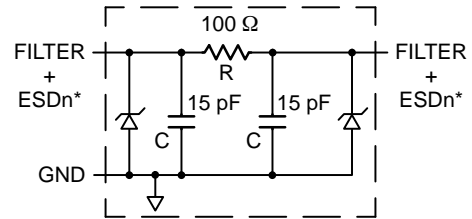


WDFN12  
DE SUFFIX  
CASE 511AZ



WDFN16  
DE SUFFIX  
CASE 511AV

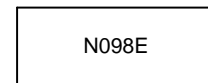
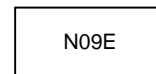
### BLOCK DIAGRAM



1 of 6 or 8 EMI/RFI + ESD Channels

\*See Package/Pinout Diagrams for Expanded Pin Information.

### MARKING DIAGRAM



N09E = CM1409-06DE  
 N098E = CM1409-08DE

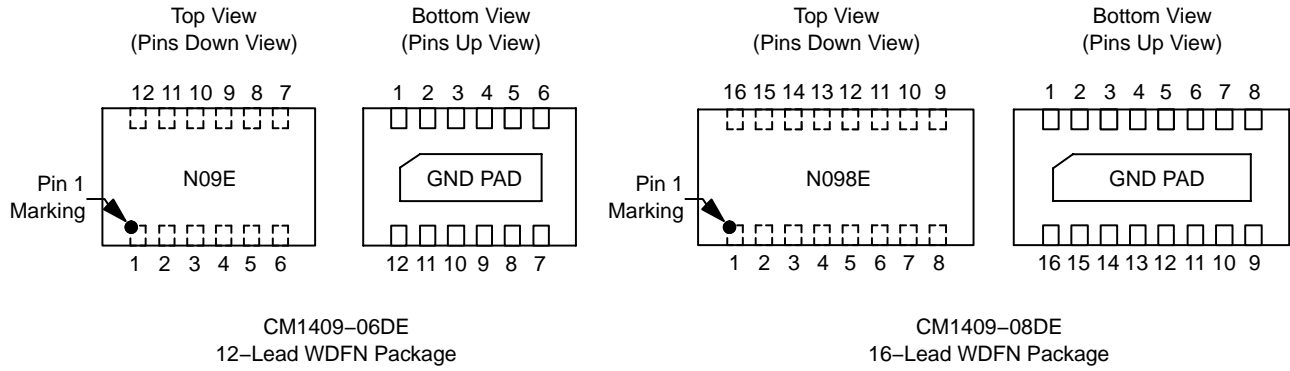
### ORDERING INFORMATION

Device	Package	Shipping†
CM1409-06DE	WDFN12 (Pb-Free)	3000/Tape & Reel
CM1409-08DE	WDFN16 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

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## PACKAGE / PINOUT DIAGRAMS



**Table 1. PIN DESCRIPTIONS**

Device Pin(s)		Name	Description	Device Pin(s)		Name	Description
-06	-08			-06	-08		
1	1	FILTER1	Filter + ESD Channel 1	12	16	FILTER1	Filter + ESD Channel 1
2	2	FILTER2	Filter + ESD Channel 2	11	15	FILTER2	Filter + ESD Channel 2
3	3	FILTER3	Filter + ESD Channel 3	10	14	FILTER3	Filter + ESD Channel 3
4	4	FILTER4	Filter + ESD Channel 4	9	13	FILTER4	Filter + ESD Channel 4
5	5	FILTER5	Filter + ESD Channel 5	8	12	FILTER5	Filter + ESD Channel 5
6	6	FILTER6	Filter + ESD Channel 6	7	11	FILTER6	Filter + ESD Channel 6
	7	FILTER7	Filter + ESD Channel 7		10	FILTER7	Filter + ESD Channel 7
	8	FILTER8	Filter + ESD Channel 8		9	FILTER8	Filter + ESD Channel 8
GND PAD		GND	Device Ground				

**CM1409**

**SPECIFICATIONS**

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## PERFORMANCE INFORMATION

Typical Filter Performance ( $T_A = 25\text{ }^\circ\text{C}$ , DC Bias = 0 V, 50  $\Omega$  Environment)

Figure 1. Insertion Loss vs. Frequency (FILTER1 Input to GND)

Figure 1. Insertion Loss vs. Frequency (FILTER1 Input to GND)

Figure 2. Insertion Loss vs. Frequency (FILTER2 Input to GND)

Figure 2. Insertion Loss vs. Frequency (FILTER2 Input to GND)

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## PERFORMANCE INFORMATION (Cont'd)

Typical Filter Performance ( $T_A = 25\text{ C}$ , DC Bias = 0 V, 50  $\Omega$  Environment)

PERFORMANCE INFORMATION (Cont'd)

Typical Diode Capacitance vs. Input Voltage

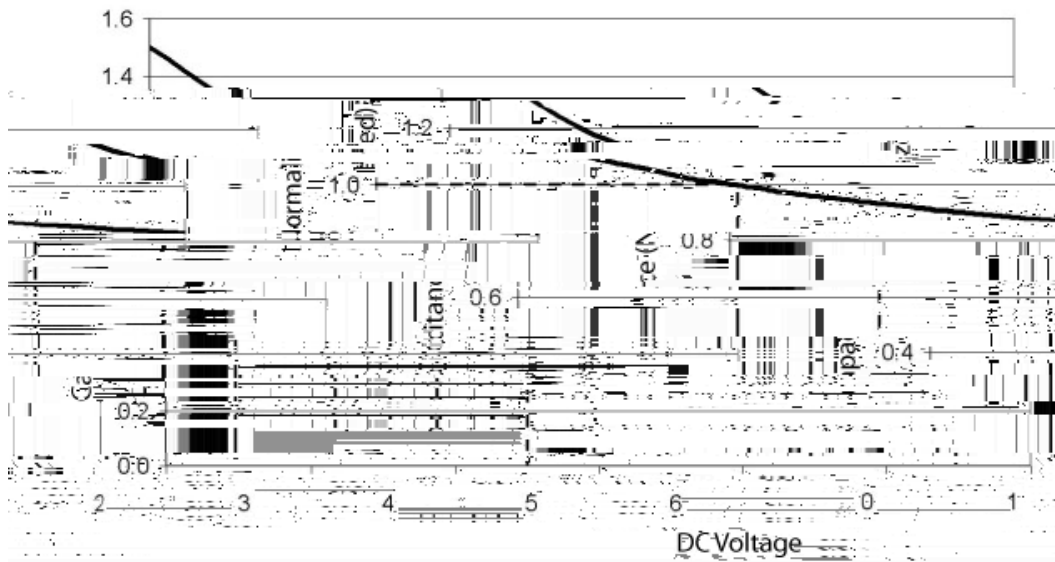
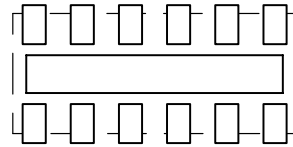
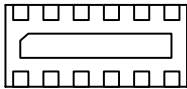
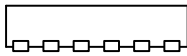
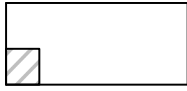


Figure 5. Filter Capacitance vs. Input Voltage  
(normalized to capacitance at 2.5 VDC and 25 C)

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## PACKAGE DIMENSIONS

**WDFN12, 3x1.35, 0.5P**  
CASE 511AZ-01  
ISSUE O



**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 MM FROM TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

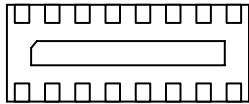
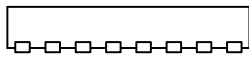
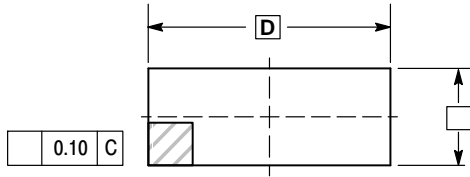
MILLIMETERS		
DIM	MIN	MAX
A	0.70	0.80
A1	0.00	0.05
A3	0.20 REF	
b	0.20	0.30



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## PACKAGE DIMENSIONS

WDFN16, 4x1.6, 0.5P  
CASE 511AV-01  
ISSUE O



**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 MM FROM TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

MILLIMETERS		
DIM	MIN	MAX
A	0.70	0.80
A1	0.00	0.05
A3	0.20 REF	
b	0.20	0.30
D	4.00 BSC	
D2	3.10	3.30
E	1.60 BSC	
E2	0.30	0.50
e	0.50 BSC	

L	0.20	0.40
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