

D (B)
$$1 = 100 \Omega$$
, $2 = 100 \Omega$

PNP Transistors with Monolithic Bias Resistor Network

This series of digital transistors is designed to replace a single device and its external resistor bias network. The Bias Resistor Transistor (BRT) contains a single transistor with a monolithic bias network consisting of two resistors; a series base resistor and a base–emitter resistor. The BRT eliminates these individual components by integrating them into a single device. The use of a BRT can reduce both system cost and board space.

Features

- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- S and NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS $(T_A = 25^{\circ}C)$

Rating	Symbol	Max	Unit
Collector-Base Voltage	V_{CBO}	50	Vdc
Collector–Emitter Voltage	V_{CEO}	50	Vdc
Collector Current – Continuous	I _C	100	mAdc

Table 1. ORDERING INFORMATION

Device	Part Marking	Package	Shipping [†]
MUN2136T1G	6N	SC-59 (Pb-Free)	3000 / Tape & Reel
MMUN2136LT1G, NSVMMUN2136LT1G*	ACG	SOT-23 (Pb-Free)	3000 / Tape & Reel
MUN5136T1G, NSVMUN5136T1G*	6N	SC-70/SOT-323 (Pb-Free)	3000 / Tape & Reel
DTA115EET1G, NSVDTA115EET1G*	6N	SC-75 (Pb-Free)	3000 / Tape & Reel
DTA115EM3T5G	6N	SOT-723 (Pb-Free)	8000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}S and NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.

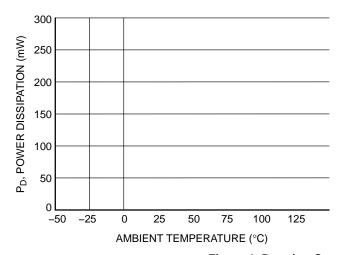


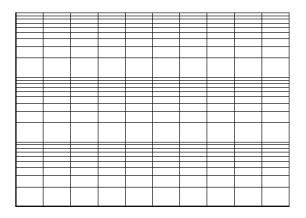
Figure 1. Derating Curve

Table 2. THERMAL CHARACTERISTICS

Characteristic		Symbol	Max	Unit
THERMAL CHARACTERISTICS (SC-59) (MUN2136)				
Total Device Dissipation $T_A = 25^{\circ}C$ Derate above 25°C	(Note 1) (Note 2) (Note 1) (Note 2)	P _D	230 338 1.8 2.7	mW mW/°C

Table 3. ELECTRICAL CHARACTERISTICS (T _A = 25°			

TYPICAL CHARACTERISTICS MUN2136, MMUN2136L, MUN5136, DTA115EE, DTA115EM3





SOT 23 (TO 236) 2.90x1.30x1.00 1.90P CASE 318 ISSUE AU

DATE 14 AUG 2024

SOT 23 (TO 236) 2.90x1.30x1.00 1.90P CASE 318 ISSUE AU

DATE 14 AUG 2024

	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 7: PIN 1. EMITTER 2. BASE 3. COLLECTOR	STYLE 8: PIN 1. ANODE 2. NO CONNECTION 3. CATHODE	ı
STYLE 9:	STYLE 10:	STYLE 11:	STYLE 12:	STYLE 13: STYLE 14: PIN 1. SOURCE PIN 1. CATHODE 2. DRAIN 2. GATE 3. GATE 3. ANODE
PIN 1. ANODE	PIN 1. DRAIN	PIN 1. ANODE	PIN 1. CATHODE	
2. ANODE	2. SOURCE	2. CATHODE	2. CATHODE	
3. CATHODE	3. GATE	3. CATHODE-ANODE	3. ANODE	
STYLE 15:	STYLE 16:	STYLE 17:	STYLE 18:	STYLE 19:
PIN 1. GATE	PIN 1. ANODE	PIN 1. NO CONNECTION	PIN 1. NO CONNECTION	I PIN 1. CATHODE
2. CATHODE	2. CATHODE	2. ANODE	2. CATHODE	2. ANODE
3. ANODE	3. CATHODE	3. CATHODE	3. ANODE	3. CATHODE-ANODE
	STYLE 22: PIN 1. RETURN 2. OUTPUT 3. INPUT	STYLE 23: PIN 1. ANODE 2. ANODE 3. CATHODE 3.		

SC-70 (SOT-323) CASE 419 ISSUE R



DATE 11 OCT 2022

GENERIC MARKING DIAGRAM



XX = Specific Device Code

M = Date Code ■ Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-

STYLE 1: CANCELLED	STYLE 2: PIN 1. ANODE 2. N.C. 3. CATHODE	STYLE 3: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 5: PIN 1. ANODE 2. ANODE 3. CATHODE	
STYLE 6:	STYLE 7:	STYLE 8:	STYLE 9:	STYLE 10:	STYLE 11:
PIN 1. EMITTER	PIN 1. BASE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. CATHODE
2. BASE	2. EMITTER	2. SOURCE	2. CATHODE	2. ANODE	2. CATHODE
3. COLLECTOR	3. COLLECTOR	3. DRAIN	3. CATHODE-ANODE	3. ANODE-CATHODE	3. CATHODE



SC75-3 1.60x0.80x0.80, 1.00P CASE 463 ISSUE H

DATE 01 FEB 2024

RECOMMEND	f

SOT-723 1.20x0.80x0.50, 0.40P CASE 631AA ISSUE E

DATE 24 JAN 2024

GENERIC MARKING



= Specific Device Code = Date Code XX

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