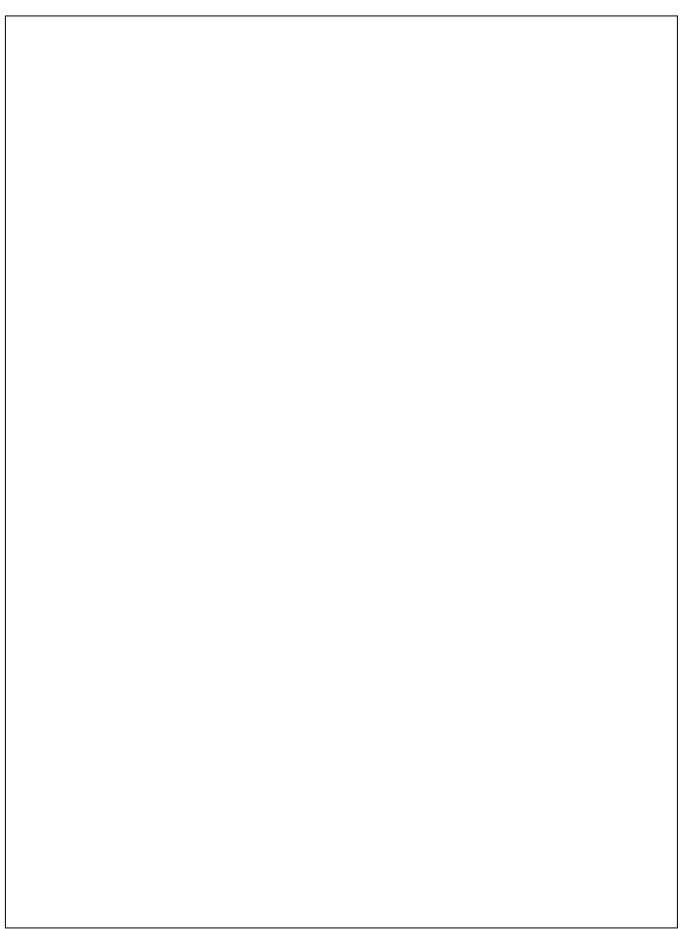


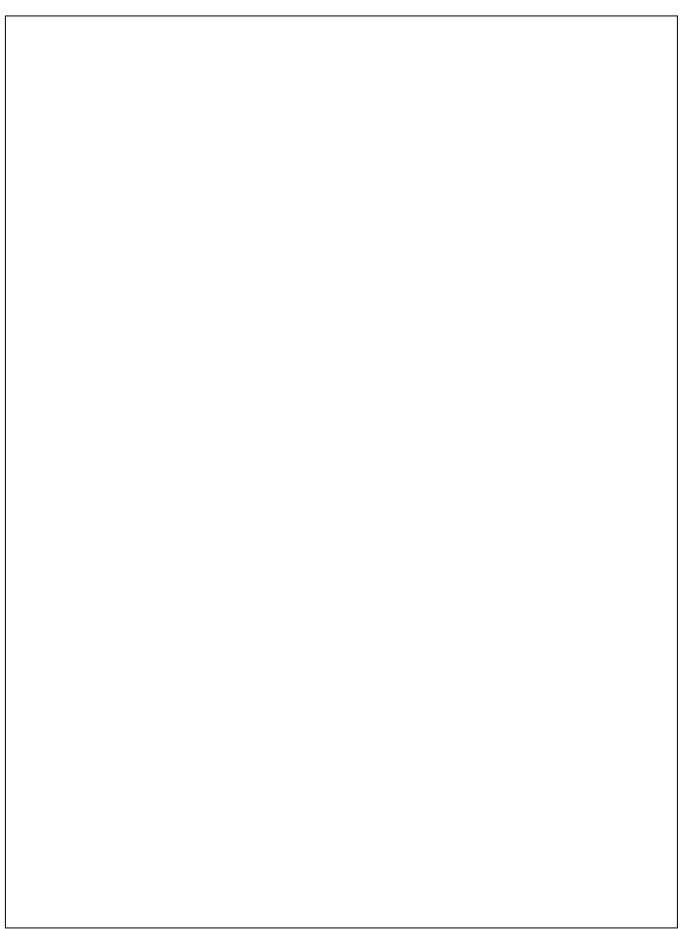
Is Now Part of

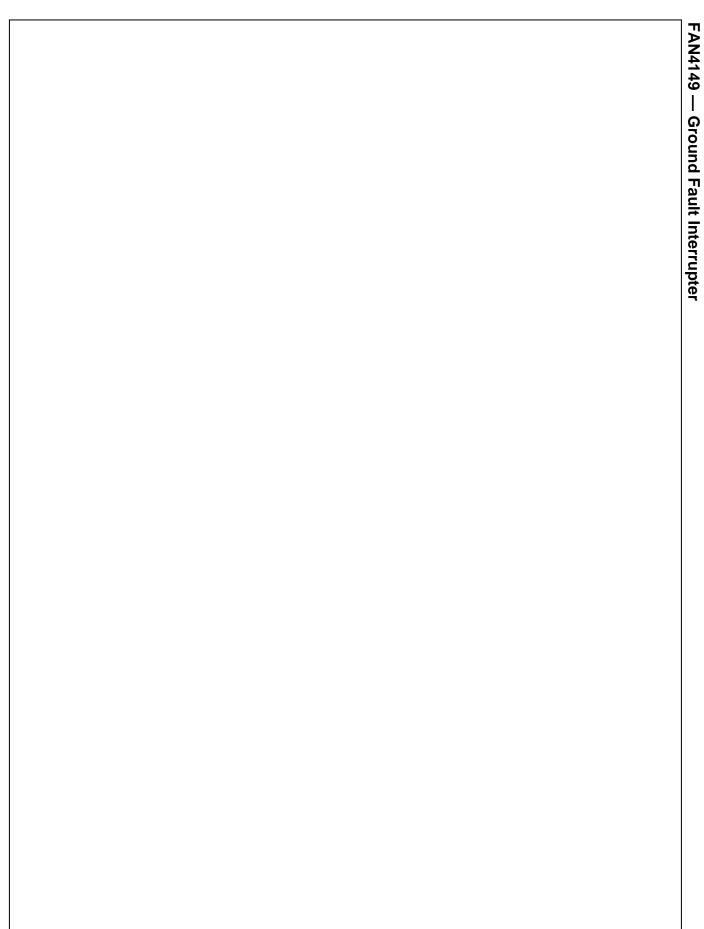


To learn more about ON Semiconductor, please visit our website at www.onsemi.com

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.dON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor doe ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor doe ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON

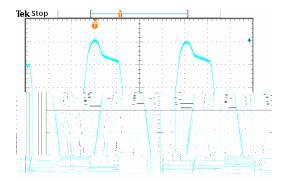






Typical Performance Characteristics (Continued)

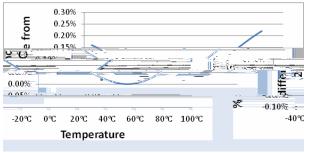
Unless otherwise specified, T_A=25°C and according to Figure 1 with SCR disconnected.



Ch2: AmpOut (Pin 6), 2 V/Div

Figure 8. Typical Waveform for Grounded Neutral Detection

Typical Temperature Characteristics



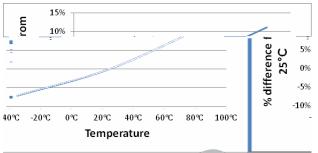


Figure 9. Shunt Regulator Voltage vs. Temperature

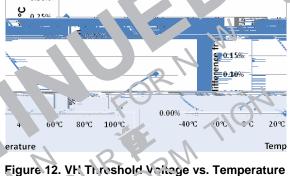
0.30% 0.25% 0.10%چې= ere 0.00%

√s. Te

perature

Temperature

Figure 11. Reference Voltage vs. Ten. 91.



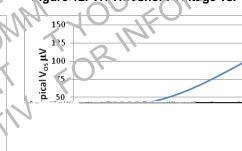


Figure 10. Quiescent Curre

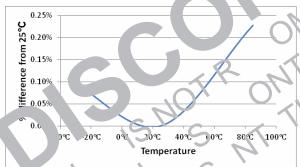


Figure 13. VL Threshold Voltage vs. Temperature

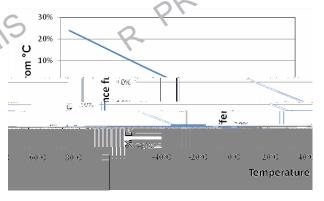
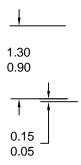
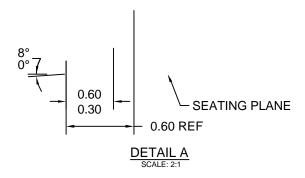


Figure 15. I_{OUT} SCR Out vs. Temperature

Figure 14. Typical Vos vs. Temperature





NOTES:

- A. THIS PACKAGE CONFORMS TO JEDEC MO-178, VARIATION AB.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
- D. DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
- E. DIMENSIONS AND TOLERANCING AS PER ASME Y14.5M-1994
- F. DRAWING FILE NAME: MA06EREV2

APPROVALS	DATE				
DRAWN:		L=/			
	5 JULY 07				
CHECKED:		SE		_	
APPROVED:					
PROJECTION	7	SCALE			
INCH EMM3		FORMERLY:			