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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an he Fairchile Faat utilizes an underscore



FAN5640 — Dual High-Side Constant Current Source for High-Voltage Keypad LED Illumination

Features

20V Maximum Driver Input Level Dual Output 25mA Drive Capability per Channel Two Strings of 2-4 LEDs Each External Resistor Sets Maximum Current Fast Turn-On/Off Capability Low Bias Current SC70-6 Package Thermal Shutdown Protection

Applications

Keypad Illumination Main Display and Sub-Display Illumination Cell Phones, Smart Phones Pocket PCs

Block Diagram	FAN564(
) — Dual High-
Reference + u	-Side Constan
	Current Sour
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	tage Keypad L
	ED Illuminatio
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Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter			Max.	Unit	
V _{EN}	Enable Voltage			6.0	V	
	V _{IN} , V _{IOUT1} , V _{IOUT2}			22.0	V	
I _{RSET}	Current Sourced by RSET			120	А	
TJ	Junction Temperature			150	°C	
T _{STG}	Storage Temperature			150	°C	
TL	Lead Soldering Temperature, 10 Seconds			260	°C	
ESD	Electrostatic Discharge Protection Level	Human Body Model	2		k)/	
		Charged Device Model	2		ΝV	

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ens

Electrical Characteristics

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Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
Power Sup	plies	•			•	
V _{IN}	Input Voltage Range		6		20	V
Ι _Q	Quiescent Current	Measured at GND pin, $V_{IN} = 20V$		48	65	μA
		Measured at GND pin, $V_{IN} = 6V$		44	55	
I _{SD}	Shutdown Supply Current	$V_{IN} = 20V, EN = GND$		8	13	μA
		$V_{IN} = 6V, EN = GND$		5	8	
Enable High-Level Input Voltage			1.2			v
VEN	Enable Low-Level Input Voltage				0.4	
I _{EN}	Enable Input Current	EN = 5V		9	15	μA
		EN=GND		0.1	1.0	
Regulation						
I _{LIM1}	Channel 1 Current Limit ⁽²⁾		30			mA
I _{LIM2}	Channel 2 Current Limit ⁽²⁾		30			mA
I _{OUT}	Output Current Accuracy	$2.5\text{mA} < I_{OUT}$ $5\text{mA}^{(3)}$	-15		+15	%

 $V_{IN} = 6V$ to 20V, $T_A = -40^{\circ}$ C to +85°C, unless otherwise noted. Typical values are at $T_A = 25^{\circ}$ C, $V_{IN} = 14V$, $I_{LED} = 15$ mA.

Typical Characteristics

Unless otherwise noted, C_{IN} = 4.7µF, V_{EN} =1.8V, T_A = 25°C, white LED with V_F =3.3V at I_{OUT} =10mA.



FAN5640 — Dual High-Side Constant Current Source for High-Voltage Keypad LED Illumination

Typical Characteristics (Continued)

Unless otherwise noted, C_{IN} = 4.7µF, V_{EN} =1.8V, T_A = 25°C, white LED with V_F =3.3V at I_{OUT} =10mA.



Application Information

Setting the Output Current Level

An internally generated reference current is mirrored on the MOSFETs connected to the outputs IOUT1 and IOUT2 (pins 1 and 6, respectively). The current mirror ratio is 275 (typical). The voltage on pin 3 (RSET) is 0.475V in steady state; therefore, the programmed current through each of the outputs is:

$$I_{OUT}$$
 275 $\frac{0.475}{R_{EXT}}$ EQ. 1

where R_{EXT} is the external resistor connected from pin 3 to ground. Increasing this external programming resistor reduces the output current. For the maximum rated 25mA rating of each output, the minimum value of the external resistor is:

$$R_{EXT}$$
 275 $\frac{0.475}{I_{OUT}}$ 275 $\frac{0.475}{0.025}$ 5.225k EQ. 2

The LED output current accuracy is ±10% for 25mA current (see the Electrical Characteristics table). In the worst-case scenario, the calculated value of I_{OUT} can lead to an error of ±10% in the LED current. Since the tolerance of R_{EXT} also affects the LED current accuracy, a precision resistor should be chosen to have the least effect on the overall accuracy of the LED current (see Figure 12).

Floating vs. Tied Outputs

Unused outputs can be left floating. The current through is zero, regardless of the current programmed at pin 3. However, ESD protection is enhanced if the unused output pin is tied to VIN (pin 5).

External Capacitors

Because the FAN5640 is stable without capacitors on the outputs, no capacitors are recommended. Typical input decoupling usually present on incoming supply rails should suffice in most applications. If necessary, a small input capacitance may be placed between the input pin and ground without adverse effects.

Dropout Limitations

As for any LDO regulator, there are limitations on how close the input and output rails can be to maintain regulation. The minimum difference is referred to as the dropout. The relevant information is provided in the Typical Performance curve Dropout Voltage vs. LED Current (see Figure 8). The equation for the data is:

This is equivalent to an R_{DS} of 64 with an additional offset of 350mV. This equation is helpful in determining the minimum dissipation in the device and the lowest input voltage for a given application.

Multiple LED Displays

For portable applications, the FAN5640 can be powered from the output of any typical boost regulator. Multiple LED displays can be created with the FAN5640 powered from the output of the FAN5333, as shown in Figure 20. Note that the output voltage of the FAN5333 depends upon the number of LEDs in its output string. Being conscious of the minimum dropout requirements of the FAN5640; if three series LEDs are required to be present at its output, then the FAN5333 should have four series LEDs in its output string.



PWM Dimming

PWM dimming can be implemented by toggling the enable (EN) pin (pin 4). The recommended PWM frequency range is 100Hz to 3kHz. For example, if the rise time is 2.2µs, the actual duty cycle applied internally to the output MOSFETs is slightly less than the duty cycle of the signal applied on the enable pin. This leads to a slight non-linearity in the measured LED current. That error is:

$$\frac{I_{OUT}}{I_{OUT SET}} = \frac{2.2 f_{PWM}}{D_{PWM}} 100\% EQ.4$$

For example, at a PWM frequency of 3kHz, with an applied duty cycle of 10%, the typical error is:

$$\frac{I_{OUT}}{I_{OUT SET}} = \frac{2.2 \ 3k}{0.1} \ 100 \ 6.6\% \qquad EQ.5$$

So, if R_{SET} is 5.225k , the theoretically expected LED current, with a PWM duty cycle of 10%, is 2.5mA. However, the actual (measured) LED current is less by 6.6%. It is (1-0.066) multiplied by 2.5mA, which is 2.335mA. In this way, the actual LED current for any PWM duty cycle and frequency can be estimated.

Input Rail Dimming

The LEDs can also be dimmed by modulating the input supply rail. See Figure 15, PWM Dimming By VIN Pin, under Typical Characteristics. A maximum frequency of 1KHz is recommended.

Power Dissipation

At an ambient temperature (T_A) , the power dissipation (P_D) and the junction temperature (T_J) are related to each other as described in the following equation:

$$T_J T_A P_D J_A EQ.6$$

where:

 $P_{D} \quad (V_{IN} \quad V_{O}) \quad I_{OUT_Total} \quad V_{IN} \quad I_{Q} \quad \frac{V_{RSET}}{R_{EXT}} \quad V_{IN} \quad V_{RSET}$ and

 $I_{OUT_{Total}} = I_{OUT1} + I_{OUT2}$.

The quiescent current (I_Q) can be found in the Electrical Characteristics section. The junction-to-ambient thermal resistance ($_{JA}$) puts a limit on V_{O_MAX}, I_{OUT_MAX}, and the maximum dropout (V_{IN}-V_O) _{MAX}. This affects the number of LEDs used, the current used to drive them, and so on. Ensure that thermal shutdown does not occur. The formula that correlates all these variables is:

$$(V_{IN} \quad V_O)_{MAX} \quad \frac{T_{J_MAX} \quad T_{A_MAX}}{J_A \quad I_{OUT_Total}}$$
 EQ. 7

This should be solved for T_{J MAX} and the result verified as less than the over-temperature shutdown threshold of 150°C (typical). An additional 25°C margin is recommended to account for tolerances on the shutdown threshold; T_{J_MAX} should not exceed 125°C. The JA is dependent on the surrounding PCB layout and can be around 300°C/W for an SC-70 package. This can be improved by providing a heat sink of surrounding copper ground on the PCB. The addition of backside copper with vias, stiffeners, and other enhancements can reduce this value. The heat contributed by the dissipation of other devices located nearby must be included in design considerations. Once the limiting parameters in these two relationships have been determined, the design can be modified to ensure that the device remains within specified operating conditions. If overload conditions are not considered, it is possible for the device to enter a thermal cycling loop, in which the circuit enters a shutdown condition, cools, re-enables, and again overheats and shuts down repeatedly due to an unmanaged fault condition.

LED Selection

The FAN5640 is designed to drive 2-4 LEDs or a higher number of monochrome LEDs. The maximum number of LEDs per channel can be calculated as a function of V_{IN} and the sum of the forward voltage of each LED at the maximum specified current. The minimum number of LEDs driven by FAN5640 is the result of calculating the maximum power dissipated by the IC in the given operating conditions. The forward voltage of LEDs depends upon type of LEDs and the manufacturer. In terms of maximum number of LEDs and LED current, refer to the Dropout Voltage vs. LED Current graph in the Typical Characteristics (*see Figure 8*).

Manufacturer	Part	Website		
HARVATEK	HT-T169TW			



