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# FAN6961

## Boundary Mode PFC Controller

### Features

- Boundary Mode PFC Controller
- Low Input Current THD
- Controlled On-Time PWM
- Zero-Current Detection
- Cycle-by-Cycle Current Limiting
- Leading-Edge Blanking instead of RC Filtering
- Low Startup Current:

**Application Diagram**



Figure 1. Typical Application

**Block Diagram**

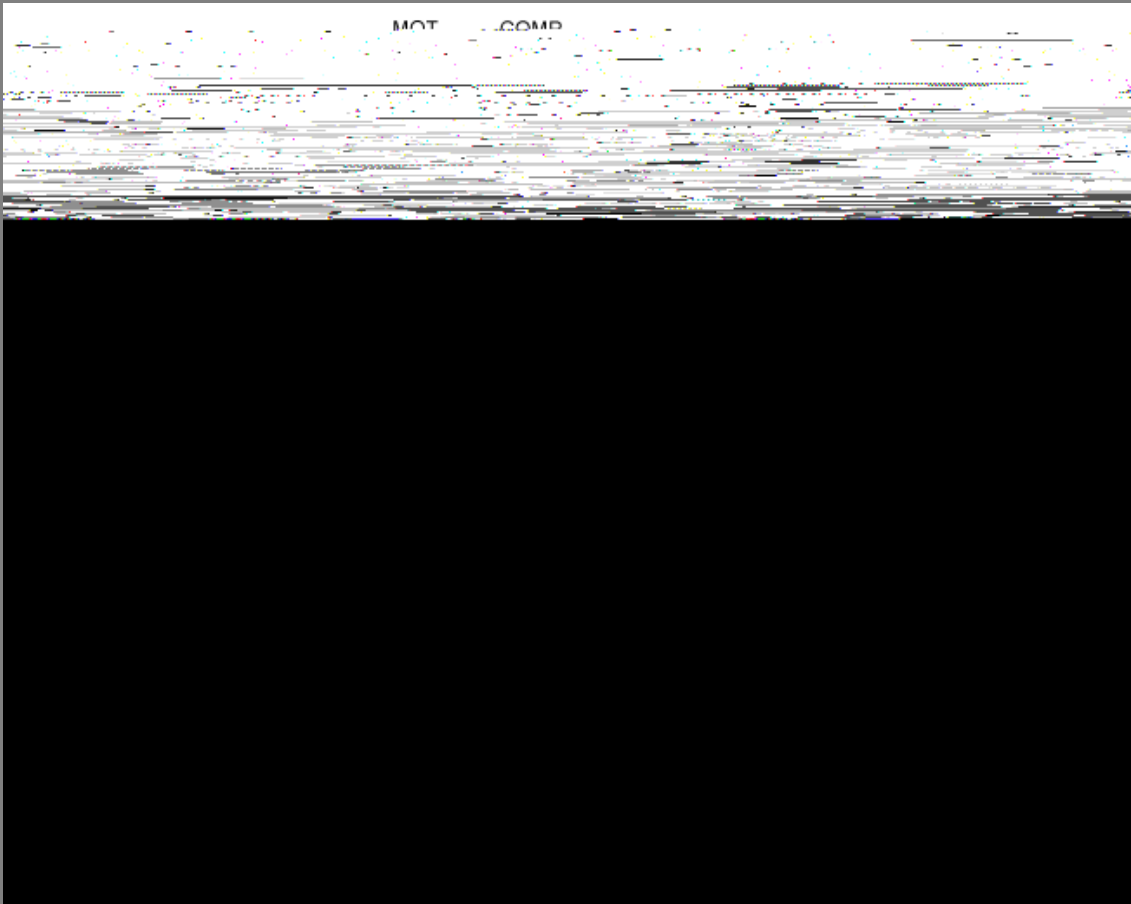


Figure 2. Function Block Diagram

## Marking Information

Figure 3. Marking Information

## Pin Configuration

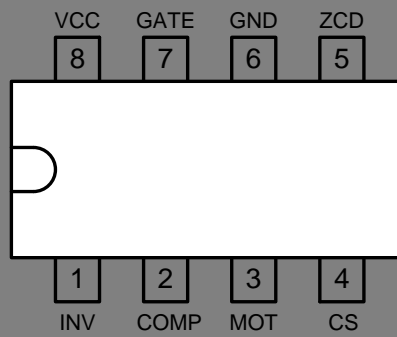


Figure 4. DIP and SOP Pin Configuration (Top View)

## Pin Definitions

## Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. All voltage values, except differential voltage, are

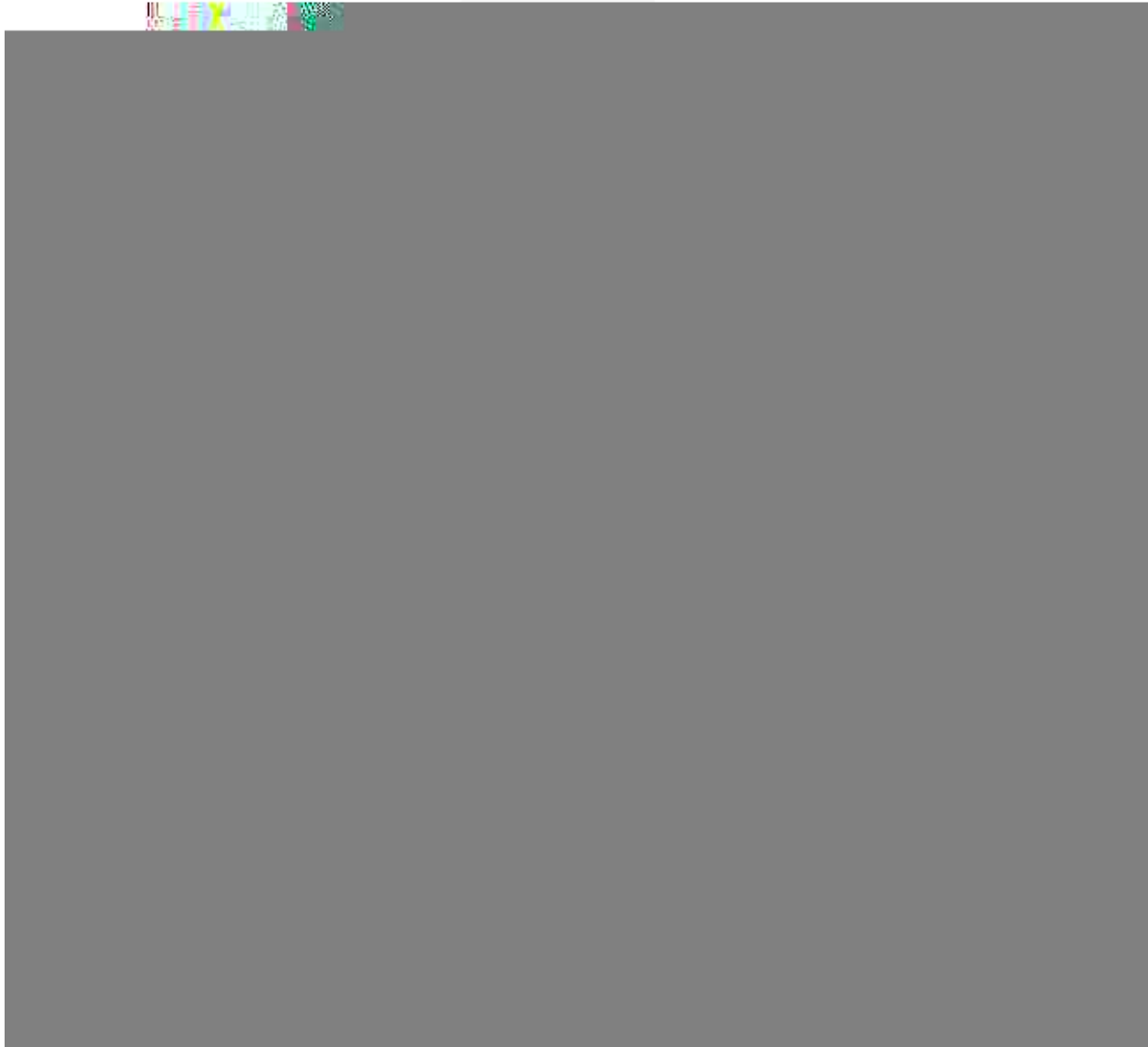


## Electrical Characteristics

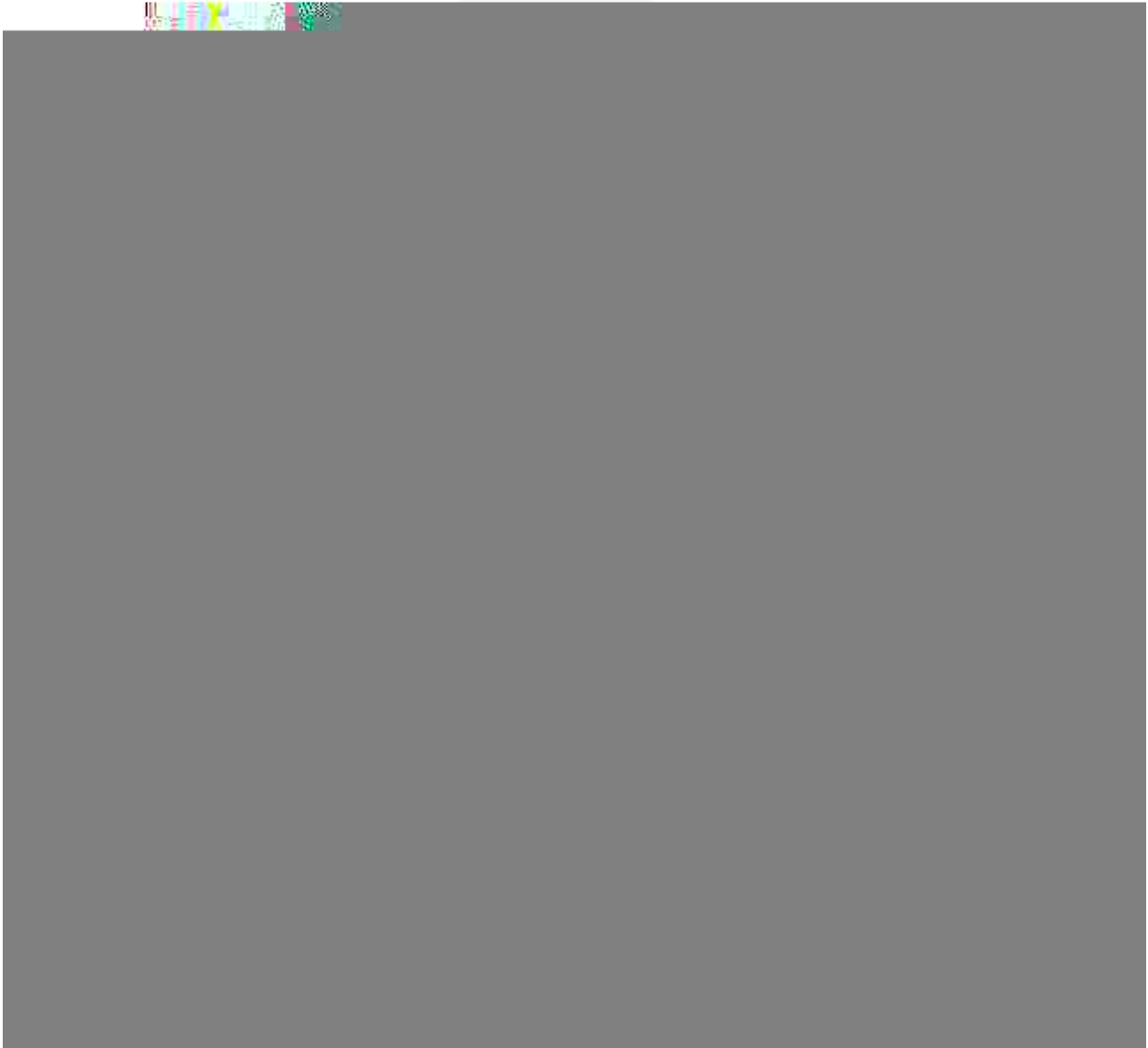
Unless otherwise noted,  $V_{CC}=15\text{ V}$  and  $T_J=-40^\circ\text{C}$  to  $125^\circ\text{C}$ . Current is defined as positive into the device and negative out of the device.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
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### $V_{CC}$ Section













## Reference Circuit

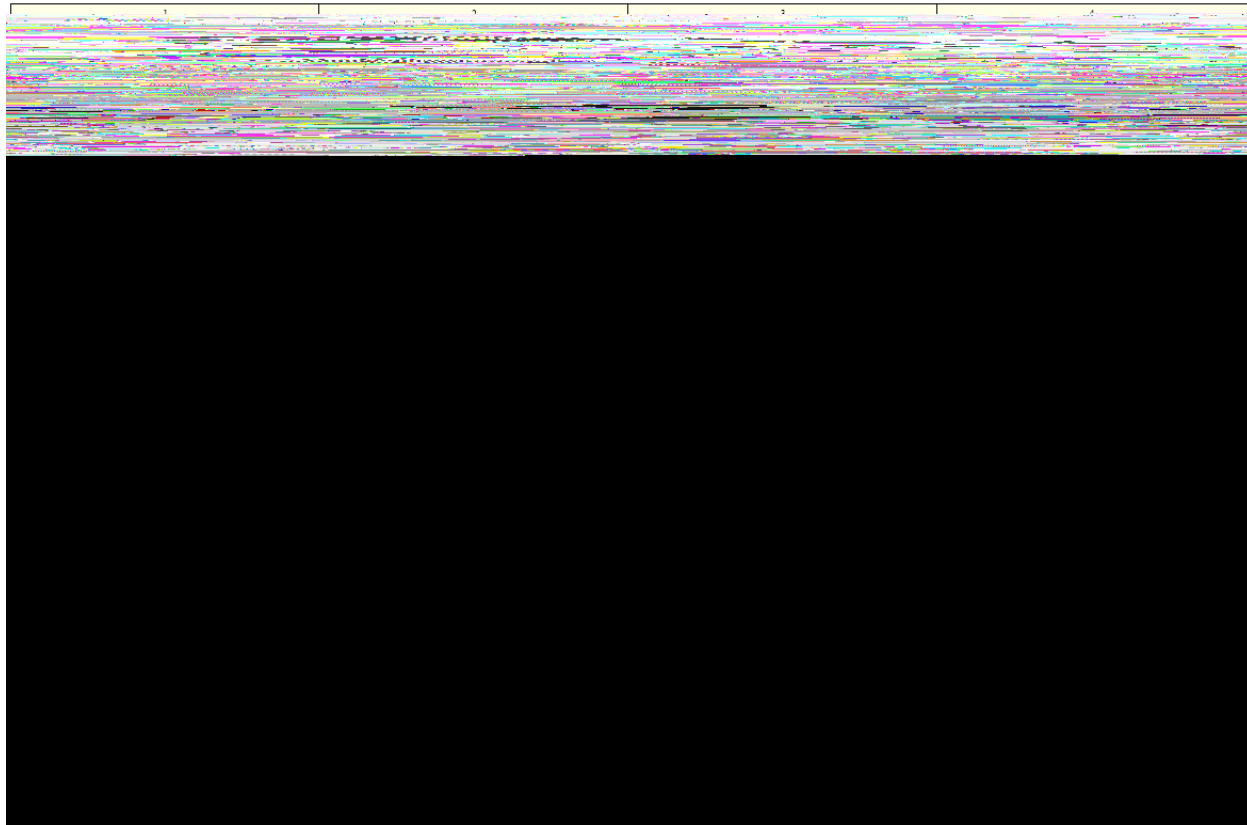
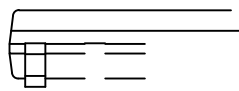
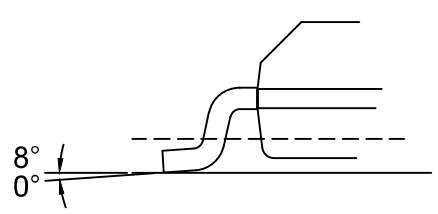


Figure 14. Reference Circuit



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