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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.

August 2016

FAN6961
Boundary Mode PFC Controller

Features

- Boundary Mode PFC Controller
- Low Input Current THD
- Controlled On-Time PWM
- Zero-Current Detection
- Cycle-by-Cycle Current Limiting
- Leading-Edge Blanking instead of RC Filtering
- Low Startup Current:

Application Diagram

Figure 1. Typical Application

Block Diagram

Figure 2. Function Block Diagram

Marking Information

Figure 3. Marking Information

Pin Configuration

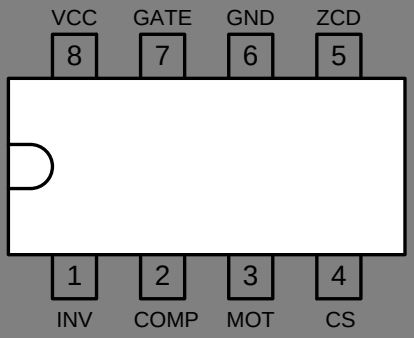


Figure 4. DIP and SOP Pin Configuration (Top View)

Pin Definitions

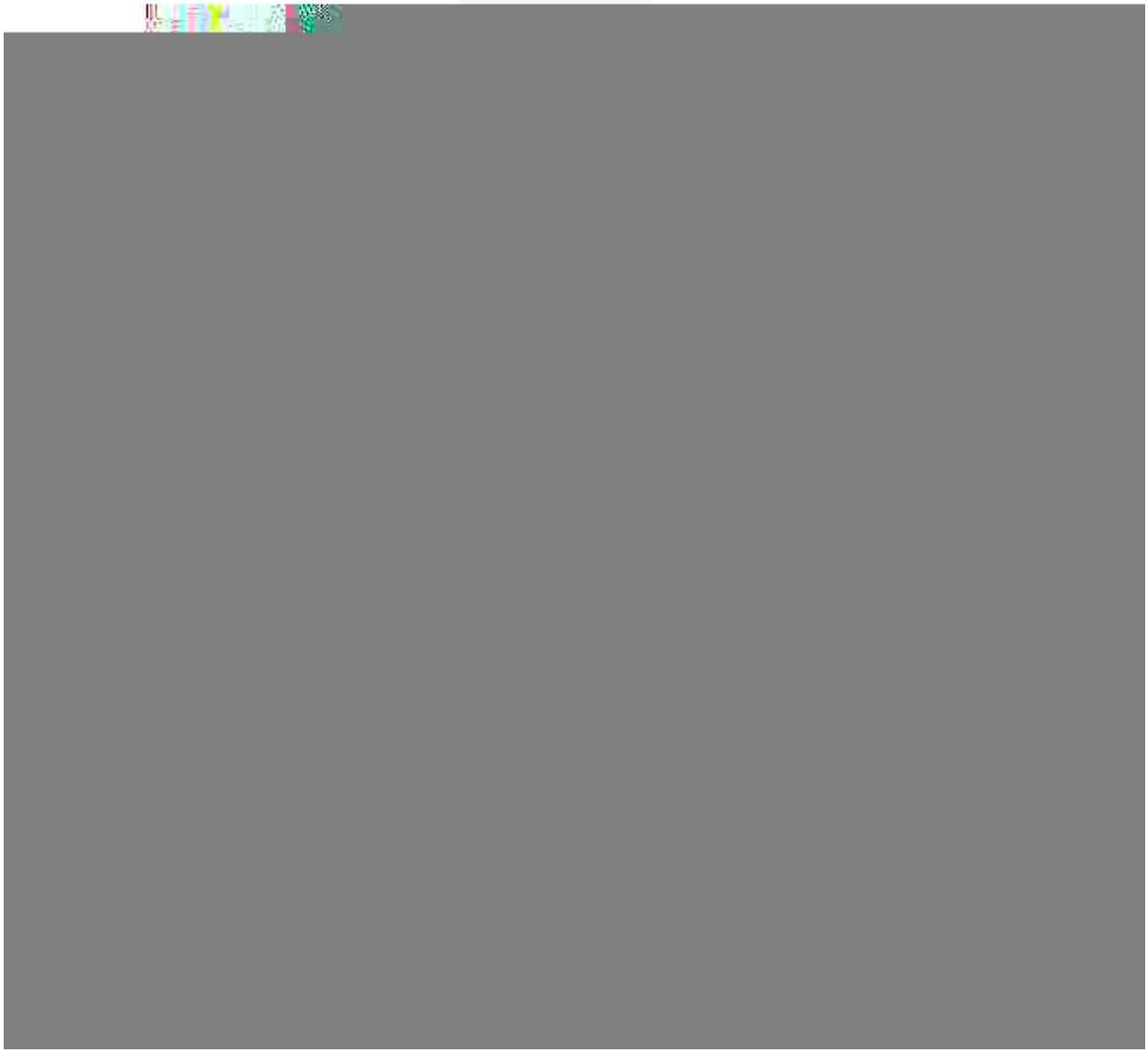
Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. All voltage values, except differential voltage, are gi4.14 re24 653.74 p

Electrical Characteristics

Unless otherwise noted, V_{CC}=15 V and T_J= -40°C to 125°C. Current is defined as positive into the device and negative out of the device.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
V_{CC} Section						



Reference Circuit

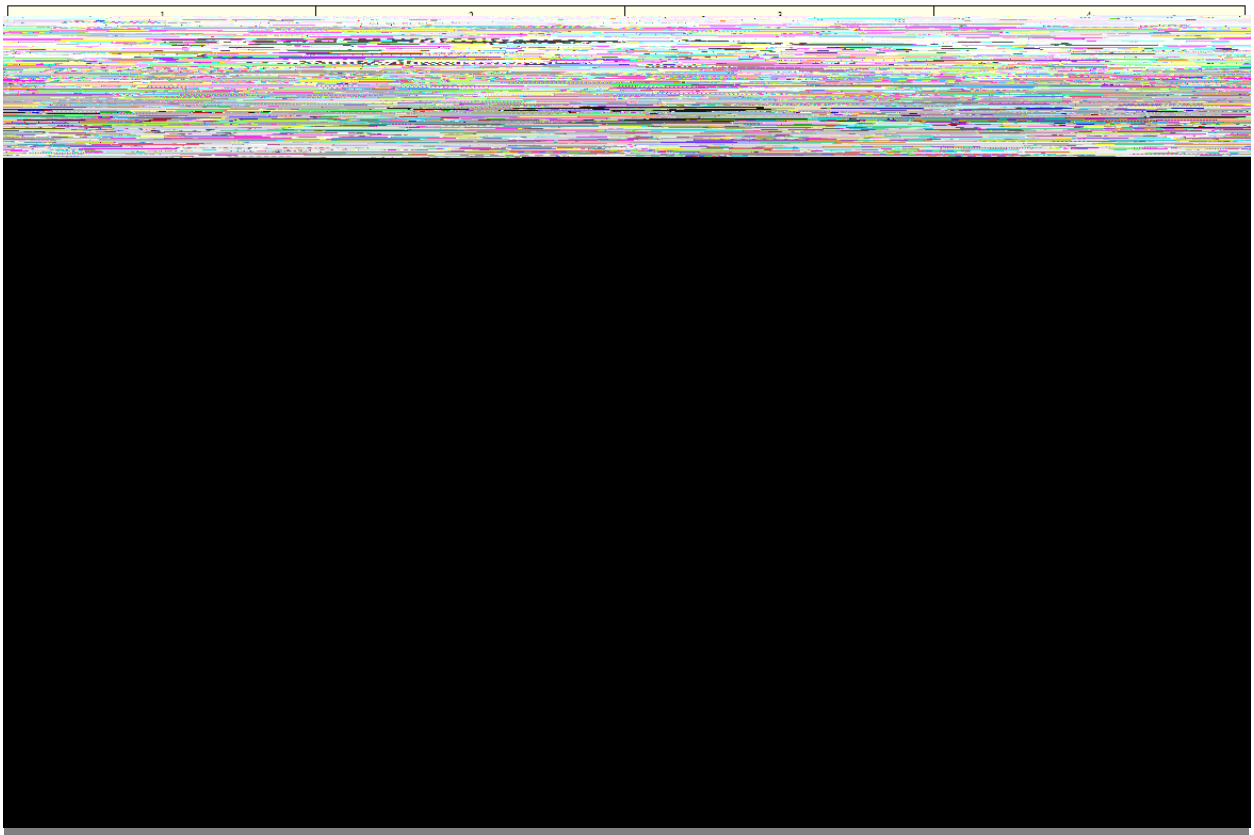
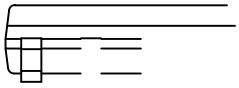
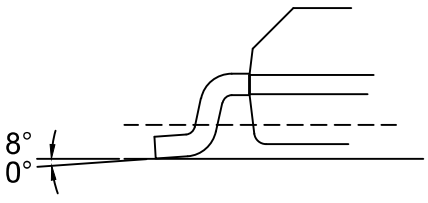


Figure 14. Reference Circuit




8°
0°

8 5

4