

FFSB3065B



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TYPICAL CHARACTERISTICS

($T_J = 25$)

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($T_J = 25^\circ$)

TEST CIRCUIT AND WAVEFORMS

$L = 0.5 \text{ mH}$
 $R < 0.1 \ \Omega$
 $V_{DD} = 50 \text{ V}$
 $E_{AVL} = 1/2LI^2 [V_{R(AVL)} / (V_{R(AVL)} - V_{DD})]$
 $Q1 = \text{IGBT (} BV_{CES} > \text{DUT } V_{R(AVL)} \text{)}$

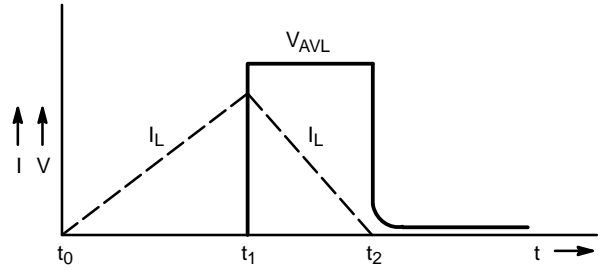
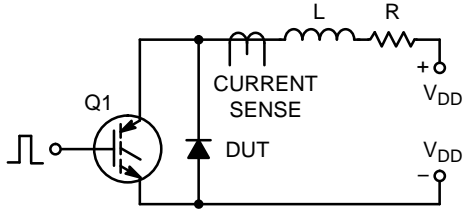
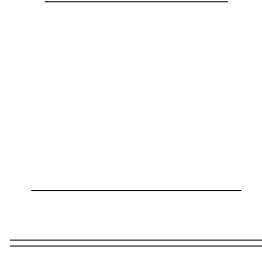
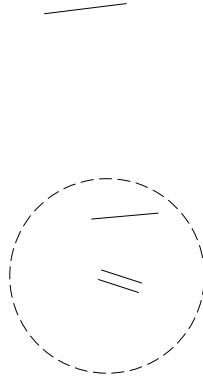


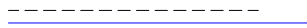
Figure 9. Unclamped Inductive Switching Test Circuit & Waveform

D²PAK2 (TO-263-2L)
CASE 418BK
ISSUE O

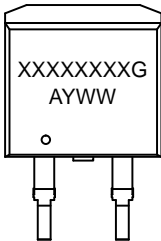
DATE 02 AUG 2018



DET/



**GENERIC
MARKING DIAGRAM***



- XXX = Specific Device Code
- A = Assembly Location
- Y = Year
- WW = Work Week
- G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

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