

Silicon Carbide (SiC) Schottky Diode - EliteSiC, 6 A, 650 V, D2, DPAK

FFSD0665B-F085

Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size and cost.

Features

- Max Junction Temperature 175°C
- Avalanche Rated 24.5 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery / No Forward Recovery
- AEC Q101 Qualified and PPAP Capable
- These Devices are Pb Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- Automotive HEV EV Onboard Chargers
- Automotive HEV EV DC DC Converters

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter		Symbol	Value	Unit
Peak Repetitive Reverse Voltage		V_{RRM}	650	V
Single Pulse Avalanche Energy ($T_J = 25^{\circ}C$, $I_{L(pk)} = 9.9$ A, $L = 0.5$ mH, $V = 50$ V)		E _{AS}	24.5	mJ
Continuous Rectified Forward Current	T _C < 154	lF	6.0	Α
	T _C < 135		9.1	
Non-Repetitive Peak Forward Surge Current	$T_{C} = 25^{\circ}C,$ $t_{P} = 10 \ \mu s$	I _{FM}	493	Α
	$T_C = 150$ °C, t_P		-	

1

FFSD0665B-F085

THERMAL RESISTANCE

Symbol	Parameter	Value	Unit
$R_{ heta JC}$	R _{0JC} Thermal Resistance, Junction-to-Case		°C/W

ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
ON CHARAC	TERISTICS					
V _F F	Forward Voltage	$I_F = 6.0 \text{ A}, T_J = 25^{\circ}\text{C}$	-	1.38	1.7	V
		I _F = 6.0 A, T _J = 125°C	-	1.53	2.0	1
		I _F = 6.0 A, T _J = 175°C	-	1.67	2.4	1
I _R Reverse Current	Reverse Current	V _R = 650 V, T _J = 25°C	_	0.5	40	μΑ
	V _R = 650 V, T _J = 125°C	-	1.0	80]	
		$V_R = 650 \text{ V}, T_J = 175^{\circ}\text{C}$	_	-	•	

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TYPICAL CHARACTERISTICS

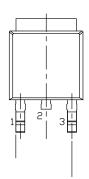
Figure 7. Capacitance Stored Energy

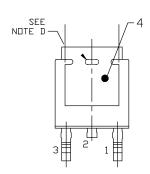
Figure 8. Junction-to-Case Transient Thermal Response

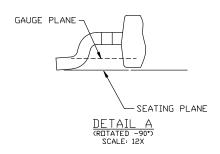


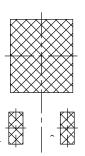
DPAK3 6.10x6.54x2.29, 4.57P CASE 369AS ISSUE B

DATE 20 DEC 2023









LAND PATTERN RECOMMENDATION

GENERIC MARKING DIAGRAM*

XXXXXX XXXXXX AYWWZZ

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

XXXX = Specific Device Code

A = Assembly Location Y = Year

Y = Year WW = Work Week

ZZ = Assembly Lot Code

