ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Min	Max	Unit
V_{CC}	Supply Voltage	−0.5	4.6	V
D_{IN}	DC Input Voltage	−0.5	6.0	V
D_{OUT}	DC Output Voltage	−0.5	4.7	V
I_{OSD}	Driver Short−Circuit Current	Continuous		mA
T_{STG}	Storage Temperature Range	−65	+150	°C
T_J				

AC ELECTRICAL CHARACTERISTICS (All typical values are at T_A = 25°C and with V_{CC} = 3.3 V. Over−supply voltage and operating temperature ranges, unless otherwise specified.)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
t_{PLHD}	Differential Propagation Delay, LOW−to−HIGH	R_L = 100 Ω, CL = 10 pF, see Figure 2 and Figure 3	0.5	−	1.5	ns
t_{PHLD}	Differential Propagation Delay, HIGH−to−LOW		0.5	−	1.5	ns
t_{TLHD}	Differential Output Rise Time (20% to 80%)		0.4	−	1.0	ns
t_{THLD}	Differential Output Fall Time (80% to 20%)		0.4	−	1.0	ns
$t_{SK(P)}$	Pulse Skew $\|t_{PLH} - t_{PHL}\|$		−	−	0.5	

TYPICAL PERFORMANCE CHARACTERISTICS

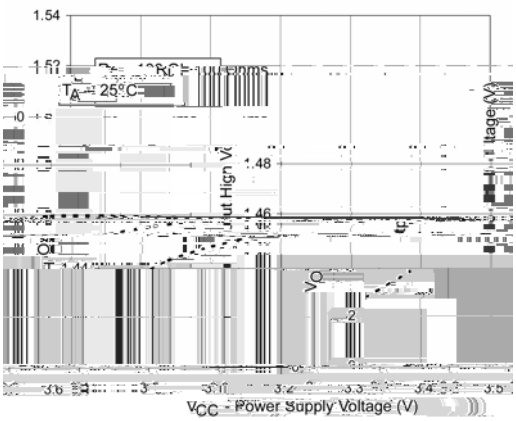

Figure 4. Output High Voltage vs. Power Supply Voltage

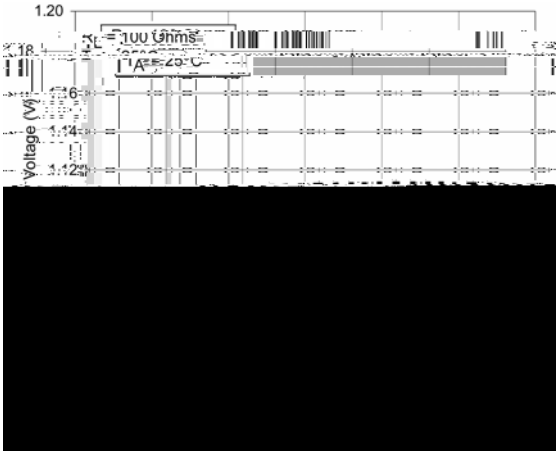

Figure 5. Output Low Voltage vs. Power Supply Voltage

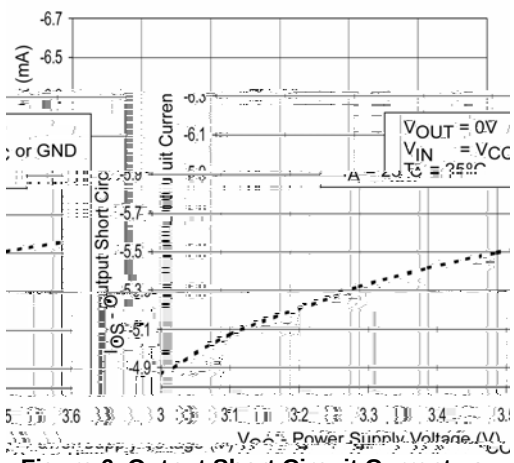

Figure 6. Output Short Circuit Current vs. Power Supply Voltage

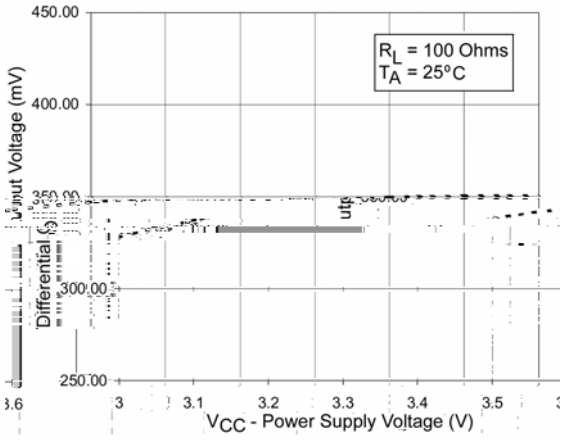

Figure 7. Differential Output Voltage vs. Power Supply Voltage

Figure 8. Differential Output Voltage vs. Load Resistor

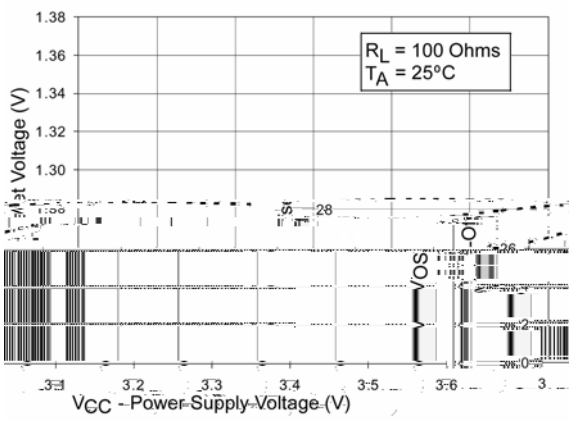

Figure 9. Offset Voltage vs. Power Supply Voltage

TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUED)

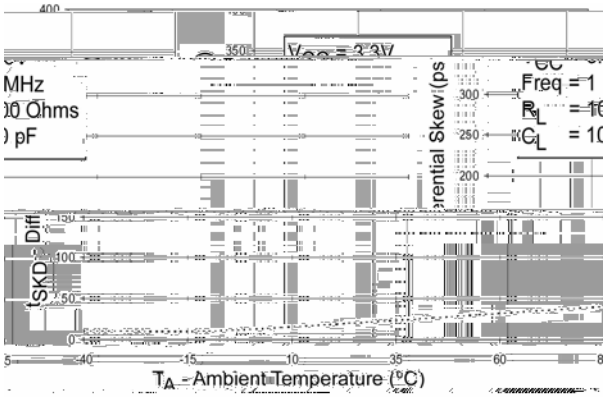

Figure 16. Differential Pulse Skew ($t_{PLH} - t_{PHL}$)

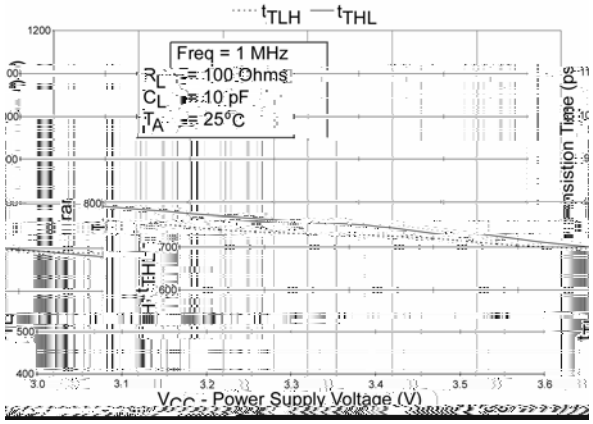

Figure 17. Transition Time vs. Power Supply Voltage

Figure 18. Transition Time vs. Ambient Temperature

ORDERING INFORMATION

Part Number	Operating Range Temperature	Package	Shipping†
FIN1027AMX	−40 to +85°C	8−Lead Small Outline Package (SOIC), JEDEC MS−012, 0.150 inch Narrow (Pb−Free, Halide Free)	

SOIC8
CASE 751EB
ISSUE A

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