



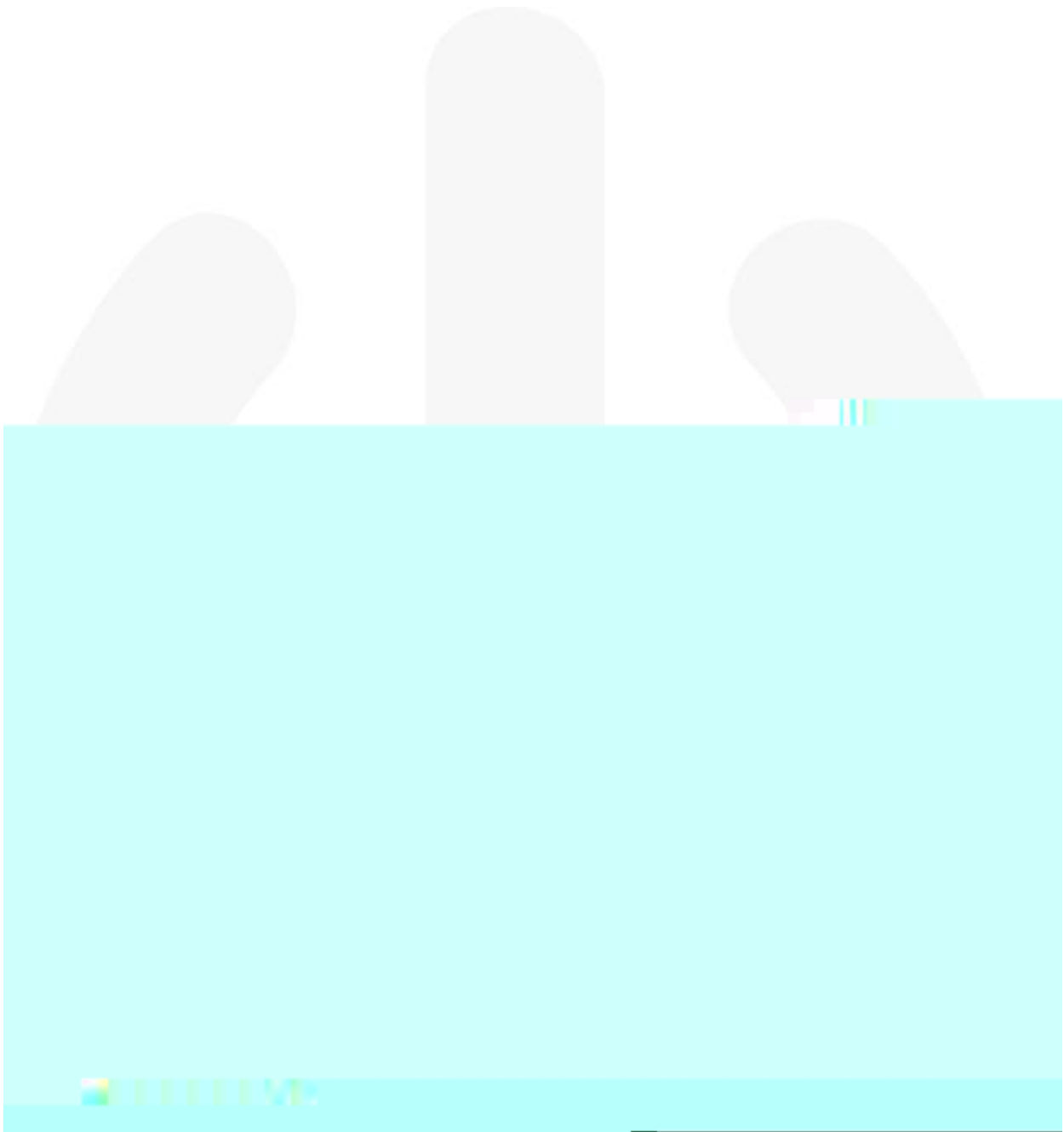
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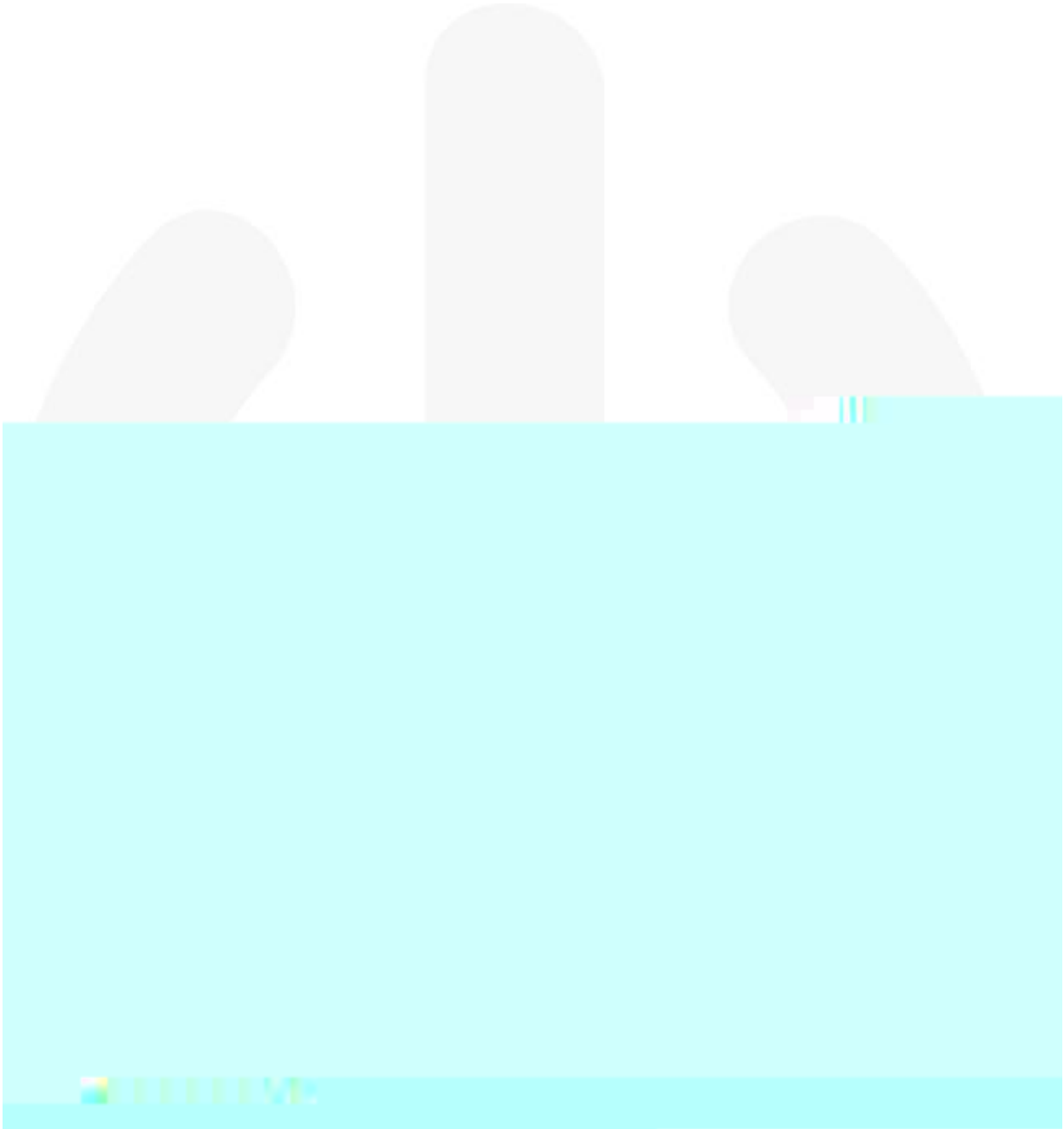


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## Absolute Maximum Ratings

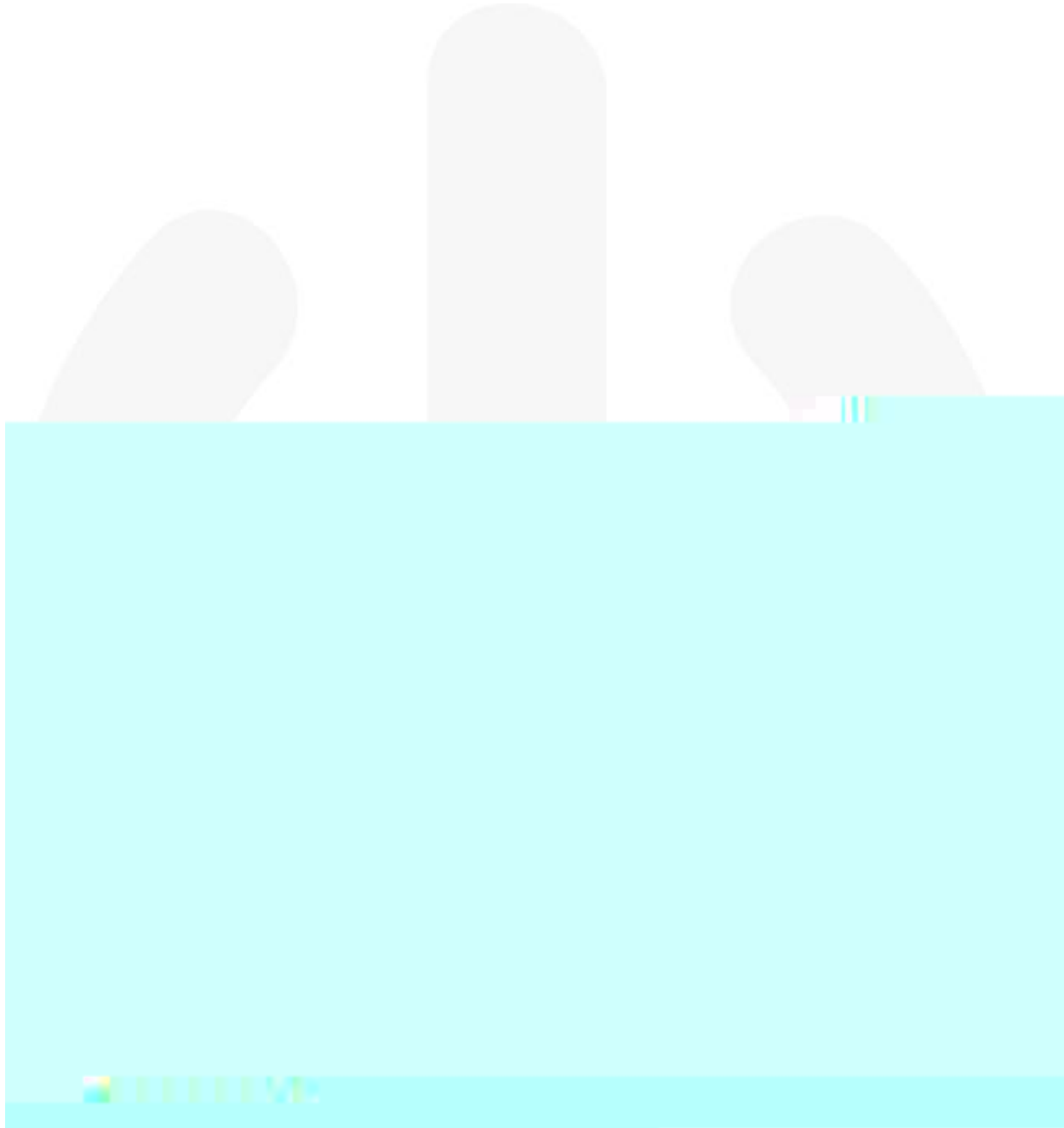
Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Min.	Max.	Unit
$V_{CC}$	Supply Voltage	-0.5	+4.6	V
$V_{IN}$	LVDS DC Input Voltage	-0.5	+4.6	V
$V_{OUT}$	LVDS DC Output Voltage	-0.5	+4.6	V
$I_{OSD}$	Driver Short-Circuit Current			

### DC Electrical Characteristics

Over-supply voltage and operating temperature ranges, unless otherwise specified. All typical values are at  $T_A=25^\circ\text{C}$  and with  $V_{CC}=3.3\text{ V}$ .

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
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## AC Electrical Characteristics

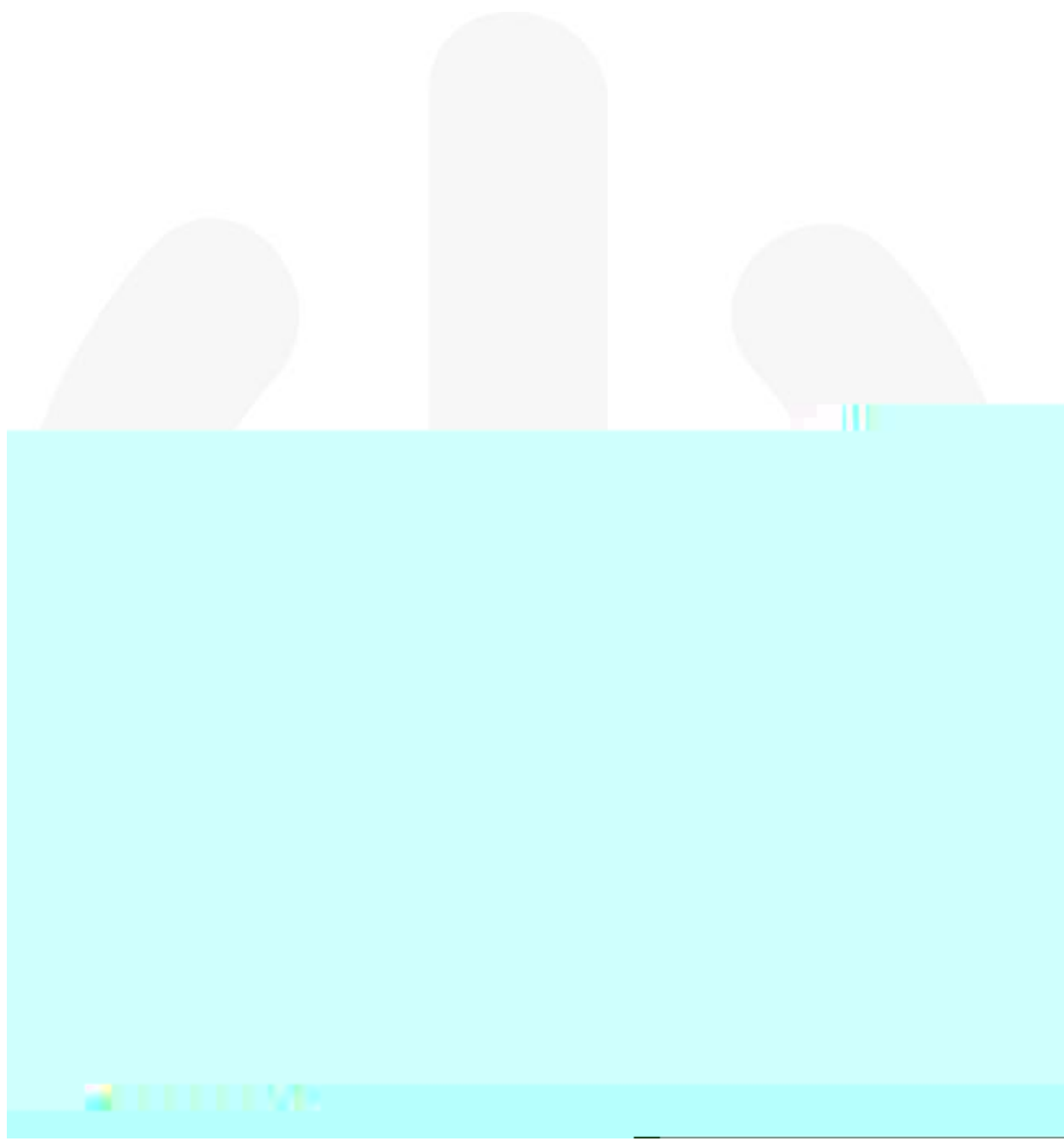
Over-supply voltage and operating temperature ranges, unless otherwise specified. All typical values are at  $T_A=25^\circ\text{C}$  and with  $V_{CC}=3.3\text{ V}$ .

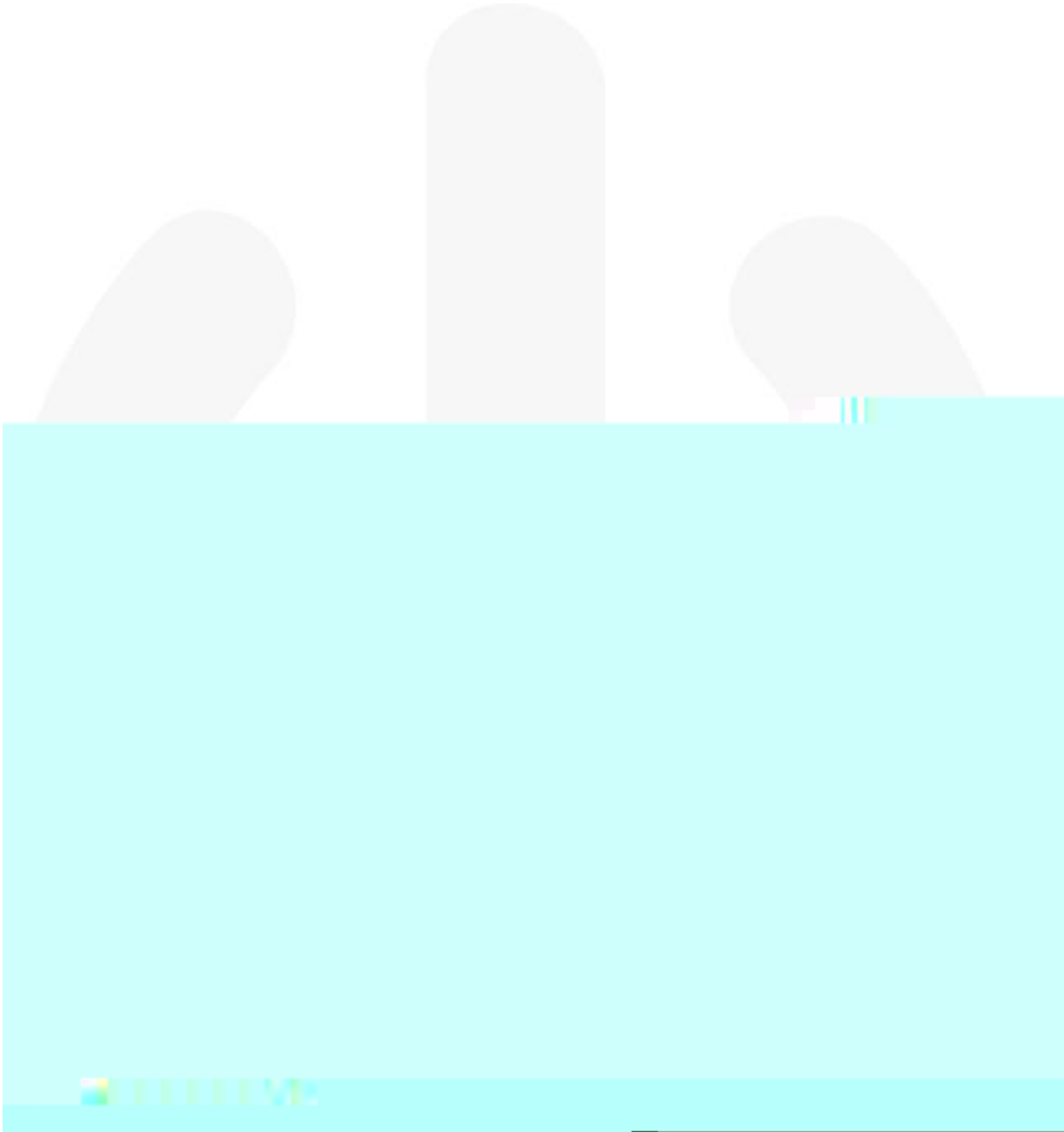
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
<b>Switching Characteristics - LVDS Outputs</b>						
$t_{PLHD}$	Differential Propagation Delay LOW-to-HIGH	See Figure 5, Figure 6			2	ns
$t_{PHLD}$	Differential Propagation Delay HIGH-to-LOW				2	ns
$t_{TLHD}$	Differential Output Rise Time (20% to 80%)		0.2		1.0	ns
$t_{THLD}$	Differential Output Fall Time (80% to 20%)		0.2		1.0	ns
$t_{SK(P)}$	69.36 572.u.08 reW\$ SkeW(RPIH626 587.26					

## Required Specifications and Test Diagrams

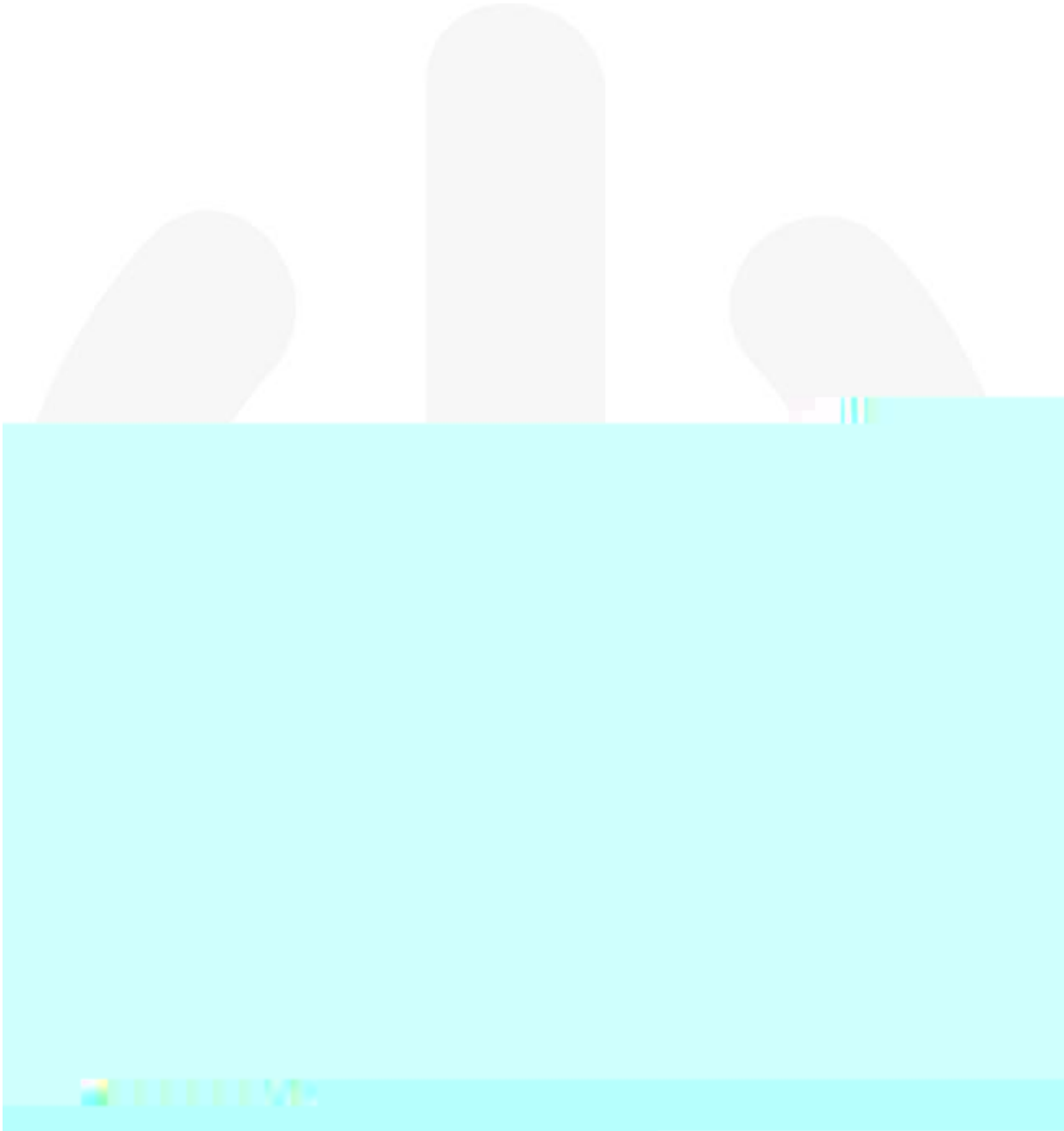
### Notes:

7. Electrostatic Discharge Capability: Human Body Model and Machine Model ESD should be measured using MIL-STD-883C method 3015.7 standard.
- 8.









Required Specifications and Test Diagrams (Continued)

Figure 9. LVTTTL Output Propagation Delay and Transition Time Test Circuit

Notes:

17. A:  $Z_0=50$  and  $C_T=28$  pF



