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SEMICONDUCTOR

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FL6961 Single-Stage Flyback and Boundary Mode PFC **Controller for Lighting**

Description

The FL6961 is a general lighting power controller for low- to high-power lumens applications requiring power

factor correction. It is designed for flyback or boost

The FL6961 provides a controlled on-time to regulate

the output DC voltage and achieves natural power factor

correction (PFC). The maximum on-time of the external switch is programmable to ensure safe operation during

AC brownouts. An innovative multi-vector error amplifier

provides rapid transient response and precise output voltage clamping. A built-in circuit disables the controller

if the output feedback loop is opened. The startup

current is lower than 20µA and the operating current is less than 6mA. The supply voltage can be up to 25V,

converter operating in Boundary Mode.

maximizing application flexibility.

Features

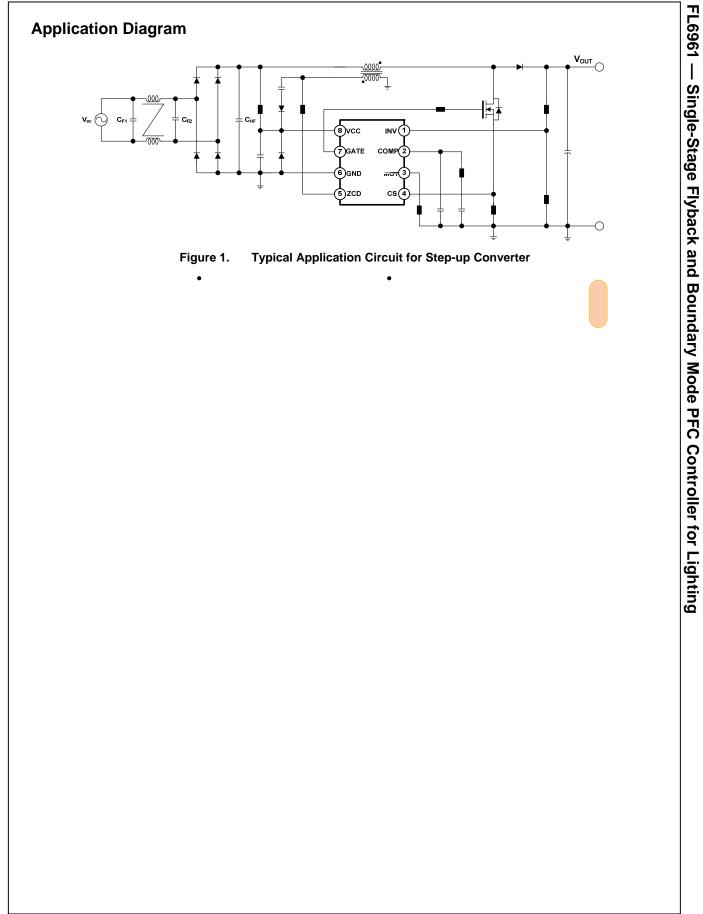
- Boundary Mode PFC Controller
- Low Input Current THD
- Controlled On-Time PWM
- Zero-Current Detection
- Cycle-by-Cycle Current Limiting
- Leading-Edge Blanking Instead of RC Filtering
- Low Startup Current: 10µA Typical
- Low Operating Current: 4.5mA Typical
- Feedback Open-Loop Protection
- Programmable Maximum On-Time (MOT)
- **Output Over-Voltage Clamping Protection**
- Clamped Gate Output Voltage: 16.5V

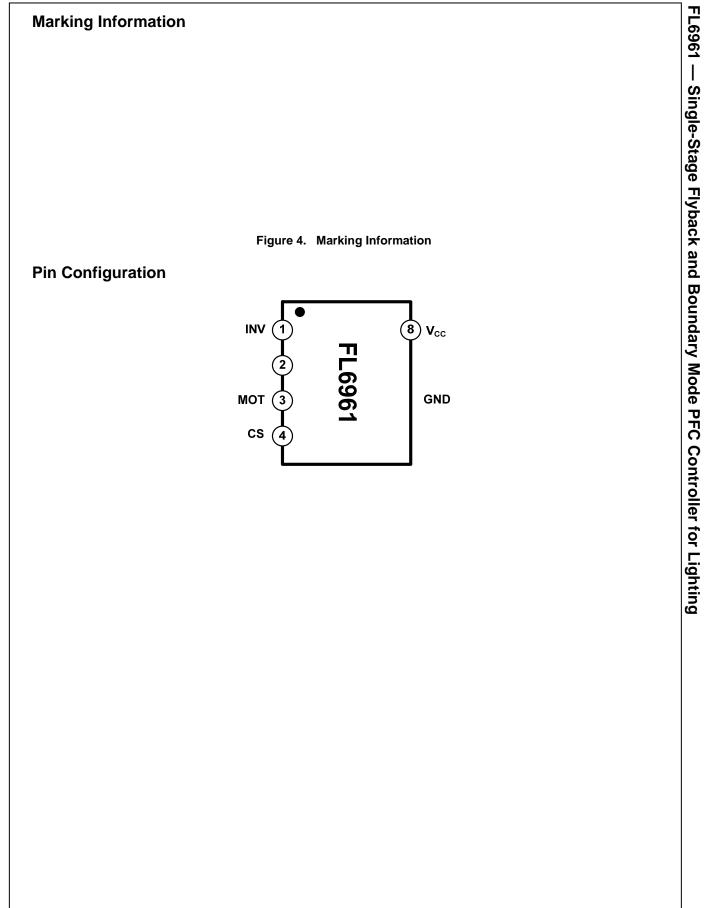
Applications

- General LED Lighting
- Industrial, Commercial and Residential Fixtures
- Outdoor Lighting: Street, Roadway, Parking, Construction, and Ornamental LED Lighting

Ordering Information

Part Number	Operating Temperature Range	Package	Packing Method	
FL6961MY	-40°C to +125°C	8-Pin, Small Outline Package (SOP)	Tape & Reel	





Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. All voltage values, except differential voltage, are given with respect to GND pin.

Symbol	Parameter	Min.	Max.	Unit
V _{VCC}	DC Supply Voltage		30	V
V _{HIGH}	Gate Driver	-0.3	30.0	V
V_{LOW}	Others (INV, COMP, MOT, CS)	-0.3	7.0	V
V_{ZCD}	Input Voltage to ZCD Pin	-0.3	12.0	V
PD	Power Dissipation		660	mW
TJ	Operating Junction Temperature	-40	+150	С
JA	Thermal Resistance (Junction-to-Air)		150	C/W
JC	Thermal Resistance (Junction-to-Case)		39	C/W
T _{STG}	Storage Temperature Range	-65	+150	С
ΤL	Lead Temperature (Wave Soldering or IR, 10 Seconds)		+230	С
ESD	Human Body Model: JESD22-A114		2.5	K٧
E9D	Machine Model: JESD22-A115		200	V

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Тур.	Max.	Unit
T _A	Operating Ambient Temperature	-40		+125	С

Electrical Characteristics

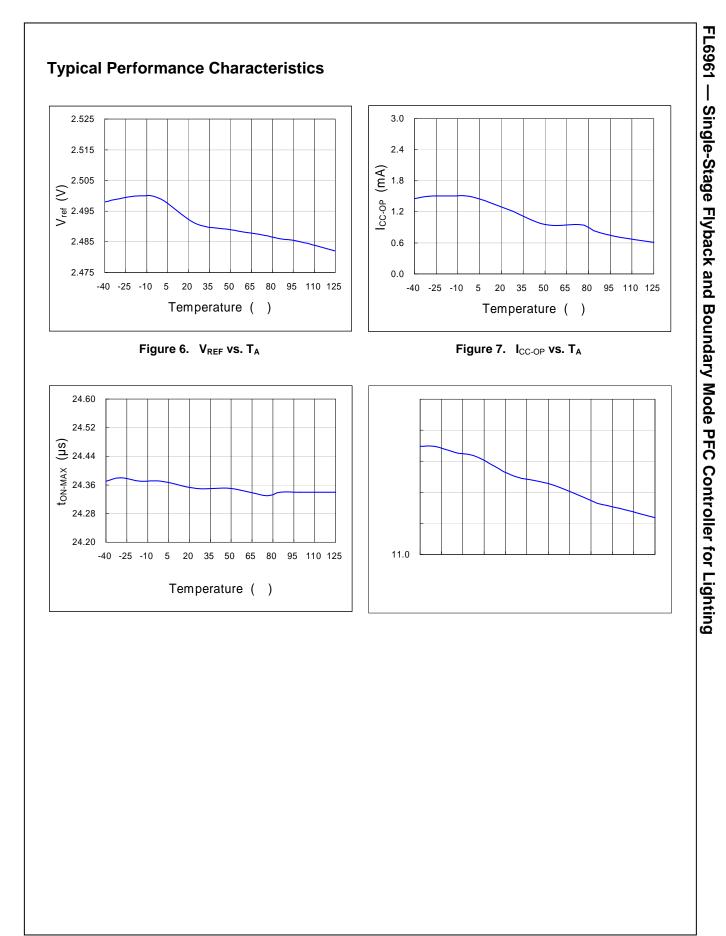
Unless otherwise noted, V_{CC} =15V and T_{J} =-40°C to 150°C. Current is defined as positive into the device and negative out of the device.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
V _{CC} Section	on					
V _{CC-OP}	Continuous Operation Voltage				24.5	V
V _{CC-ON}	Turn-On Threshold Voltage		11.5	12.5	13.5	V
$V_{\text{CC-OFF}}$	Turn-Off Threshold Voltage		8.5	9.5	10.5	V
I _{CC-ST}	Startup Current	$V_{CC}=V_{CC-ON}-0.16V$		10	20	μA
I _{CC-OP}	Operating Supply Current	V_{CC} =12V, V_{CS} =0V, C _L =3nF, f _{SW} =60KHz		4.5	6	mA
V _{CC-OVP}	V _{DD} Over-Voltage Protection Level		26.8	27.8	28.8	V
t _{D-VCCOVP}	V _{DD} Over-Voltage Protection Debounce			30		μs
Error Am	plifier Section		•			•
V_{REF}	Reference Voltage		2.475	2.500	2.525	V
Gm	Transconductance			125		µmho
V _{INVH}	Clamp High Feedback Voltage			2.65	2.70	V
VINVL	Clamp Low Feedback Voltage		2.25	2.30		V
V _{OUT HIGH}	Output High Voltage		4.8			V
V _{OZ}	Zero Duty Cycle Output Voltage		1.15	1.25	1.35	V
V _{INV-OVP}	Over-Voltage Protection for INV Input		2.70	2.75	2.80	V
VINV-UVP	Under-Voltage Protection for INV Input		0.40	0.45	0.50	V
	Queene Queenet	V _{INV} =2.35V, V _{COMP} =1.5V	10	20		
I _{COMP}	Source Current	V _{INV} =1.5V	550	800		μA
	Sink Current	V _{INV} =2.65V, V _{COMP} =5V	10	20		1
Current-S	Sense Section		•			•
V _{PK}	Threshold Voltage for Peak Current Limit Cycle-by-Cycle Limit		0.77	0.82	0.87	V
t _{PD}	Propagation Delay				200	ns
		R _{MOT} =24k , V _{COMP} =5V		400	500	
t _{LEB}	Leading-Edge Blanking Time	R _{MOT} =24k , V _{COMP} =V _{OZ} +50mV		270	350	ns
Gate Sect	tion					
Vz - out	Output Voltage Maximum (Clamp)	V _{CC} =25V	14.5	16.0	17.5	V
V _{OL}	Output Voltage Low	V _{CC} =15V, I _O =100mA			1.4	V
V _{OH}	Output Voltage High	V _{CC} =14V, I _O =100mA	8			V
t _R	Rising Time	V _{CC} =12V, C _L =3nF, 20~80%		80		ns
t _F	Falling Time	V _{CC} =12V, C _L =3nF, 80~20%		40		ns

Electrical Characteristics

Unless otherwise noted, V_{CC} =15V and T_{J} =-40°C to 150°C. Current is defined as positive into the device and negative out of the device.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
Zero-Curi	rent Detection Section					
V _{ZCD}	Input Threshold Voltage Rising Edge	V _{ZCD} Increasing	1.9	2.1	2.3	V
H_{YS} of V_{ZCD}	Threshold Voltage Hysteresis	V _{ZCD} Decreasing		0.35		V
V _{ZCD-HIGH}	Upper Clamp Voltage	I _{ZCD} =3mA			12	V
V _{ZCD-LOW}	Lower Clamp Voltage	I _{ZCD} =-1.5mA	0.3			V
t _{DEAD}	Maximum Delay, ZCD to Output Turn-On	V _{COMP} =5V, f _{SW} =60KHz	100		400	ns
trestart	Restart Time	Output Turned Off by ZCD	300	500	700	μs
t _{INHIB}	Inhibit Time (Maximum Switching Frequency Limit)	R _{MOT} =24k		2.8		μs
V _{DIS}	Disable Threshold Voltage		130	200	250	mV
t _{ZCD-DIS}	Disable Function Debounce Time	R _{MOT} =24k , V _{ZCD} =100mV	800			μs
Maximum	On Time Section					
V _{MOT}	Maximum On Time Voltage		1.25	1.30	1.35	V
t _{on-max}	Maximum On Time Programming (Resistor Based)	$\begin{array}{c} R_{MOT}{=}24k \hspace{0.1in}, \hspace{0.1in} V_{CS}{=}0V, \\ V_{COMP}{=}5V \end{array}$		25		μs



Typical Performance Characteristics (Continued)

Functional Description

Error Amplifier

The inverting input of the error amplifier is referenced to INV. The output of the error amplifier is referenced to COMP. The non-inverting input is internally connected to a fixed $2.5V \pm 2\%$ voltage. The output of the error amplifier is used to determine the on-time of the PWM output and regulate the output voltage. To achieve a low input current THD, the variation of the on-time within one input AC cycle should be very small. A multivector error amplifier is built in to provide fast transient response and precise output voltage clamping.

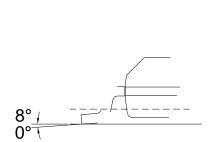
Connecting a capacitance, such as $1\mu F$, between COMP and GND is suggested. The error amplifier is a trans-conductance amplifier that converts voltage to current with a $125\mu mho$.

Startup Current

Typical startup current is less than $20\mu A$. This ultra-low startup current allows the usage of a high resistance, low-wattage startup resistor. For example, 1M

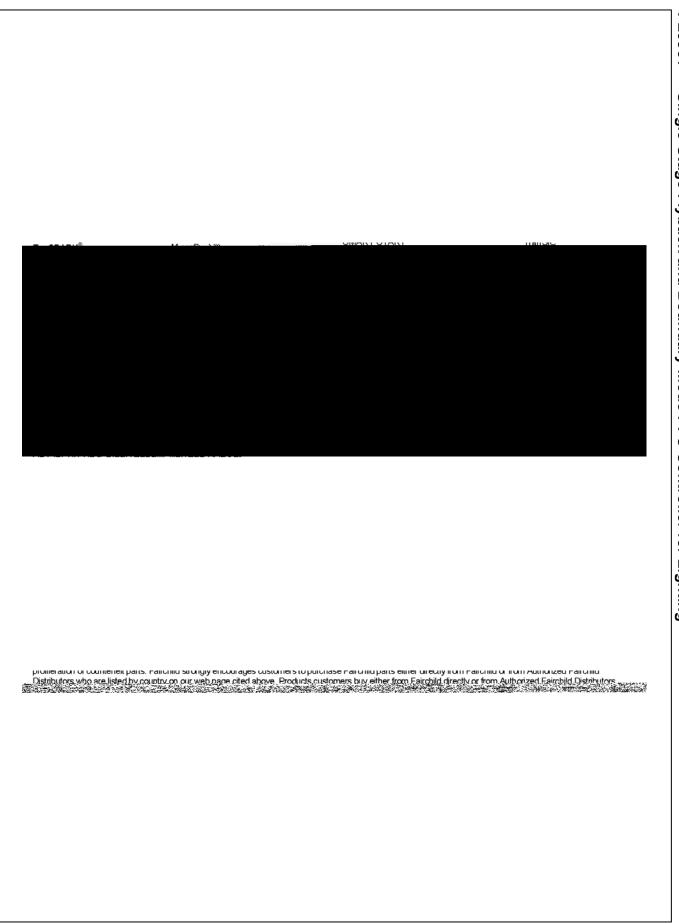


SEE DETAIL A



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