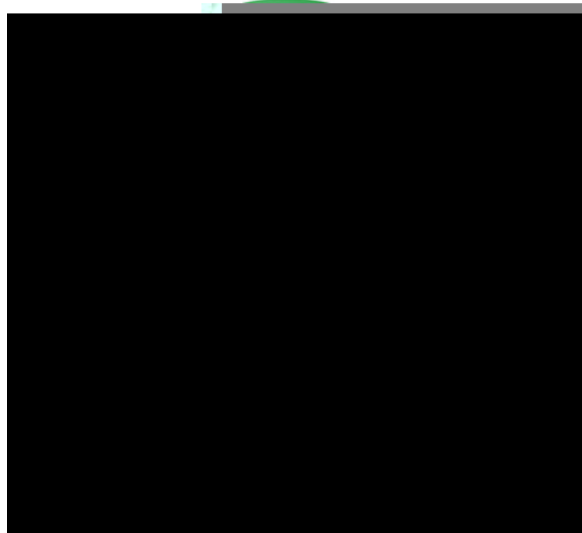




Is Now Part of



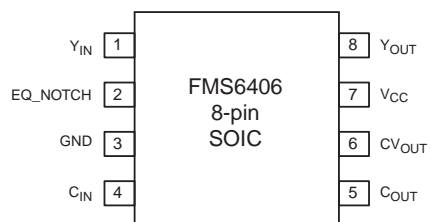
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www.onsemi.com**

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.



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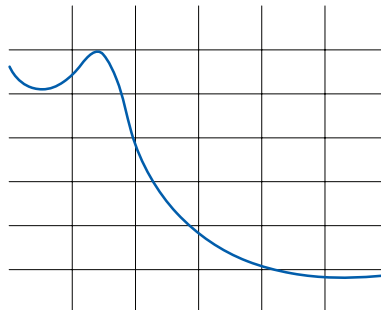
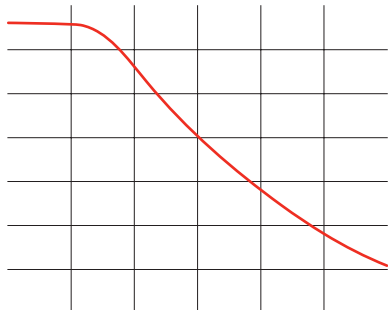
FMS6406 8-pin SOIC

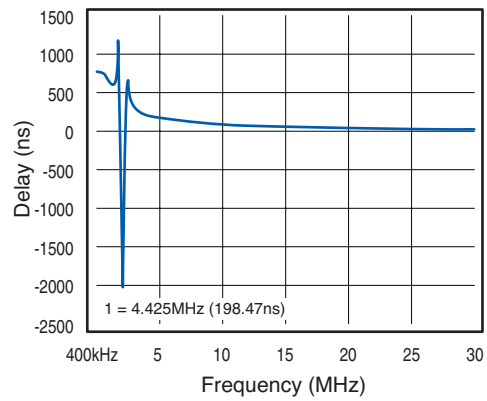
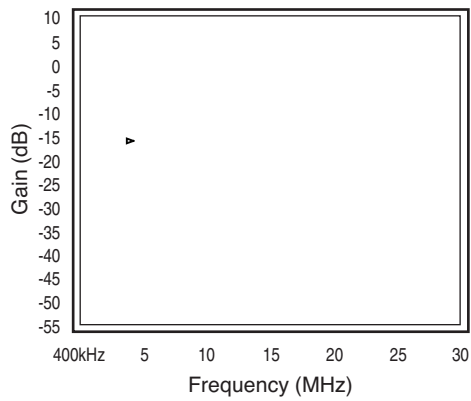


Pin Assignments

Typical Performance Characteristics

$T_c = 25^\circ\text{C}$, $V_i = 1V_{pp}$, $V_{CC} = 5V$, all inputs AC-coupled with $0.1\mu\text{F}$, all outputs are AC-coupled with $220\mu\text{F}$ into $150\ \Omega$, referenced to 400kHz ; unless otherwise noted.





Typical Performance Characteristics

$T_C = 25^\circ\text{C}$, $V_i = 1V_{pp}$, $V_{CC} = 5V$, $HD/N_SD = 0$, $R_{SOURCE} = 37.5 \Omega$, all inputs AC-coupled with $0.1 \mu\text{F}$, all outputs are AC-coupled with $220 \mu\text{F}$ into 150Ω , referenced to 400kHz ; unless otherwise noted.

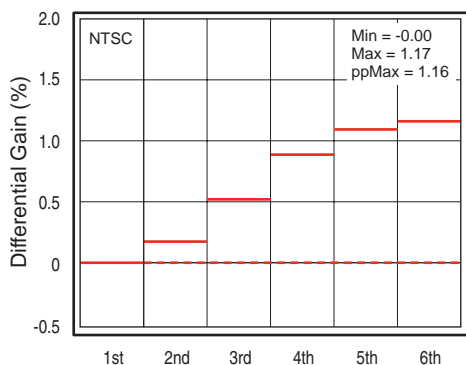


Figure 13. Differential Gain, V_{OUT}

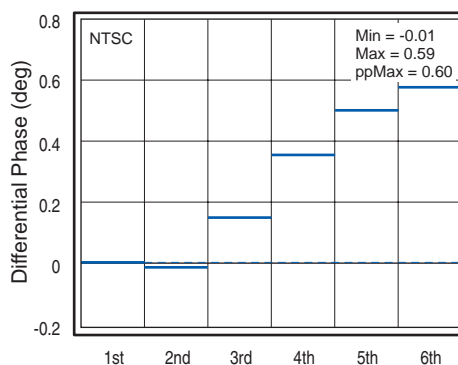


Figure 14. Differential Phase, V_{OUT}

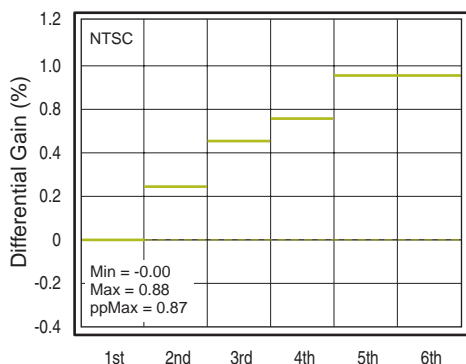


Figure 15. Differential Gain, C_{OUT}

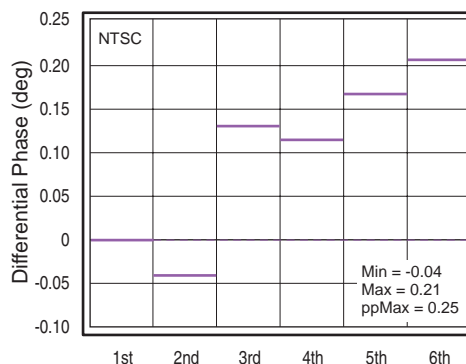


Figure 16. Differential Phase, C_{OUT}

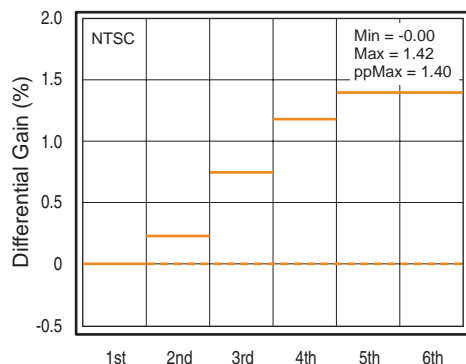


Figure 17. Differential Gain, CV_{OUT}

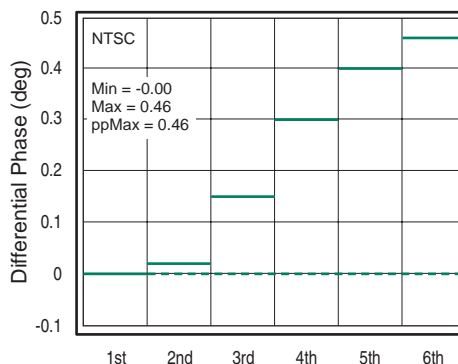
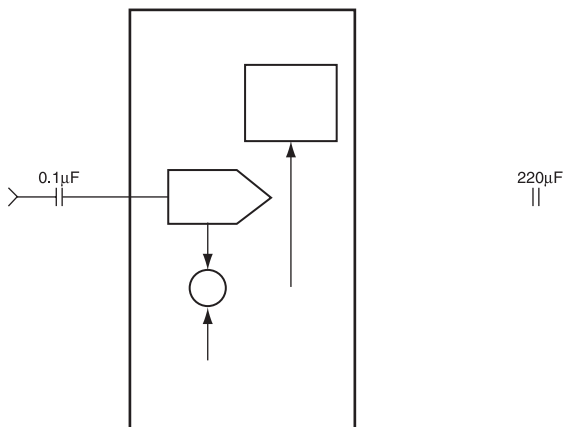


Figure 18. Differential Phase, CV_{OUT}

Typical Application Diagrams



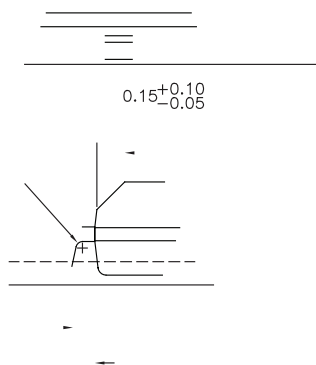
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Figure 22. AC-Coupled Application Diagram

Figure 23. DC-Coupled Application Diagram

Mechanical Dimensions

8-Lead Outline Package (SOIC)



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