

MARKING DIAGRAM

Features

Switch Type	DPDT (2x)
Switch Type	
Input Type	Data / Audio Switch
Input Signal Range	0 to V _{CC}
V _{CC}	1.65 to 4.45 V
R _{ON}	2.5 Ω at 2.7 V
R _{FLAT}	0.8 Ω at 2.7 V
ESD	8 kV HBM
Bandwidth	245 MHz
C _{ON} at 240 MHz	16 pF
C _{OFF} at 240 MHz	6.0 pF
Features	Low I _{CTT}
Package	16-Lead UMLP 1.80 x 2.60 x 0.55 mm, 0.40 mm pitch
Top Mark	KA
Ordering Information	FSA2466UMX

Applications

- MP3 Portable Media Players
- Cellular Phones, Smartphones



FSA2466

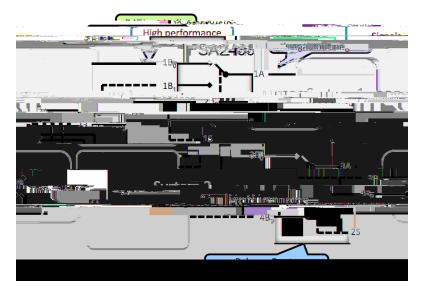


Figure 1. Typical Mobile Phone Application

ORDERING INFORMATION

Part Number	Top Mark	Operating Temperature Range	Package	Shipping [†]
FSA2466UMX	KA	−40 to 85°C	16-Lead, Quad, Ultrathin Molded Leadless Package (UMLP), 1.8 x 2.6 mm	5000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Pin Configuration

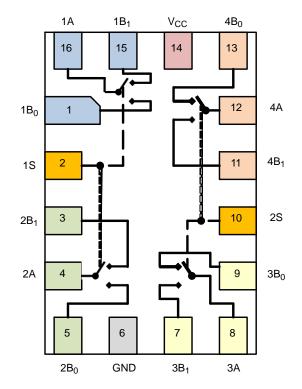


Figure 2. FSA2466UMX (Top View)

PIN DESCRIPTIONS

Pin #	Name	Туре	Description						
1	1 B 0	I/O	Data / Audio Port						
2			Original for Data & Original Data 4.9.0		$1B_0 = 1A \& 2B_0 = 2A$				
2	2 1S	Input	Control Input for Data & Common Ports 1 & 2	1	1B1 = 1A & 2B1 = 2A				
3	2B1	I/O	Data / Audio Port		•				
4	2A	I/O	-						

ABSOLUTE MAXIMUM RATINGS

Symbol	Parame	Min	Мах	Unit	
V _{CC}	Supply Voltage		-0.50	5.25	V
VS	Switch Voltage	Switch Voltage			
V _{IN}	Input Voltage	-0.5	5.0	V	
I _{IK}	Input Diode Current	-50		mA	
I _{SW}	Switch Current		350	mA	
I _{SWPEAK}	Peak Switch Current (Pulsed at 1 ms Durat		500	mA	
T _{STG}	Storage Temperature Range	-65	+150	°C	
ΤJ	Junction Temperature		+150	°C	
ΤL	Lead Temperature, Soldering 10 seconds		+260	°C	
ESD	Human Body Model, JESD22-A114	I/O to GND		8	kV
		Power to GND		8	
		All Other Pins		8	1

DC ELECTRICAL CHARACTERISTICS (Typical values are at T_A = 25°C unless otherwise specified.)

Symbol		Condition		ו	Γ _A = +25°	C	T _A = -40		
	Parameter		V _{CC} (V)	Min.	Тур.	Max.	Min.	Max.	Unit
VIH	Input Voltage High		4.30				1.4		V
			2.70 to 3.60				1.3		
			2.30 to 2.70				1.1		1
			1.65 to 1.95				0.9		
V _{IL}	Input Voltage Low		4.30					0.7	V
			2.70 to 3.60					0.5	1
			2.30 to 2.70					0.4	
			1.65 to 1.95					0.4	
I _{IN}	Control Input Leakage	$V_{IN} = 0 V \text{ to } V_{CC}$	1.65 to 4.30				-0.5	0.5	μΑ
I _{NO(OFF)} I _{NC(OFF)}	Off Leakage Current of Port nB0 and nB1	nA=0.3 V, V _{CC} -0.3V	3 V, V _{CC} -0.3V 1.95 to 4.30	-10		10	-50	50	nA
NC(OFF)		nB0 or nB1=0.3 V, V _{CC} –0.3V or Floating							
I _{A(ON)}	On Leakage Current of Port A	nA = 0.3 V, V _{CC} -0.3V	1.95 to 4.30	-10		10	-50	50	nA
ĺ		nB ₀ or nB ₁ = 0.3 V, V _{CC} -0.3V or Floating							
R _{ON}	Switch On Resistance (Note 3)	I _{OUT} =100 mA	4.30		•	•	•		
		I _{QUT} =100 mA, nB ₀ or			_				
		nB1 CC	2.30						

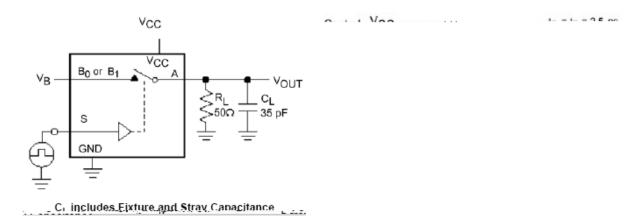
AC ELECTRICAL CHARACTERISTICS

(Typical values are are at $T_A = 25^{\circ}C$ unless otherwise specified.)

				T _A = +25°C		T _A = −40 to +85°C				
Symbol	Parameter	Condition	V _{CC}	Min.	Тур.	Max.	Min.	Max.	Unit	Figure
0.1	nB ₀ or nB1=1.5 V R _L =50 Ω , C _L =35 pF	3.6 to 4.3			50		60	ns	Figure 3	
		2.7 to 3.6			65		75			
			2.3 to 2.7			80		90		
t _{OFF}		nB₀ or nB1=1.5 V R _I =50 Ω, C _I =35 pF	3.6 to 4.3			32		40	ns	Figure 3
		KL=30 32, OL=35 PF	2.7 to 3.6			42		50		
			2.3 to 2.7			r	Ω,	-		

FSA2466

AC Loadings and Waveforms







V_{GC.}

Figure 4. Break–Before–Make Timing

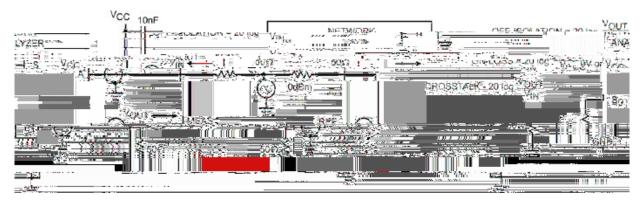


Figure 5. Off Isolation and Crosstalk

AMOUT

AC Loadings and Waveforms (Continued)

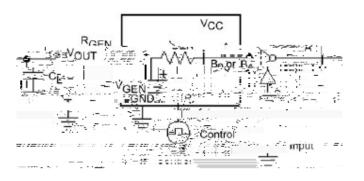


Figure 6. Charge Injection

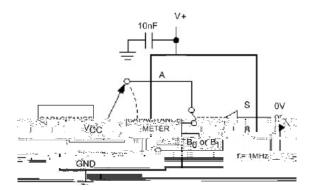


Figure 7. On / Off Capacitance Measurement Setup



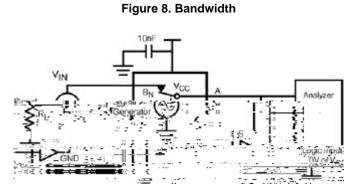


Figure 9. Harmonic Distortion



UQFN16 1.80x2.60x0.50, 0.40P CASE 523BF ISSUE A

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<u>D.45</u> L4 0.45 0.50 0.55 JLERANCES FOR FEATURE C Τſ

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