

MARKING DIAGRAM

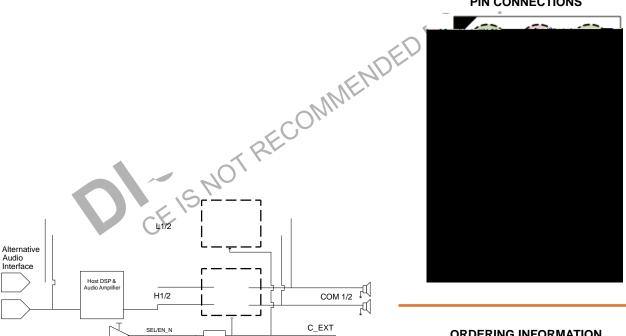
VTZZ AYYWW

VT = Specific Device Code ZZ = 2 ïdigit Lot Run Code

= Assembly Location Code YY = 2 ïdigit Year Code

WW = Weekly Date Code (1 to 52)

PIN CONNECTIONS



ORDERING INFORMATION

See detailed ordering, marking and shipping information on page 7 of this data sheet.

This document contains information on a product under development. ON Semiconductor reserves the right to change or discontinue this product without notice.

GPIO

VDD can be connected to supply or GPIO output



