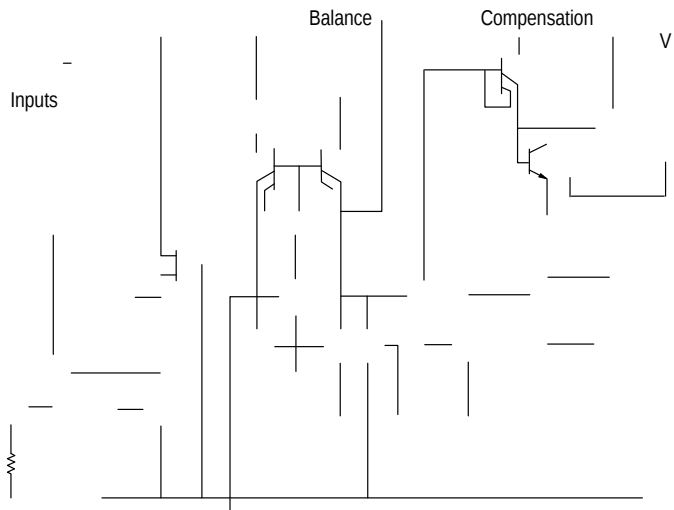




# LM301A, LM201A, LM201AV



# LM301A, LM201A, LM201AV

## MAXIMUM RATINGS

Rating	Symbol	Value			Unit
		LM201A	LM201AV	LM301A	
		±	±	±	
		←----- ± -----→			
		←----- ± -----→			
		←----- -----→			
--	°				°
		-	-		°
		←----- - -----→			°

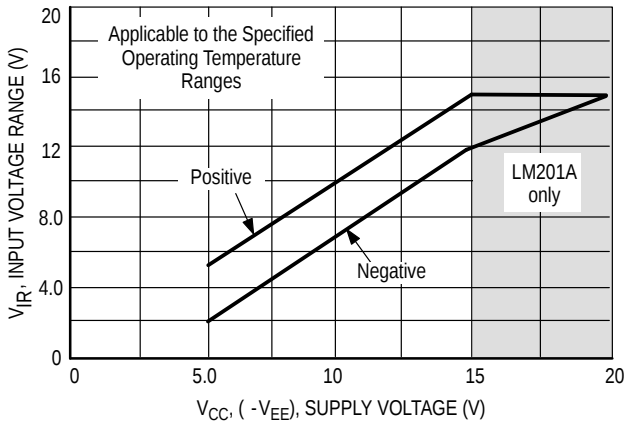
## ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	LM201A / LM201AV			LM301A			Unit
		Min	Typ	Max	Min	Typ	Max	
≤ Ω		-			-			
		-			-			
		-			-			
				-			-	Ω
±		-			-			
±		-	-	-	-	-	-	
± ± Ω				-			-	

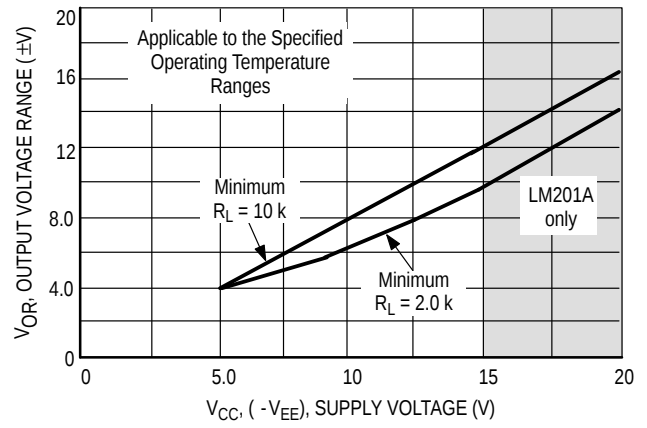
The following specifications apply over the operating temperature range.

≤ Ω		-	-		-	-		
		-	-		-	-		
≤ ≤	Δ Δ	-			-			μ °
° ≤ ≤	Δ Δ	-			-			°
≤ ≤ °		-			-			
		-	-		-	-		
± ± Ω			-	-		-	-	
±		-	-	-	-	-	-	
±		-	-	-	-	-	-	
≤ Ω				-			-	
≤ Ω				-			-	
± ± Ω, Ω)		±	±	-	±	±	-	
		±	±	-	±	±	-	
±		-			-	-	-	

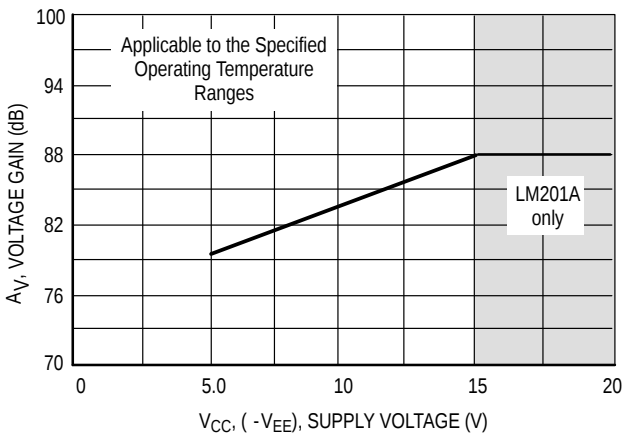
# LM301A, LM201A, LM201AV



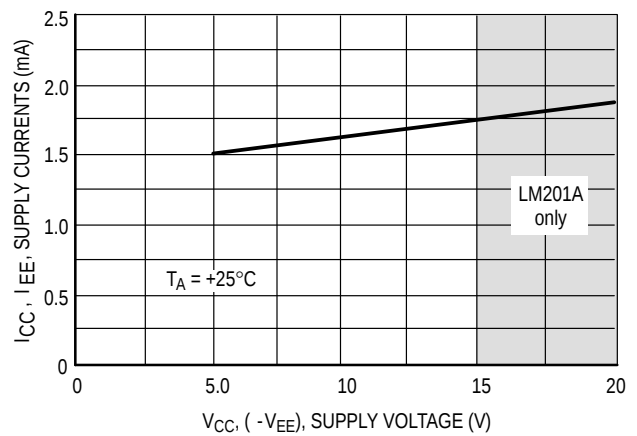
**Figure 4. Minimum Input Voltage Range**



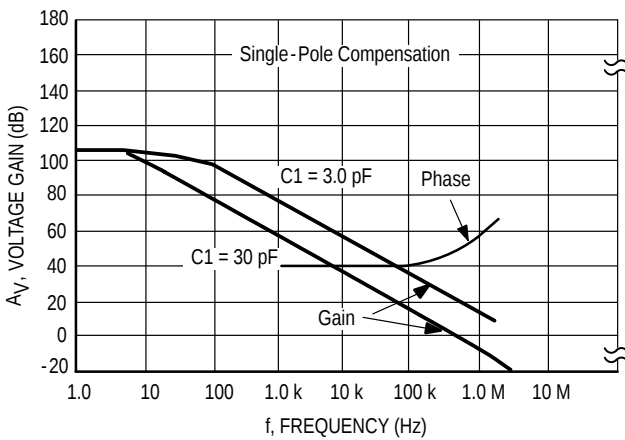
**Figure 5. Minimum Output Voltage Swing**



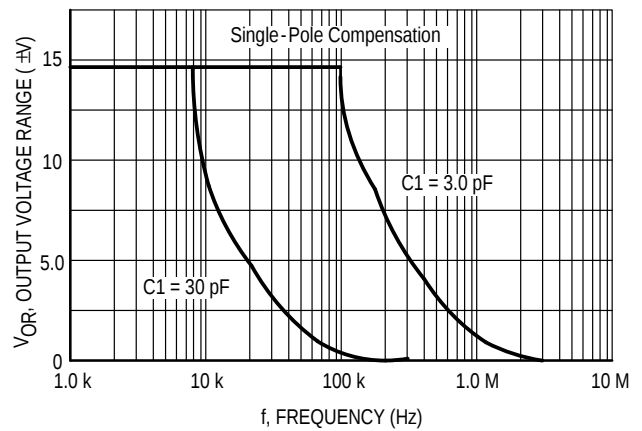
**Figure 6. Minimum Voltage Gain**



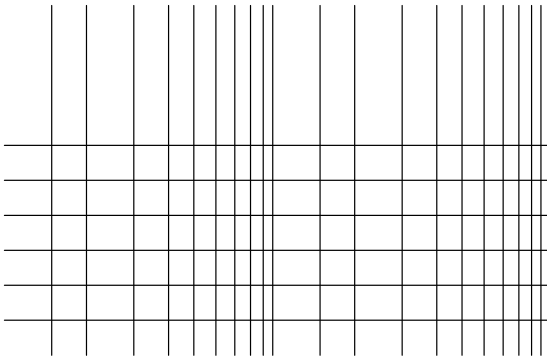
**Figure 7. Typical Supply Currents**



**Figure 8. Open Loop Frequency Response**

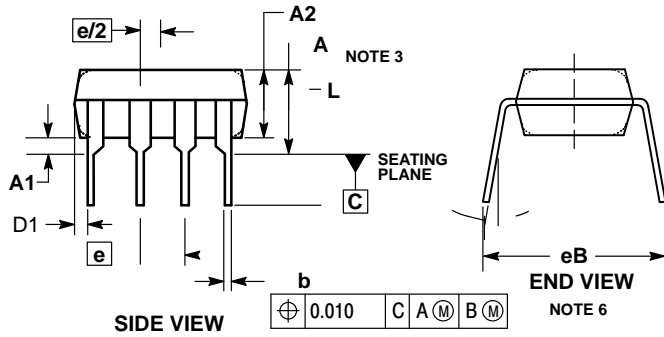
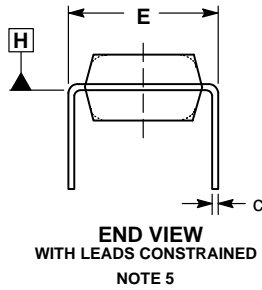
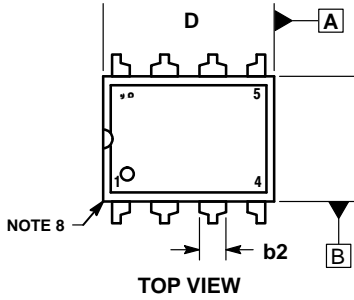


**Figure 9. Large Signal Frequency Response**



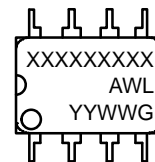
**PDIP 8**  
CASE 626-05  
ISSUE P

DATE 22 APR 2015



DIM	INCHES			
	MIN	MAX		
A	-----	0.210		
A1	0.015	-----		
A2	0.115	0.195	2.92	4.95
b	0.014	0.022		
C	0.008	0.014		
D	0.355	0.400		
D1	0.005	-----		
E	0.300	0.325		
e	0.100 BSC			
L	0.115	0.150	2.92	3.81

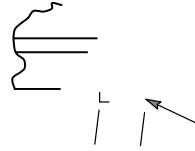
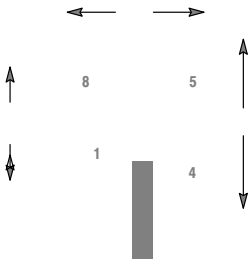
**GENERIC  
MARKING DIAGRAM\***



- XXXX = Specific Device Code
- A = Assembly Location
- WL = Wafer Lot
- YY = Year
- WW = Work Week
- G = Pb-Free Package

**SOIC 8 NB**  
CASE 751-07  
ISSUE AK

DATE 16 FEB 2011



SEATING  
PLANE







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