

MC100EP58

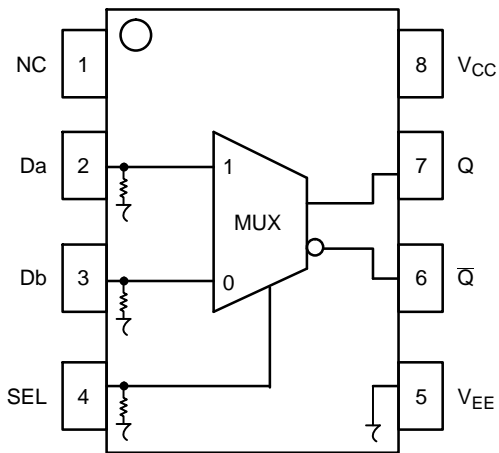


Figure 1. 8-Lead Pinout (Top View) and Logic Diagram

Table 1. PIN DESCRIPTION

| PIN | FUNCTION |
|-----------------|-------------------|
| Da*, Db* | ECL Data Inputs |
| SEL* | ECL Select Inputs |
| Q, Q̄ | ECL Data Outputs |
| V _{CC} | Positive Supply |
| V _{EE} | Negative Supply |
| NC | No Connect |

* Pins will default LOW when left open.

Table 2. TRUTH TABLE

| SEL | Data |
|-----|------|
| H | a |
| L | b |

Table 3. ATTRIBUTES

| Characteristics | Value |
|---|-----------------------------|
| Internal Input Pulldown Resistor | 75 kΩ |
| Internal Input Pullup Resistor | N/A |
| ESD Protection Human Body Model Machine Model Charged Device Model | > 4 kV > 200 V > 2 kV |
| Moisture Sensitivity, Indefinite Time Out of Drypack (Note 1) | Pb-Free Pkg |
| TSSOP-8 | Level 3 |
| Flammability Rating Oxygen Index: 28 to 34 | UL 94 V-0 @ 0.125 in |
| Transistor Count | 41 Devices |
| Meets or exceeds JEDEC Spec EIA/JESD78 IC Latchup Test | |

1. For additional information, see Application Note [AND8003/D](#).

MC100EP58

Table 4. MAXIMUM RATINGS

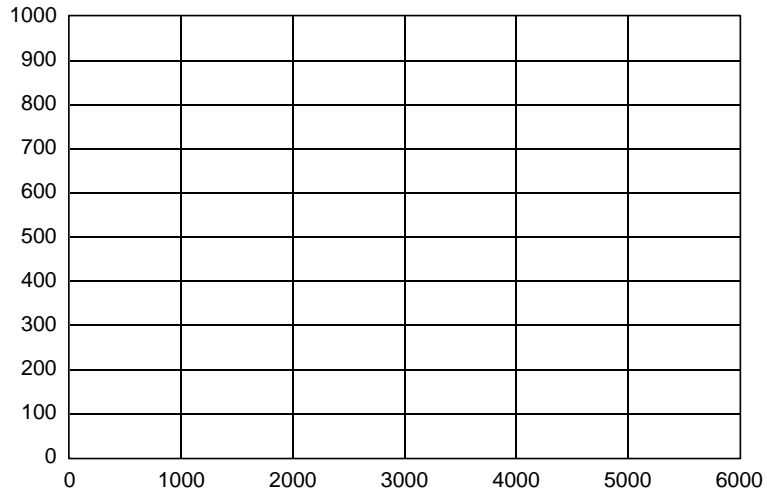
| Symbol | Parameter | Condition 1 | Condition 2 | Rating | Unit |
|----------|------------------------|-----------------------|-------------|--------|------|
| V_{CC} | PECL Mode Power Supply | $V_{EE} = 0\text{ V}$ | | 6 | V |
| V_{EE} | NECL Mode Power Supply | $V_{CC} = 0\text{ V}$ | | | |

MC100EP58

Table 6. 100EP DC CHARACTERISTICS, PECL ($V_{CC} = 5.0\text{ V}$, $V_{EE} = 0\text{ V}$ (Note 1))

| Symbol | Characteristic | 40°C | | | 25°C | | | 85°C | | | Unit |
|----------|------------------------------|------|-----|-----|------|-----|-----|------|-----|-----|------|
| | | Min | Typ | Max | Min | Typ | Max | Min | Typ | Max | |
| I_{EE} | Power Supply Current | 20 | 28 | 37 | 20 | 31 | 39 | 25 | 33 | 42 | mA |
| V_{OH} | Output HIGH Voltage (Note 2) | 3855 | | | | | | | | | |

MC100EP58



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