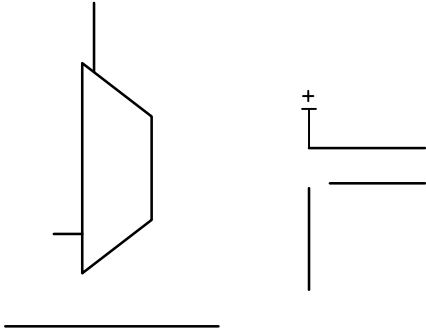


MC100EP809



**MC100EP809**

# MC100EP809

**Table 8. AC CHARACTERISTICS**  $V_{CCI} = 3.0\text{ V to }3.6\text{ V}$ ;  $V_{CCO} = 1.6\text{ V to }2.0\text{ V}$ ,  $GND = 0\text{ V}$  (Note 5)

Symbol	Characteristic	0 C			25 C			85 C			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
$V_{Opp}$	Differential Output Voltage (Figure 3)										
	$f_{out} < 100\text{ MHz}$	600	850		600	850		600	850		mV
	$f_{out} < 500\text{ MHz}$	600	750		600	750		600	750		mV
	$f_{out} < 750\text{ MHz}$	450	575		450	575		450	575		mV
$t_{PLH}$ $t_{PHL}$	Propagation Delay (Differential Configuration)										
	LVPECL_CLK to Q	680	800	930	700	820	950	780	920	1070	ps
	HSTL_CLK to Q	690	830	990	700	850	1000	790	950	1110	ps

t

# MC100EP809

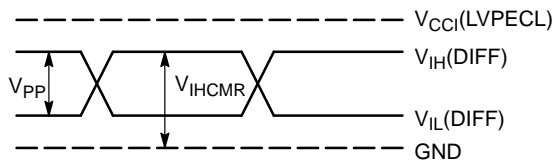


Figure 4. LVPECL Differential Input Levels

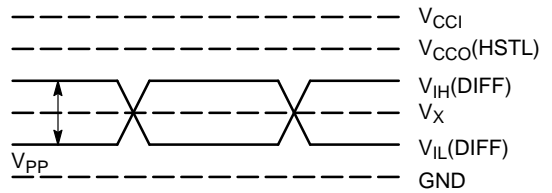


Figure 5. HSTL Differential Input Levels

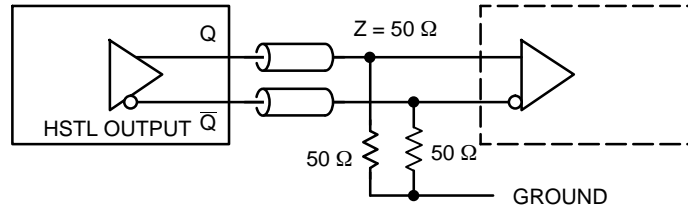
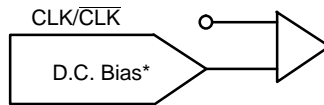
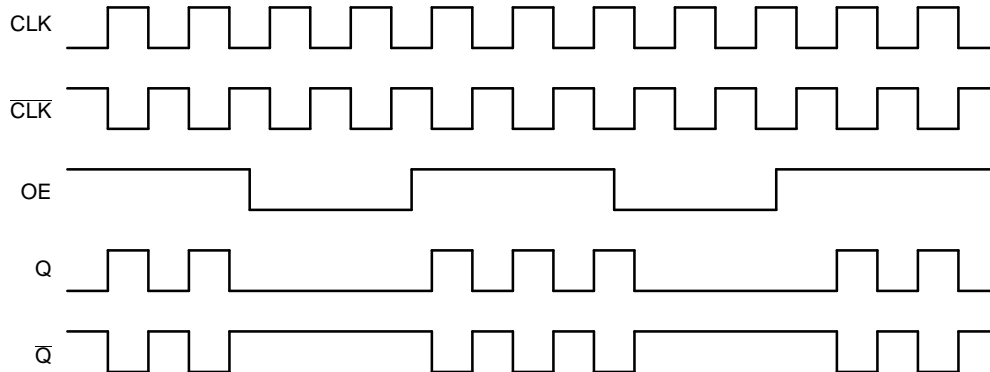


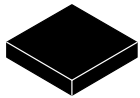
Figure 6. HSTL Output Termination and AC Test Reference



\*Must be CLK/CLK common mode voltage:  $((V_{IH} + V_{IL})/2)$ .

Figure 7. Single-Ended CLK/CLK Input Configuration

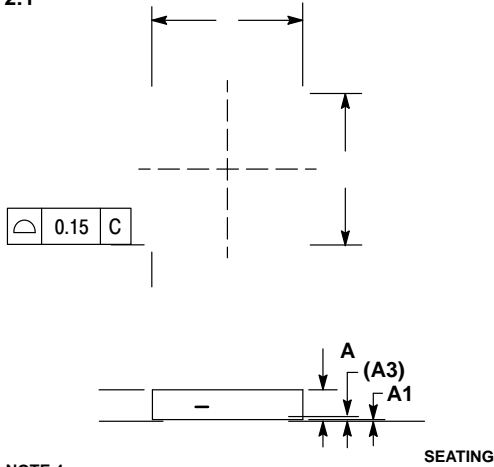




**QFN32 5x5, 0.5P**  
CASE 488AM  
ISSUE A

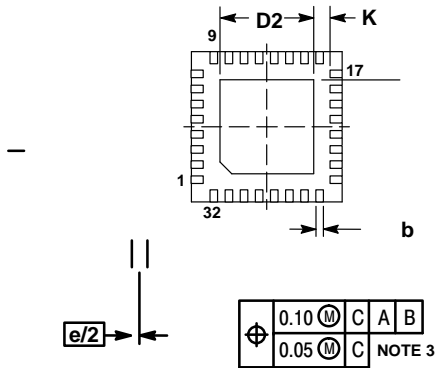
DATE 23 OCT 2013

SCALE 2:1



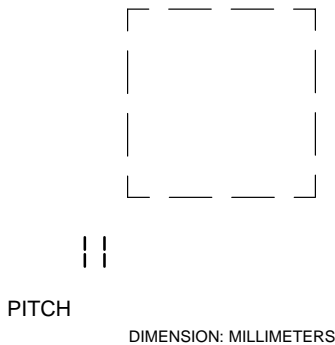
NOTE 4

	MAX
A1	0.80 1.00
A3	0.20 REF 0.05
b	0.18 0.30
D	5.00 BSC
D2	2.95 3.25
E	5.00 BSC
E2	2.95 3.25
e	0.50 BSC
K	0.20
L	0.30 0.50
L1	0.15



XXXXXXXXX  
XXXXXXXXX  
AWLYYWW#  
#Free indicator, "G" or

**RECOMMENDED**



PITCH

DIMENSION: MILLIMETERS

<b>DOCUMENT NUMBER:</b>	<b>98AON20032D</b>	

**onsemi**, **onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi**

---

---