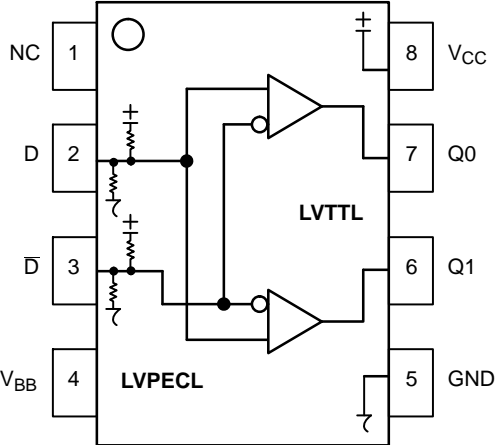


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(Top View)

Figure 1. 8-Lead Pinout and Logic Diagram

Table 1. PIN DESCRIPTION

Pin	Function
Q0, Q1	LVTTTL Outputs
D0**, D1**	Differential LVPECL Inputs Pair
V _{CC}	Positive Supply
V _{BB}	Output Reference Voltage
GND	Ground
NC	No Connect
EP	(DFN8 only) Thermal exposed pad must be connected to a sufficient thermal conduit. Electric-

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Table 3. MAXIMUM RATINGS

Symbol	Parameter	Condition 1	Condition 2	Rating
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Table 5. TTL OUTPUT DC CHARACTERISTICS ($V_{CC} = 3.3\text{ V}$; $GND = 0.0\text{ V}$; $T_A = 40\text{ C to }85\text{ C}$)

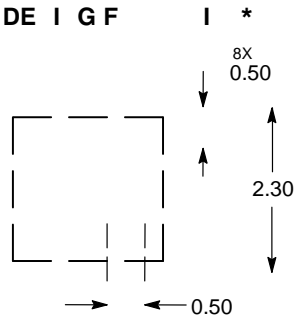
Symbol	Characteristic	Condition	Min	Typ	Max	Unit
V_{OH}	Output HIGH Voltage	$I_{OH} = 3.0\text{ mA}$	2.4			V
V_{OL}	Output LOW Voltage	$I_{OL} = 24\text{ mA}$			0.5	V
I_{CCH}	Power Supply Current		10	25	35	mA
I_{CCL}	Power Supply Current		15	34	40	mA
I_{OS}	Output Short Circuit Current		50		150	mA

NOTE: Device will meet the specifications after thermal equilibrium has been established when mounted in a test socket or printed circuit

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DATE 04 MAY 2016

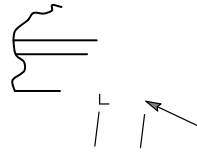
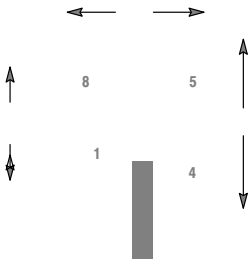


DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the [m\] □ Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.](#)

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