onse i

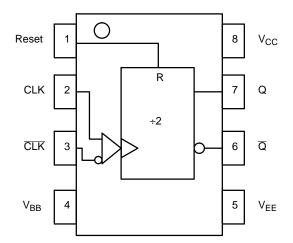


Figure 1. Logic Diagram and Pinout Assessment

Table 2. MAXIMUM RATINGS

Table 1. PIN DESCRIPTION

Pin	Function		
CLK*, CLK**	ECL Differential Clock Inputs		
Q, <u>Q</u>	ECL Differential Data ÷2 Outputs		
Reset*	ECL Asynch Reset		
V _{BB}	Reference Voltage Output		
V _{CC}	Positive Supply		
V _{EE}	Negative Supply		

*Pin will default low when left open, per internal 75 K pull-down to

 $V_{EE}.$ ** Pin will default to $V_{CC}/2$ when left open per internal 75 K Ω pull-down to V_{EE} and 75 K Ω pull-up to $V_{CC}.$

Symbol	Parameter	Condition 1	Condition 2	Rating	Unit
V _{CC}					

Table 3. LVPECL DC CHARACTERISTICS (V _{CC} = 3.3 V; V _{EE} = 0.0 V (Note 1))
--

		-40°C	25°C	85°C	
Symbol	Characteristic				Unit

		-40°C			25°C	85°C		1	
Symbol	Characteristic	Min	Тур	Мах	Min	ТурМах		Unit	

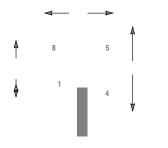
Resource Reference of Application Notes

AN1405/D	ECL Clock Distribution Techniques
AN1406/D	Designing with PECL (ECL at +5.0 V)
AN1503/D	ECLinPS I/O SPiCE Modeling Kit
AN1504/D	Metastability and the ECLinPS Family
AN1568/D	Interfacing Between LVDS and ECL
AN1672/D	The ECL Translator Guide
AND8001/D	Odd Number Counters Design
AND8002/D	Marking and Date Codes
AND8020/D	Termination of ECL Logic Devices
AND8066/D	Interfacing with ECLinPS
AND8090/D	AC Characteristics of ECL Devices

ECLinPS is a trademark of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries.



DATE 16 FEB 2011



SEATING PLANE



TSSOP 8 3.00x3.00x0.95 CASE 948R-02 ISSUE A

DATE 07 APR 2000



	MILLIN	IETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	2:0	3.10	0.114	0.122	
В					

onsemi, , and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or incruit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi