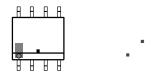
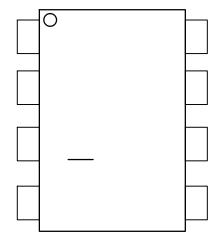




#### **MARKING DIAGRAM**



24 mA LVTTL Outputs
Flow Through Pinouts
Internal Pulldown and Pullup Resistors
These Devices are Pb-Free, Halogen Free and are RoHS Compliant



**Table 3. MAXIMUM RATINGS** 

Symbol	Parameter	Condition 1	Condition 2	Rating	Unit
				-	
				_	
θ			-		
θ			_		
θ			-		
θ			_		
θ			_		
θ			_		

\_

**Table 4. LVPECL INPUT DC CHARACTERISTICS** 

		-40 C		25 C		85 C					
Symbol	Characteristic	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit
	J									]	

### Table 5. LVTTL OUTPUT DC CHARACTERISTICS

		-40 C	25 C
Symbol	Characteristic		

### **Resource Reference of Application Notes**

AN1405/D

AN1406/D

AN1503/D - ™

AN1504/D -

AN1568/D -

AN1672/D

AND8001/D -

AND8002/D -

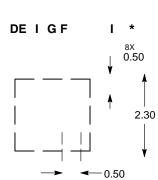
AND8020/D -

AND8066/D -

AND8090/D -

onsemi

1 CA E 4:1 DATE 04 MAY 2016

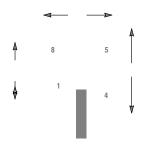


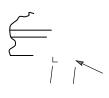
DIMENSIONS: MILLIMETERS

<sup>\*</sup>For additional information on our Pb–Free strategy and soldering details, please download the m Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## SOIC 8 NB CASE 751-07 ISSUE AK

DATE 16 FEB 2011





SEATING PLANE



#### TSSOP 8 3.00x3.00x0.95 CASE 948R-02

CASE 948R-02 ISSUE A

DATE 07 APR 2000





	MILLIN	IETERS	INCHES				
DIM	MIN	MAX	MIN	MAX			
Α	2.0	3.10	0.114	0.122			
В							

