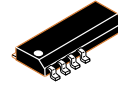


Dual 1-of-4 Decoder/Demultiplexer

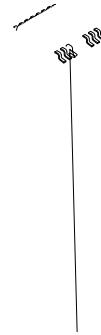
MC74AC139, MC74ACT139

The following figure shows the pinout for 16-Lead Packages Conductors (Top View)

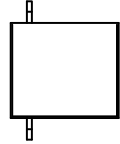
MARKING DIAGRAMS



SOIC-16
 D SUFFIX
 CASE 751B



TSSOP-16
 DT SUFFIX
 CASE 948F



ASSIGNMENT

ORDERING INFORMATION

PIN	FUNCTION

TRUTH TABLE

Inputs			Outputs			

MC74AC139, MC74ACT139

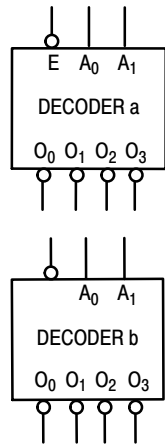
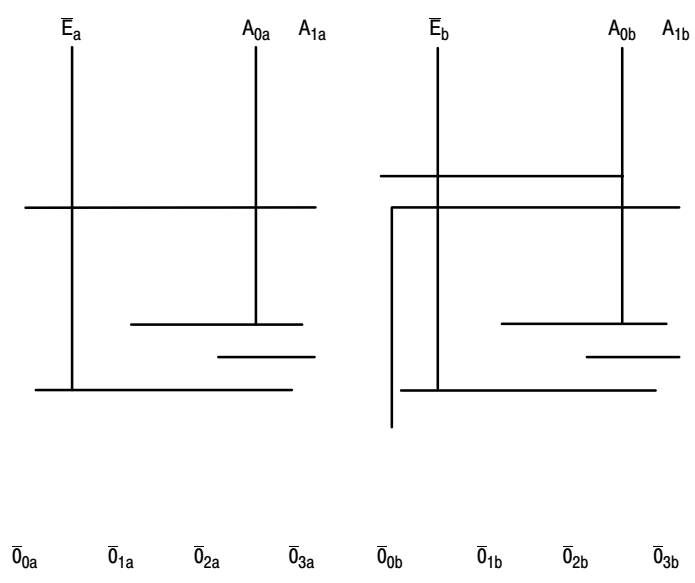


Figure 2. Logic Symbol



MC74AC139, MC74ACT139

MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
		-	
		- ≤ ≤ +	
		- ≤ ≤ +	
		±	

MC74AC139, MC74ACT139

MC74AC139, MC74ACT139

ORDERING INFORMATION

Device Order Number	Marking	Package	Shipping
		-	
		-	
		-	
		-	
		-	
		-	
		-	
		-	
		-	

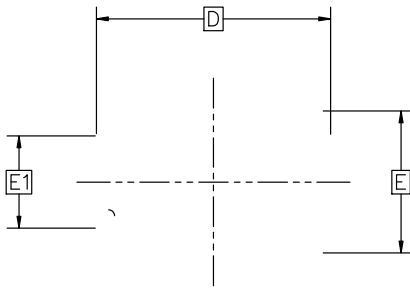


SOIC-16 9.90x3.90x1.37 1.27P
CASE 751B
ISSUE M

DATE 18 OCT 2024

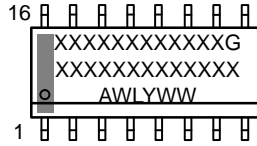
3. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.1^{mm}

b DIMENSION AT MAXIMUM MATE nm TOTAL IN EXCESS OF THE



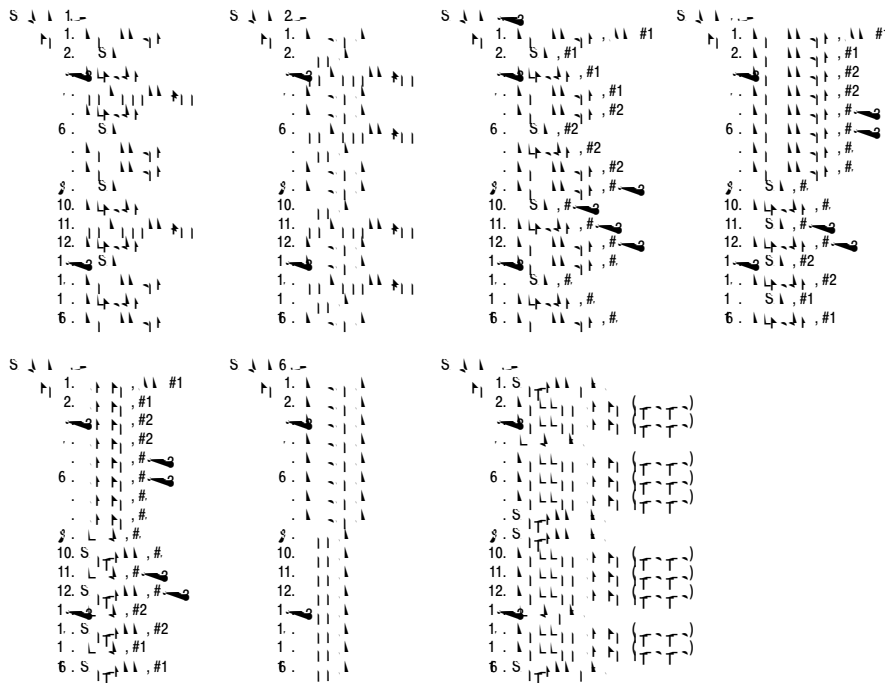
TOP VIEW

**GENERIC
MARKING DIAGRAM***



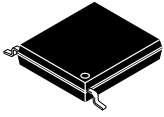
XXXXX = Specific Device Code
A = Assembly Location
WL = Wafer Lot
Y = Year
WW = Work Week
G = Pb Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



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DESCRIPTION:	SOIC-16 9.90X3.90X1.37 1.27P	PAGE 2 OF 2

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SCALE 2:1

TSSOP-16 WB
CASE 948F
ISSUE B

DATE 19 OCT 2006

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