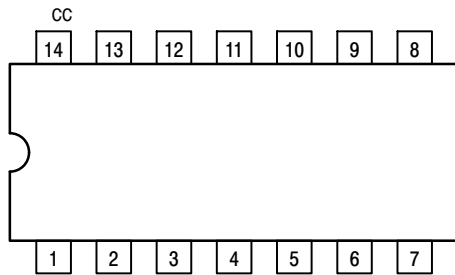


2-I **O_Q** **G**
H - **f** **-G**
CMO

MC74AC32, MC74AC 32

Features

- Outputs Source/Sink 24 mA
- 'ACT32 Has TTL Compatible Inputs
- These Devices are Pb-Free and are RoHS Compliant



MC74AC32, MC74ACT32

MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
		-	
		- ≤ ≤ +	
		- ≤ ≤ +	
		±	
		±	
		±	
		±	
		±	
		- +	°
			°
		+	°
θ			°
	°		
		-	
		-	

MC74AC32, MC74ACT32

DC CHARACTERISTICS

Symbol	Parameter	V _{CC} (V)	74AC		Unit	Conditions
			74AC			
			T _A = +25°C	T _A = -40°C to +85°C		
Typ	Guaranteed Limits					

MC74AC32, MC74ACT32

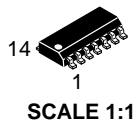
DC CHARACTERISTICS

Symbol	Parameter	V _{CC} (V)	74ACT		74ACT		Unit	Conditions
			T _A = +25°C		T _A = -40°C to +85°C			
			Typ	Guaranteed Limits				
							-	
							-	
							- μ	
			-				-	
			-				-	
							μ	

MC74AC32, MC74ACT32

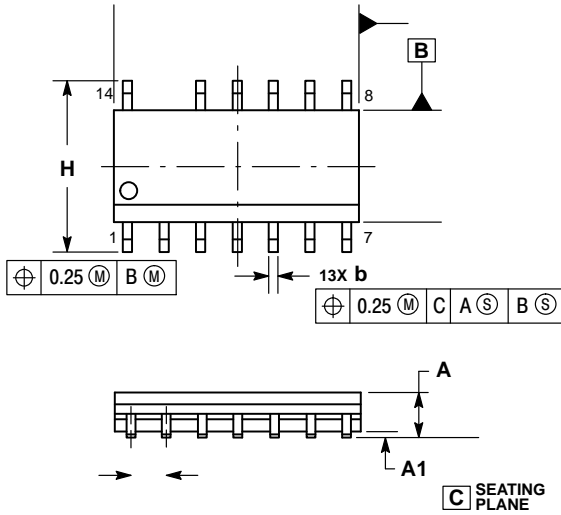
ORDERING INFORMATION

Device	Marking	Package	Shipping
		-	
		-	
		-	
		-	
		-	
		-	



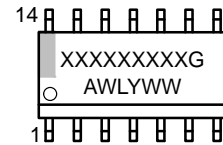
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- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE PROTRUSION SHALL BE 0.13 TOTAL IN EXCESS OF AT MAXIMUM MATERIAL CONDITION.
 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSIONS.
 5. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.

GENERIC MARKING DIAGRAM*



- XXXXXX = Specific Device Code
- A = Assembly Location
- WL = Wafer Lot
- Y = Year
- WW = Work Week
- G = Pb-Free Package

STYLES ON PAGE 2

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STYLE 7:
PIN 1. ANODE/CATHODE
2. COMMON ANODE
3. COMMON CATHODE
4. ANODE/CATHODE
5. ANODE/CATHODE

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