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V_{CC}	DC Supply Voltage	2.0	5.5	V
V_{IN}	DC Input Voltage (Note 5)	0	5.5	V
V_{OUT}	DC Output Voltage (Note 5)	0	V_{CC}	V
T_A	Operating Temperature	-40	+85	°C
t_r, t_f	Input Rise or Fall Rate	$V_{CC} = 3.0\text{ V to }3.6\text{ V}$	100	ns/V
		$V_{CC} = 4.5\text{ V to }5.5\text{ V}$	20	

V_{CC}	DC Supply Voltage	4.5	5.5	V
V_{IN}	DC Input Voltage (Note 5)	0	5.5	V
V_{OUT}	DC Output Voltage (Note 5)	Active Mode (High or Low State)	0	V_{CC}
		Tristate Mode	0	5.5
		Power-Off Mode ($V_{CC} = 0\text{ V}$)	0	5.5
T_A	Operating Temperature	-40	+85	°C
t_r, t_f	Input Rise or Fall Rate	$V_{CC} = 4.5\text{ V to }5.5\text{ V}$	0	20
				ns/V

Functional



50 pF, $V_{CC} = 5.0$ V)

	°		
	0.6	0.9	V
	-0.6	-0.9	V
e		3.5	V
e		1.5	V

	°		- °	
$V_{CC} = 3.3 \pm 0.3$ V $V_{CC} = 5.0 \pm 0.5$ V		5.0 5.0	5.0 5.0	ns
$V_{CC} = 3.3 \pm 0.3$ V $V_{CC} = 5.0 \pm 0.5$ V		4.0 4.0	4.0 4.0	ns
$V_{CC} = 3.3 \pm 0.3$ V $V_{CC} = 5.0 \pm 0.5$ V				

(MC74VHCT373A) ($C_L = 50 \text{ pF}$, $V_{CC} = 5.0\text{V}$)

		°		
V_{OLP}	Quiet Output Maximum Dynamic V_{OL}	1.2	1.6	V
V_{OLV}	Quiet Output Minimum Dynamic V_{OL}	-1.2	-1.6	V
V_{IHD}	Minimum High Level Dynamic Input Voltage		2.0	V
V_{ILD}	Maximum Low Level Dynamic Input Voltage		0.8	V

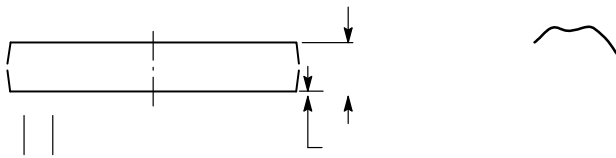
(MC74VHCT373A)

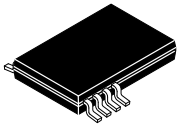
			°		-	°	
$t_{w(h)}$	Minimum Pulse Width, LE	$V_{CC} = 5.0 \pm 0.5 \text{ V}$		6.5	8.5		ns
t_{su}	Minimum Setup Time, D to LE	$V_{CC} = 5.0 \pm 0.5 \text{ V}$		1.5	1.5		ns
t_h	Minimum Hold Time, D to LE	$V_{CC} = 5.0 \pm 0.5 \text{ V}$		3.5	3.5		ns



SOIC-20 WB
CASE 751D-05
ISSUE H

DATE 22 APR 2015





SCALE 2:1

TSSOP-20 WB
CASE 948E
ISSUE D

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