3-to-8 Line Decoder

MC74VHCT138A

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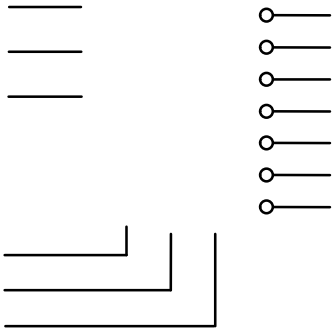
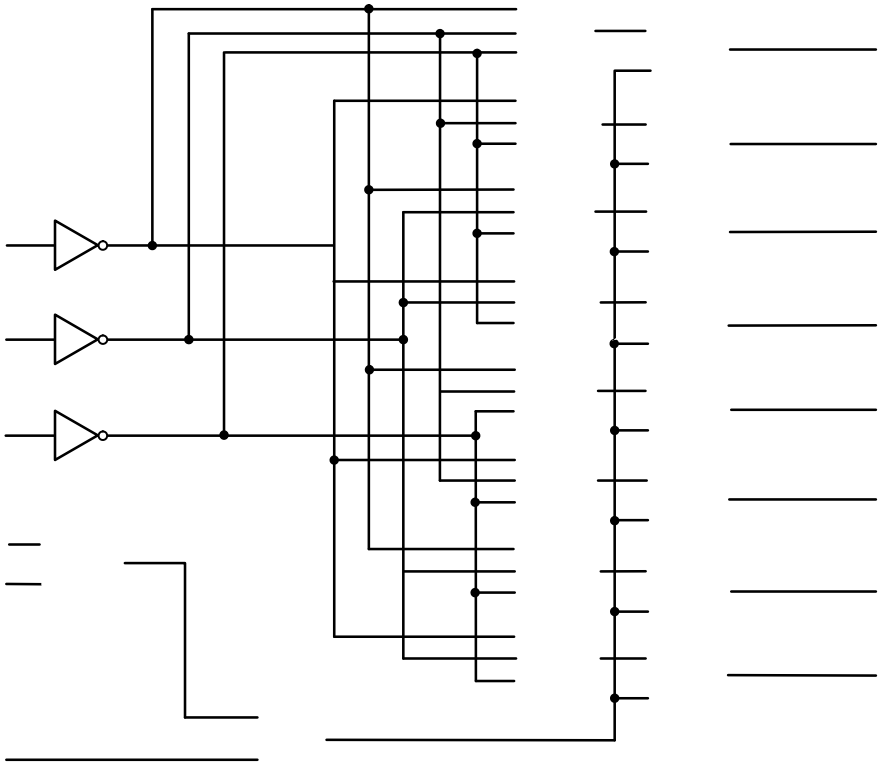
Features

, andp008 0 3 3 V(Features)Tj /TT3 1 Tf 12 0 0 13* -. 12h3 1 u111c.s.13.3 V(Fe2r1ePiu10 mdD Tc 0 Tw (: I70.2992 406.0 al



MC74VHCT138A



MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
		–	

SWITCHING WAVEFORMS

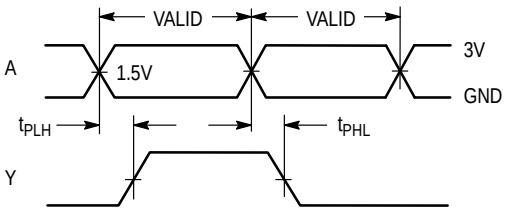


Figure 2.

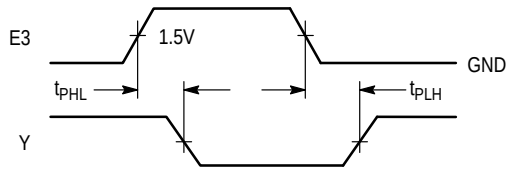


Figure 3.

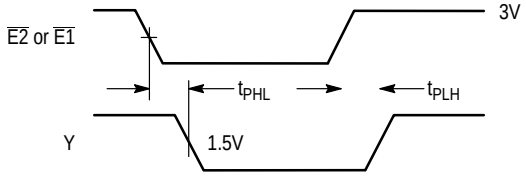


Figure 4.

Figure 5. Test Circuit

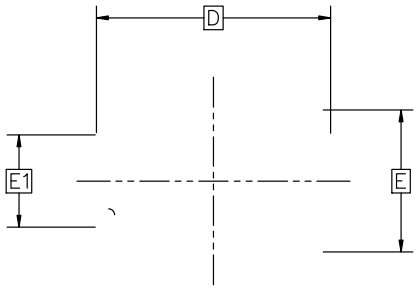
SOIC−16 9.90x3.90x1.37 1.27P
CASE 751B
ISSUE M

DATE 18 OCT 2024

3. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.1[illegible]

nm TOTAL IN EXCESS OF THE

b DIMENSION AT MAXIMUM MATE



TOP VIEW

SOIC−16 9.90x3.90x1.37 1.27P
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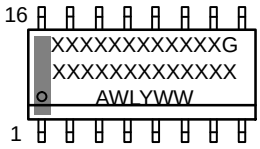
ISSUE M

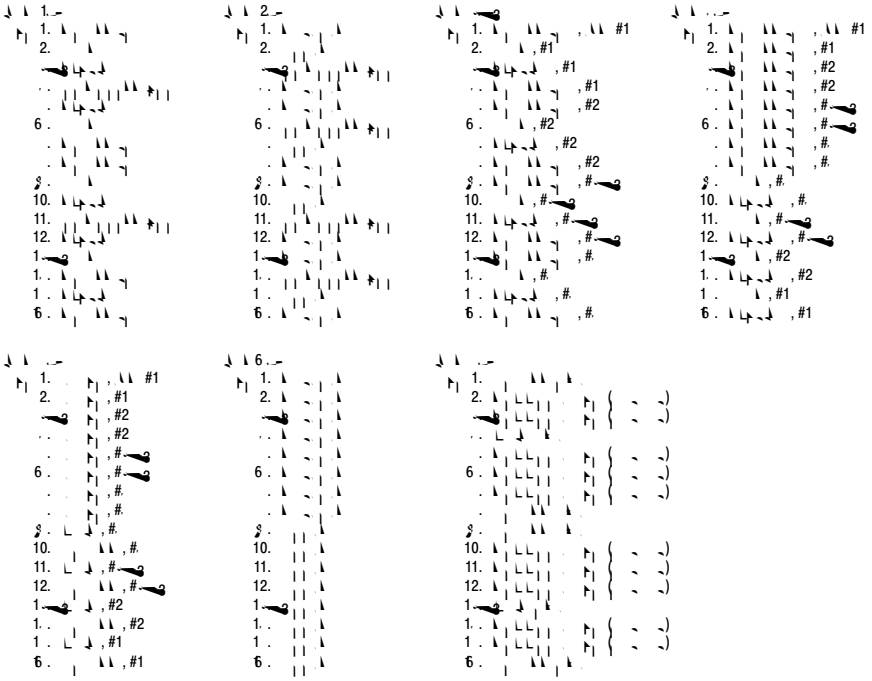
DATE 18 OCT 2024

GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code
A = Assembly Location
WL = Wafer Lot
Y = Year
WW = Work Week
G = Pb Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb Free indicator, “G” or microdot “▪”, may or may not be present. Some products may not follow the Generic Marking.



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DESCRIPTION:	**SOIC−16 9.90X3.90X1.37 1.27P**		**PAGE 2 OF 2**

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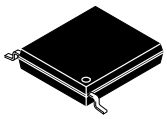

SCALE 2:1

TSSOP−16 WB
CASE 948F
ISSUE B

DATE 19 OCT 2006

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