(T_J = 25 C unless otherwise noted)

Collector–Emitter Voltage	V_{CEO}	260	Vdc
Collector–Base Voltage	V_{CBO}	260	Vdc
Emitter–Base Voltage	V_{EBO}	5.0	Vdc
Collector–Emitter Voltage – 1.5 V	V_{CEX}		
	I_C	15	Adc
Collector Current – Peak (Note 1)	I_{CM}	25	Adc
Base Current – Continuous	I_B	1.5	Adc
Total Power Dissipation @ T_C = 25 C Derate Above 25 C	P_D	200 1.43	Watts W/ C
Operating and Storage Junction Temperature Range	T_J, T_{stg}	– 65 to +150	C

Thermal Resistance, Junction–to–Case	R_{JC}	0.625	C/W

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Pulse Test: Pulse Width = 5 ms, Duty Cycle < 10%.

(T_C = 25 C unless otherwise noted)

Collector−Emitter Sustaining Voltage (I_C = 100 mAdc, I_B = 0)	$V_{CEO(sus)}$	260	−	Vdc
Collector Cutoff Current (V_{CB} = 260 Vdc, I_E = 0)	I_{CBO}	−	50	Adc
Emitter Cutoff Current (V_{EB} = 5 Vdc, I_C = 0)	I_{EBO}	−	5	Adc

Second Breakdown Collector with Base Forward Biased (V_{CE} = 50 Vdc, t = 1 s (non−repetitive) (V_{CE} = 100 Vdc, t = 1 s (non−repetitive)	$I_{S/b}$	4 1	− −	Adc

DC Current Gain (I_C = 500 mAdc, V_{CE} = 5 Vdc) (I_C = 1 Adc, V_{CE} = 5 Vdc) (I_C = 3 Adc, V_{CE} = 5 Vdc) (I_C = 5 Adc, V_{CE} = 5 Vdc) (I_C = 8 Adc, V_{CE} = 5 Vdc)	h_{FE}	 75 75 75 75 45	 150 150 150 150 −	
Collector−Emitter Saturation Voltage (I_C = 10 Adc, I_B = 1 Adc)	$V_{CE(sat)}$	−	3	Vdc

Current−Gain − Bandwidth Product (I_C = 1 Adc, V_{CE} = 5 Vdc, f_{test} = 1 MHz)	f_T	30	−	MHz
Output Capacitance (V_{CB} = 10 Vdc, I_E = 0, f_{test} = 1 MHz)	C_{ob}	−	600	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

XXXXXX = Sp
A = Lo
YY = Ye
WW = W

*This information is
device data sheet
Pb−Free indicator
may or may not be

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