

# 500 mW, Low Iz SOD-523 Surface Mount

This series of Zener diodes is packaged in a SOD-523 surface mount package. They are designed to provide voltage regulation protection and are especially attractive in situations where space is at a premium. They are well suited for applications such as cellular phones, hand held portables, and high density PC boards.

#### Features

- 500 mW Rating on FR-4 or FR-5 Board
- Wide Zener Reverse Voltage Range 1.8 V to 43 V
- Low Reverse Current  $(I_{ZT}) 50 \mu A$
- Package Designed for Optimal Automated Board Assembly
- Small Package Size for High Density Applications
- ESD Rating of Class 36 Milt Rower Dissipation on FR-5 Board,

ote 1) @ rated at	TL = 7 pove 75	5°C °C	PD	500 4.0	mW mW/°C	
nal Res	istance, o–Ambi	(Note 2) ent	$R_{\thetaJA}$	250	°C/W	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

T<sub>J</sub>, T<sub>stq</sub>

–55 to

+150

°C



SOD-523 CASE 502 STYLE 1



## MARKING DIAGRAM



XX = Specific Device Code

M = Date Code\* = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation may vary depending upon manufacturing location.

### **ORDERING INFORMATION**

MM5Z4xxxT1G	SOD–523 (Pb–Free)	3,000 / Tape & Reel
SZMM5Z4xxxT1G	SOD–523 (Pb–Free)	3,000 / Tape & Reel
MM5Z4xxxT5G	SOD-523 (Pb-Free)	8,000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

h and Storage Temperature Range

# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted, V<sub>F</sub> = 0.9 V Max. @ I<sub>F</sub> = 10 mA)

Parameter

Symbol

V

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25^{\circ}C$ unless otherwise noted, $V_F = 0.9 \text{ V Max}$ . @ $I_F = 10 \text{ mA}$ )
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		Zener Voltage (Note 3)				Leakage Current	
	Device	V <sub>Z</sub> (Volts)			@ I <sub>ZT</sub>	I <sub>R</sub> @ V <sub>R</sub>	
Device*	Marking	Min	Nom	Max	μA	μΑ	

**TYPICAL CHARACTERISTICS** 

## **TYPICAL CHARACTERISTICS**



Figure 6. Typical Capacitance



#### SOD-523 1.20x0.80x0.60 CASE 502 ISSUE F

DATE 08 FEB 2024

CONTROLLING DIMENSION: MILLIMETERS.
MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH, MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

#### MILLIMETERS

DIM MIN. NOM.

0.5∉SIDN**ING**OAND TOLERANCING PER ASME Y14.5M, 2





#### GENERIC MARKING DIAGRAM\*



M Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

STYLE 1: STYLE 2: PIN 1. CATHODE (POLARITY BAND) NO POLARITY 2. ANODE



\*For additional informor

ing and Mounting Techniques Reference manual, SOLDERRM/D.

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