JHF T HF/ MMBTH10M3T5G

The MMBTH10M3T5G device is a spin-off of our popular SOT-23 three-leaded device. It is designed for general purpose VHF/UHF applications and is housed in the SOT-723 surface mount package. This device is ideal for low-power surface mount applications where board space is at a premium.

Features

- Reduces Board Space
- This is a Halide–Free Device
- This is a Pb-Free Device

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	25	Vdc
Collector – Base Voltage	V _{CBO}	30	Vdc
Emitter – Base Voltage	V _{EBO}	3.0	Vdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1)	PD	265	mW
$T_A = 25^{\circ}C$	SOT-72		mW/°C
	CASE 631	AA	°C/W
Thermal Resistance, Junction-to-Ambient	\$ <u>₹</u> }¥£E	I 470	-0/00
Total Device Dissipation Alumina Substrate, (Note 2) T _A = 25°C	PD	640	mW
Derate above 25°C		5.1	mW/°C
Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	195	°C/W
Junction and Storage Temperature	T _J , T _{stg}	–55 to +150	°C

xceed g those listed in the Maximum Ratings table may damage the vice. If any of these limits are exceeded, device functionality should not be sumed, damage may occur and reliability may be affected. FR-5 = $1.0 \times 0.75 \times 0.062$ in. Alumina = $0.4 \times 0.3 \times 0.024$ in. 99.5% alumina.





	_
AJ M	
	T,

AJ Μ

= Specific Device Code = Date Code

MMBTH10M3T5G

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage $(I_C = 1.0 \text{ mAdc}, I_B = 0)$	V _{(BR)CEO}	25	-	_	Vdc
Collector-Base Breakdown Voltage $(I_C = 100 \ \mu Adc, I_E = 0)$	V _{(BR)CBO}	30	-	-	Vdc
Emitter–Base Breakdown Voltage $(I_E = 10 \ \mu Adc, I_C = 0)$	V _{(BR)EBO}				

MMBTH10M3T5G

TYPICAL CHARACTERISTICS

			600 -		

MMBTH10M3T5G

TYPICAL CHARACTERISTICS

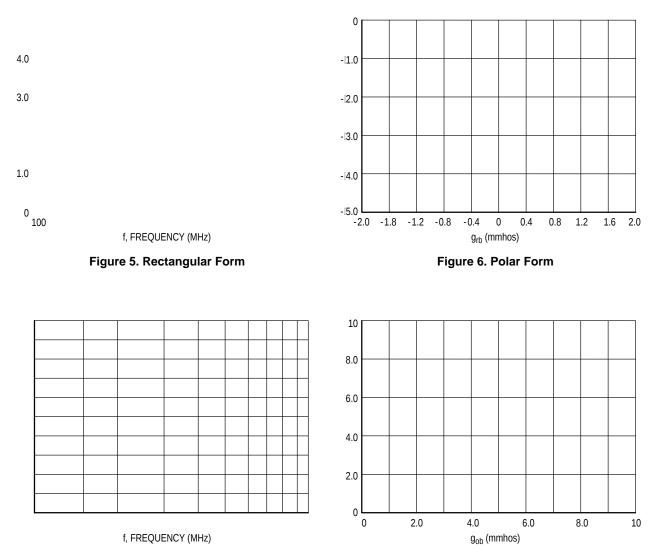


Figure 7. Rectangular Form



SOT-723 1.20x0.80x0.50, 0.40P CASE 631AA ISSUE E

DATE 24 JAN 2024

GENERIC MARKING



= Specific Device Code = Date Code ΧХ

Μ

onsemi, , and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or incruit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi