

256 Kb I²C CMOS Serial EEPROM with Software Write Protect

N24C256X

Description

Features

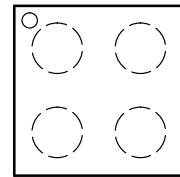
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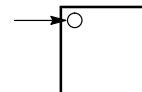
PIN CONFIGURATION



PIN FUNCTION

Pin Name	Function

MARKING DIAGRAM



ORDERING INFORMATION

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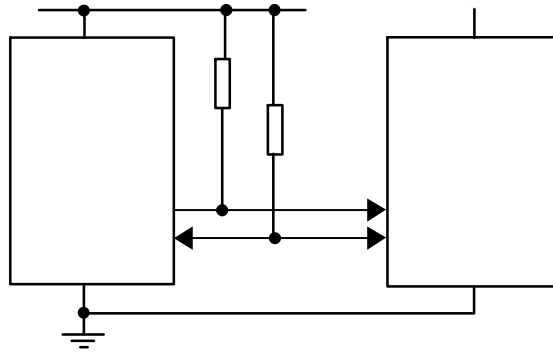


Figure 1. Functional Symbol and Typical Application

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Rating	Units
		°

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Table 4. D.C. OPERATING CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min	Max	Units

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Table 6. A.C. CHARACTERISTICS

Symbol	Parameter	Standard		Fast		Fast Plus		Units
		Min	Max	Min	Max	Min	Max	
								μ
								μ
								μ
								μ
								μ
								μ
								μ
								μ

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Power On Reset (POR)

Pin Description

SCL:

SDA:

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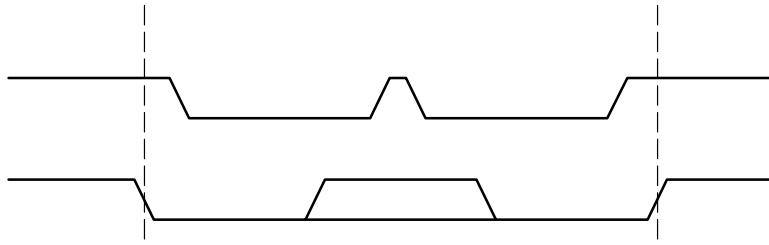


Figure 3. Start/Stop Conditions

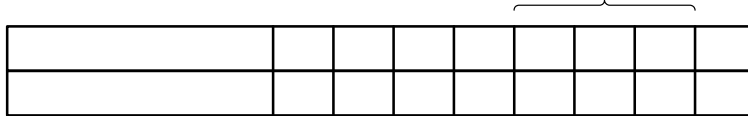


Figure 4. Slave Address Bits

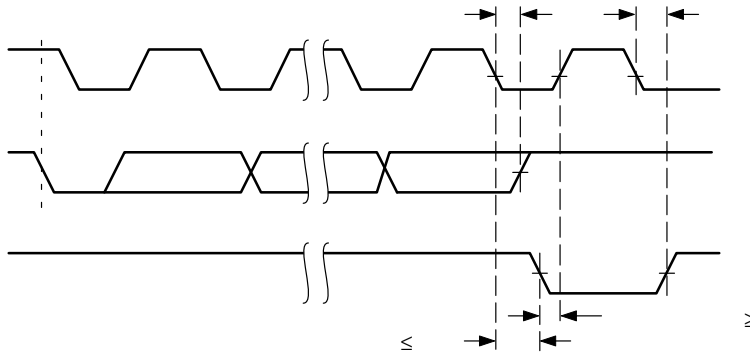


Figure 5. Acknowledge Timing

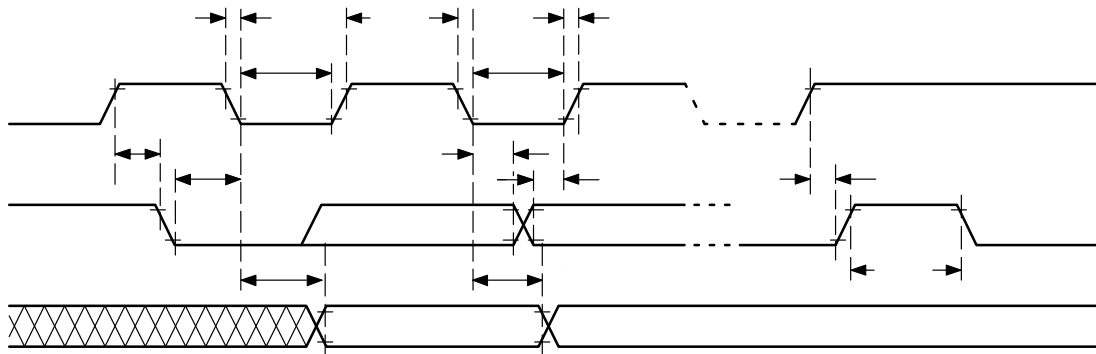


Figure 6. Bus Timing

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READ OPERATIONS

Immediate Read

Device Configuration Register Read

Selective Read

Unique ID Number Read

Sequential Read

Delivery State

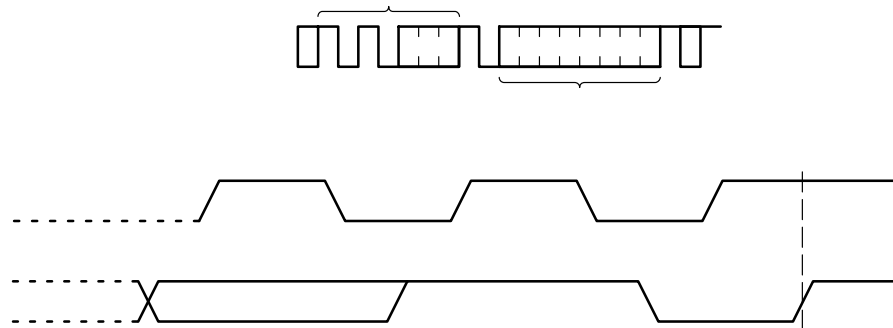


Figure 10. Immediate Read Sequence and Timing



Figure 11. Selective Read Sequence

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