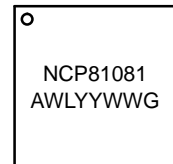
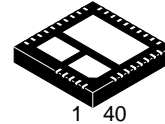


# NCP81081

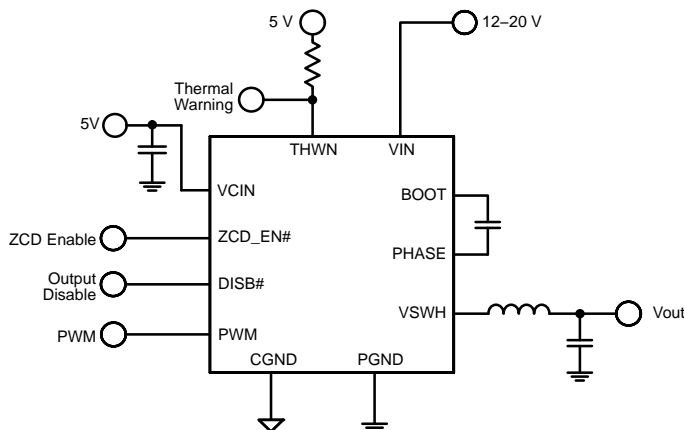
## Integrated Diode and MOSFET



ON Semiconductor

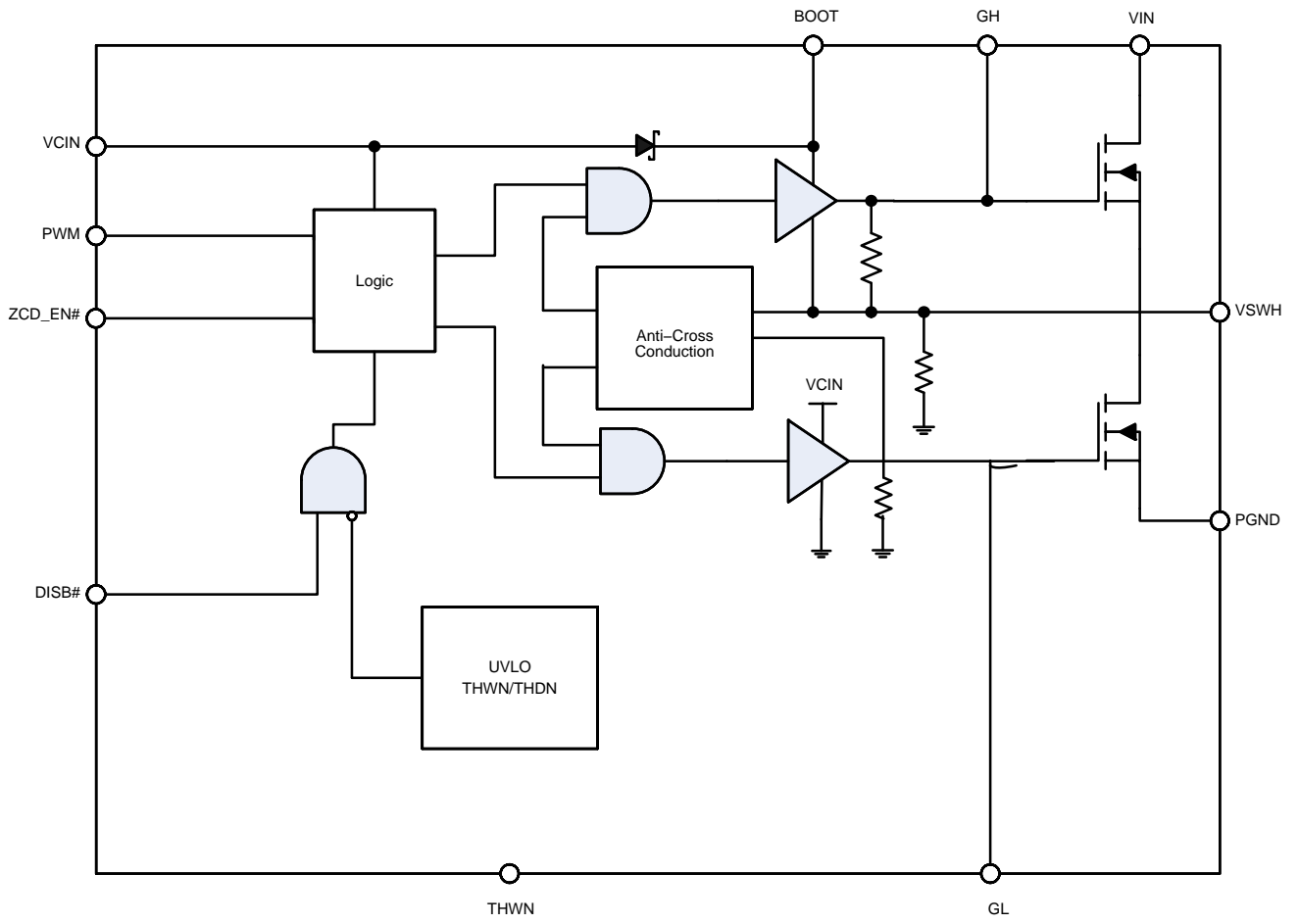


- A = Assembly Location
- WL = Wafer Lot
- YY = Year
- WW = Work Week
- G = Pb-Free Package



		†
NCP81081MNR2G	QFN40 (Pb-Free)	2500/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.





(Note 1) (VCIN = 5 V, VIN = 12 V, TA = -10°C to +100°C, unless otherwise noted)

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VCIN Current (normal mode)	-	DISB# = 5 V, PWM = OSC, FSW = 400 kHz		14	20
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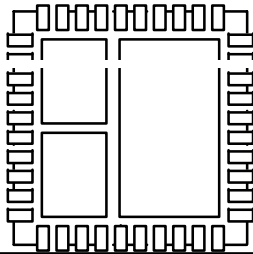
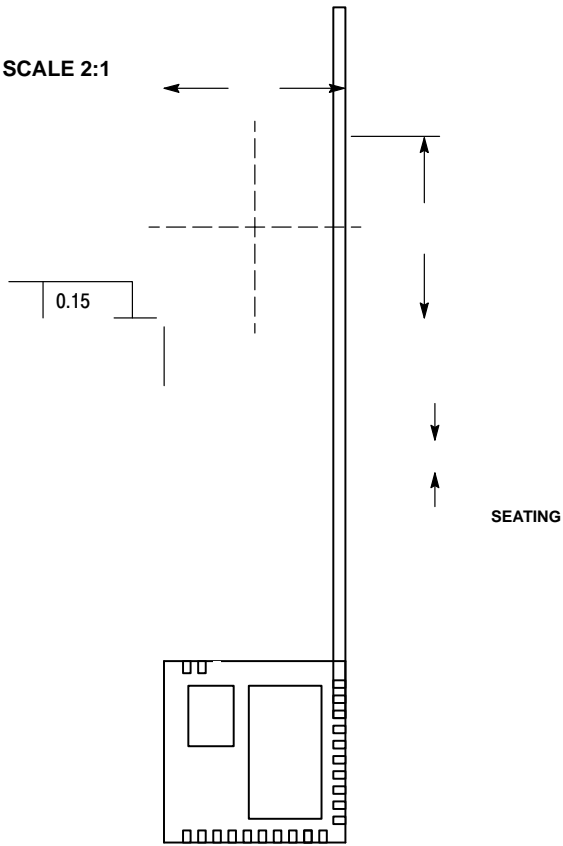
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QFN40 6x6, 0.5P  
CASE 485AZ  
ISSUE O

DATE 09 JAN 2009

SCALE 2:1



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