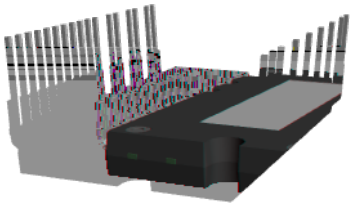
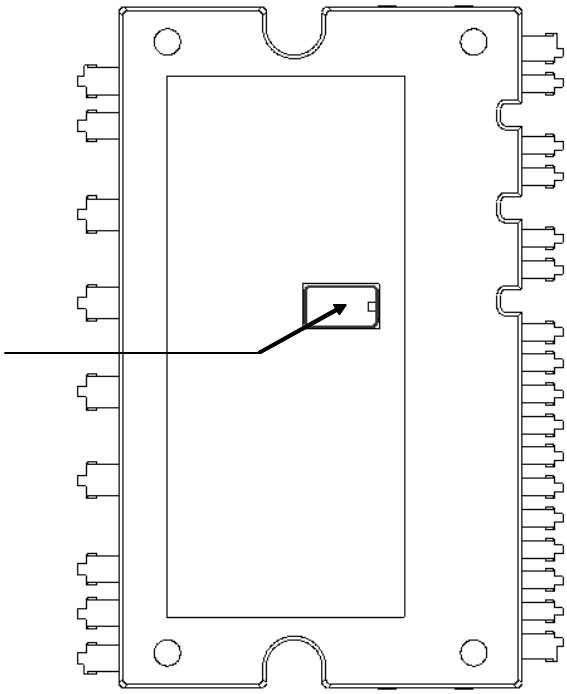


- 600 V − 15 A IGBT inverter for three−phase DC / AC power conversion (please refer to Figure 2)
- For inverter high−side IGBTs: gate drive circuit, high−voltage isolated high−speed level shifting control circuit Under−Voltage Lock−Out Protection (UVLO) NOTE: Available bootstrap circuit example is given in Figure 13.
- For inverter low−side IGBTs: gate drive circuit, Short−Circuit Protection (SCP) control supply circuit Under−Voltage Lock−Out Protection (UVLO)
- Fault signaling: corresponding to UVLO (low−side supply) and SC faults
- Input interface: active−HIGH interface, works with 3.3 / 5 V logic, Schmitt−trigger input



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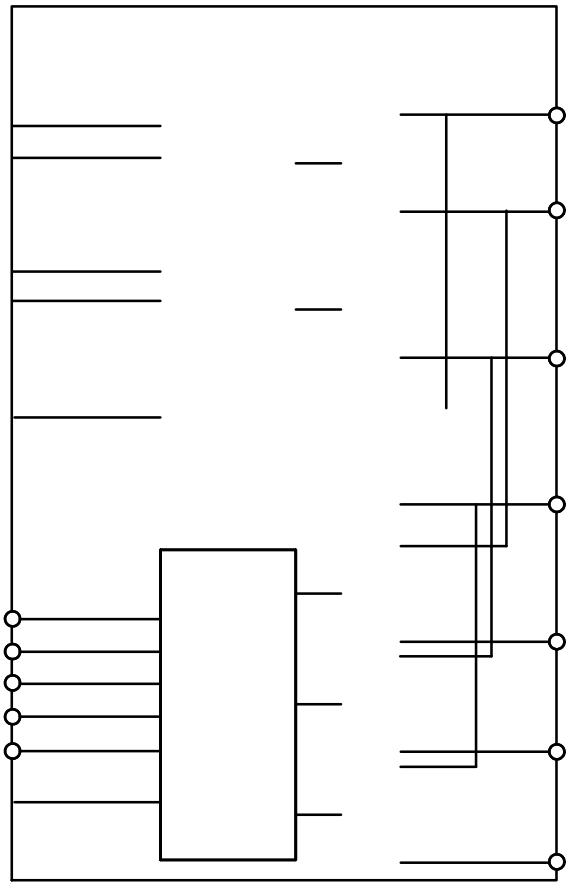

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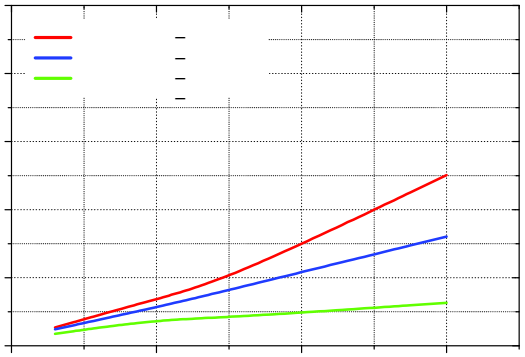

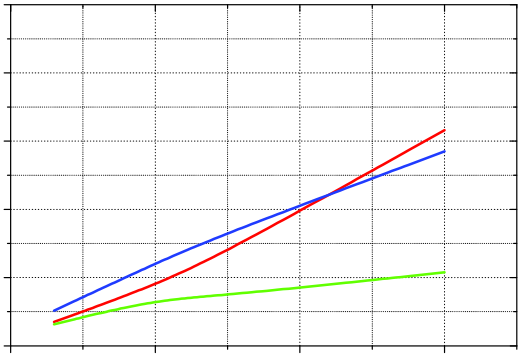

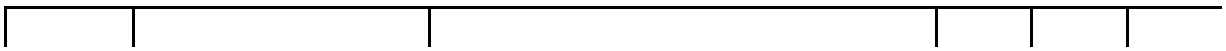

	See Figure 7			–		μ
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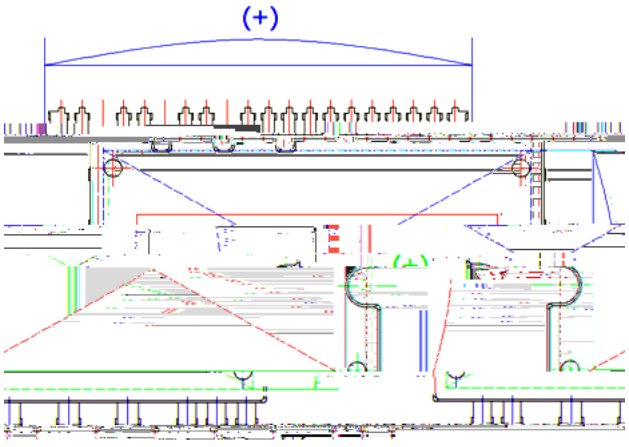

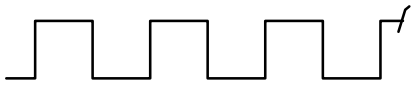

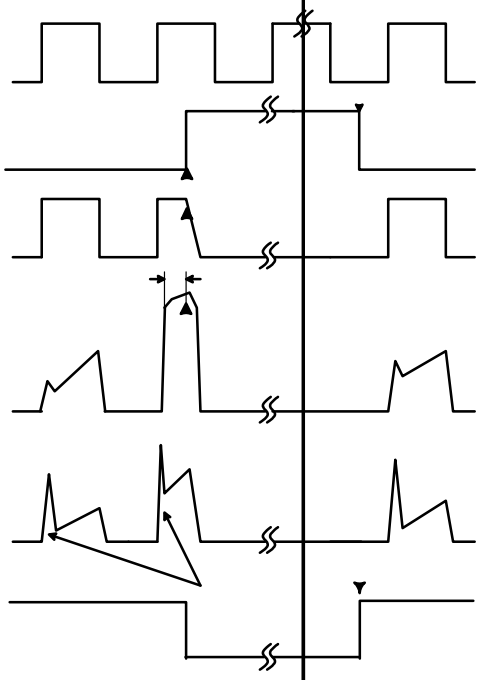

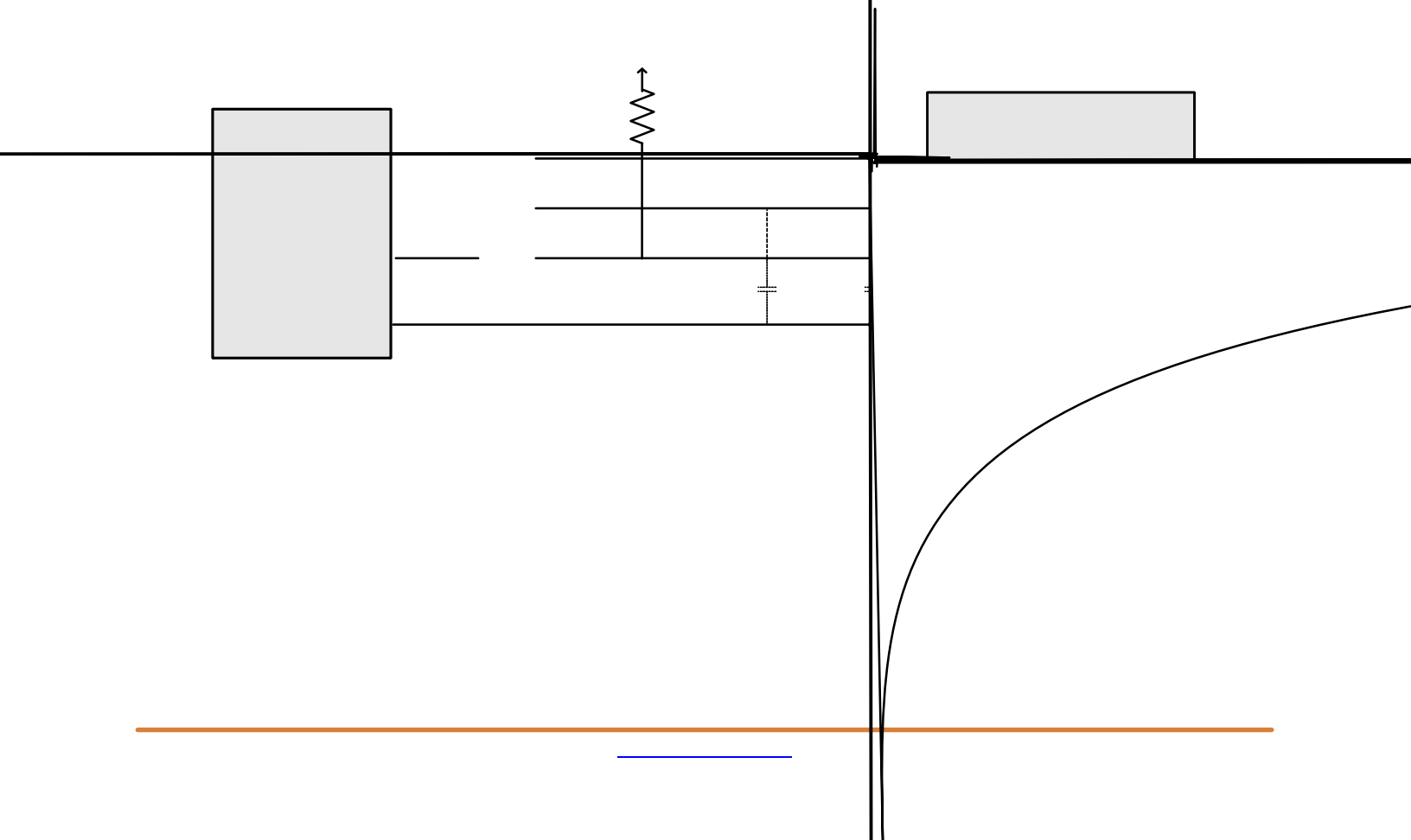

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