

- Port impedance: input/output: 50 Ω
- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- This is a Pb-Free Device

ABSOLUTE MAXIMUM RATINGS

Symbol

NSVG3109SG6

Evaluation Board

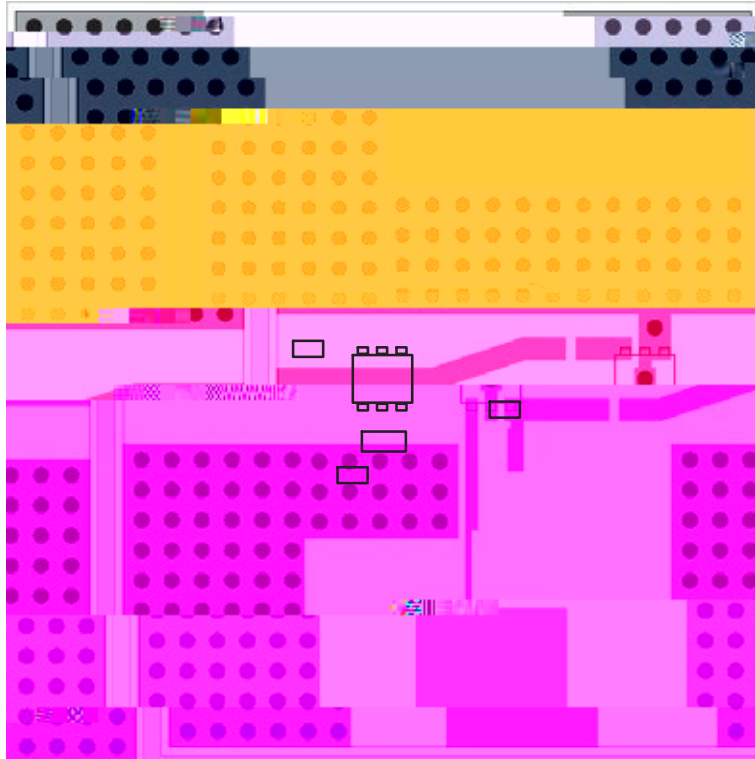
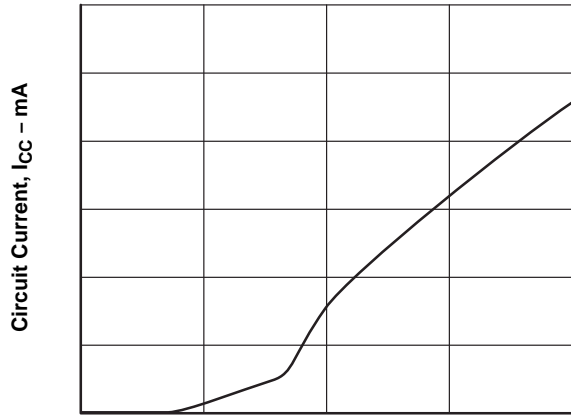


Figure 2. Evaluation Board

Characteristics



Circuit Voltage, V_{CC} - V

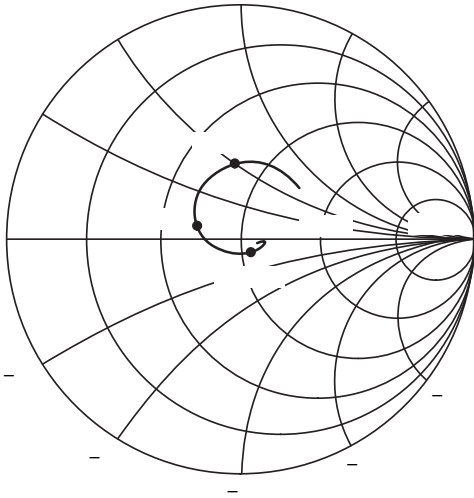
Figure 3. I_{CC} - V_{CC}

Output Level, Pout – dBm

NSVG3109SG6

S Parameter

S11



S22

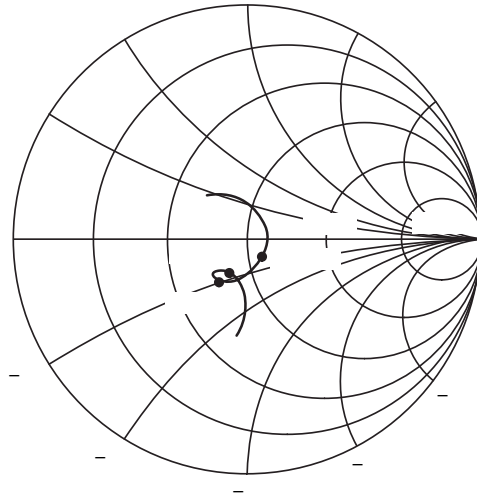
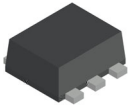


Figure 10. S Parameter

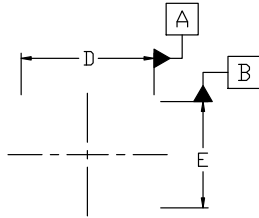
ORDERING INFORMATION

Device Order Number	Specific Device Marking	Package Type (JEITA, JEDEC)	Package Type	Shipping
		-	-	



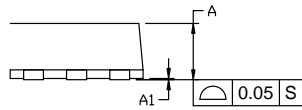
SC-88FL / MCPH6
CASE 419AS
ISSUE A

DATE 28 SEP 2022

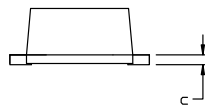


$\varnothing 0.1 \text{ (M) A}$

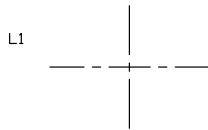
TOP VIEW



SIDE VIEW



FRONT VIEW



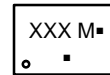
BOTTOM VIEW

NOTES:

1. NO INDUSTRY STANDARD APPLIES TO THIS PACKAGE.
2. ALL DIMENSIONS ARE IN MILLIMETERS.
3. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND THE BAR PROTRUSIONS.

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.80	0.85	0.90
A1	0.00	---	0.02
b	0.25	0.30	0.40
c	0.12	0.15	0.25
D	1.94	2.00	2.06
E	1.54	1.60	1.66
He	2.05	2.10	2.15
L	0.19	0.25	0.31
L1	0.00	0.07	0.12

GENERIC MARKING DIAGRAM*



- XXX = Specific Device Code
- M = Date Code
- = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

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