

Features

- Typ. $R_{DS(on)} = 20 \text{ m}\Omega$ @ $V_{GS} = 15 \text{ V}$
- Typ. $R_{DS(on)} = 16 \text{ m}\Omega$ @ $V_{GS} = 18 \text{ V}$
- Ultra Low Gate Charge $(Q_{G(tot)} = 200 \text{ nC})$
- Low Effective Output Capacitance (C_{oss} = 295 pF)
- 100% Avalanche Tested
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb–Free 2LI (on second level interconnection)

Typical Applications

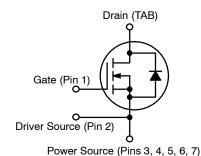
- UPS
- DC-DC Converter
- Boost Inverter

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	900	V

Gate-to-

V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
900 V	28 mΩ @ 15 V	112 A





D2PAK-7L CASE 418BJ **N-CHANNEL MOSFET**

MARKING DIAGRAM

AYWWZZ NTBG 020N090SC1

= Assembly Location Α

= Year WW = Work Week = Lot Traceability 77

NTBG020N090SC1 = Specific Device Code

٥С T_J, T_{stg} -55 to +175 Source Current (Body Diode) I_S 148 Α Single Pulse Drain-to-Source Avalanche mJ EAS 264 Energy ($I_L = 23 A_{pk}, L = 1 \text{ mH}$) (Note 4) Maximum Lead Temperature for Soldering, T_L 1/8" from Case for 10 Seconds

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. Surface mounted on a FR-4 board using 1 in 2 pad of 2 oz copper.
- 2. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 3. Repetitive rating, limited by max junction temperature.
- 4. E_{AS} of 264 mJ is based on starting $T_J = 25^{\circ}C$; L = 1 mH, $I_{AS} = 23$ A, $V_{DD} = 100 \text{ V}, V_{GS} = 15 \text{ V}.$

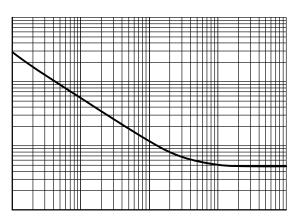
ORDERING INFORMATION

Device	Package	Shipping [†]
NTBG020N090SC1	D2PAK-7L	800 / Tape & Reel

†For information on tape and reel specifications. including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

Table 2. ELECTRICAL CHARACTERISTICS (T _J				

TYPICAL CHARACTERISTICS (continued)



TYPICAL CHARACTERISTICS (continued)

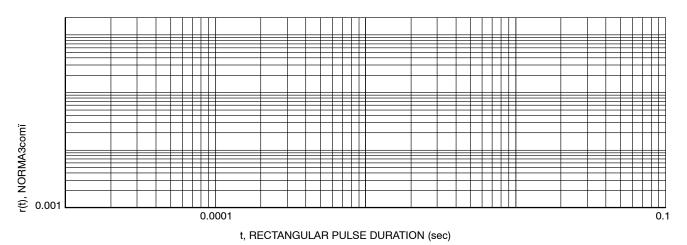


Figure 13. Junction-to-Ambient Transient Thermal Response Curve

D²PAK7 (TO-263-7L HV) CASE 418BJ ISSUE B

DATE 16 AUG 2019

Α

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C

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code A = Assembly Location

Y = Year
WW = Work Week
G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

