



# NTBG023N065M3S

## THERMAL CHARACTERISTICS

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction to Case (Note 3)	$R_{\theta JC}$	0.57	°C/W
Thermal Resistance, Junction to Ambient (Note 3)	$R_{\theta JA}$	40	

3. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.

## RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol
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## ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise specified) (continued)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>SWITCHING CHARACTERISTICS</b>						
Turn-On Delay Time	t <sub>d(ON)</sub>	V <sub>GS</sub> = -3/18 V, I <sub>D</sub> = 20 A, V <sub>DD</sub> = 400 V, R <sub>G</sub> = 4.7 Ω, T <sub>J</sub> = 175°C (Note 4, 5)	-	9.6	-	ns
Turn-Off Delay Time	t <sub>d(OFF)</sub>		-	41	-	
Rise Time	t <sub>r</sub>		-	14	-	
Fall Time	t <sub>f</sub>		-	12	-	
Turn-On Switching Loss	E <sub>ON</sub>	-3 V, I <sub>D</sub>	=	51	=	μJ
Turn-Off Switching Loss	E <sub>OFF</sub>		-	45	-	
Total Switching Loss	E <sub>TOT</sub>		-	96	-	
<b>SOURCE-TO-DRAIN DIODE CHARACTERISTICS</b>						
Forward Diode Voltage	V <sub>SD</sub>	I <sub>SD</sub> = 20 A, V <sub>GS</sub> = -3 V, T <sub>J</sub> = 25°C	=	4.5	6.0	V









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## TYPICAL CHARACTERISTICS

Switching Loss ( $\mu\text{J}$ )

$R_{\text{g}}$ , Gate Resistance ( $\Omega$ )

Figure 19. Inductive Switching Loss vs. Gate Resistance

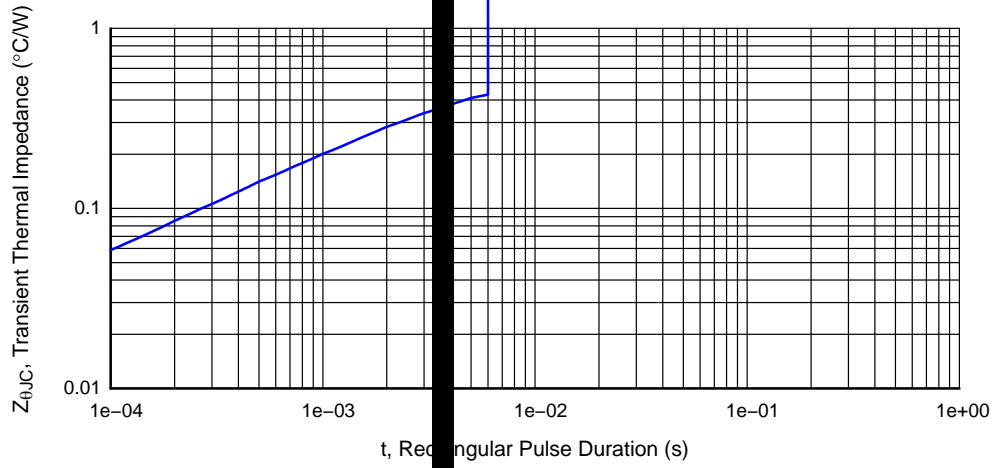


Figure 20. Thermal Response Characteristics

**D<sup>2</sup>PAK7 (TO-263-7L HV)**  
CASE 418BJ  
ISSUE B

DATE 16 AUG 2019

A

c2

H

C

**GENERIC  
MARKING DIAGRAM\***



XXXX = Specific Device Code  
A = Assembly Location  
Y = Year  
WW = Work Week  
G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



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