onsemi

S C b (SC) MOSFET - E **S** C, 23 **o** , 650 V, M3S, TOLL NTBL023N065M3S

Features

• Typical R_{DS(on)} to-Source Voltage

		V _{GS}	-8/+22	V
Continuous Drain Current	T _C = 25°C	I _D	77	А
Power Dissipation		PD	312	W
Continuous Drain Current	$T_{C} = 100^{\circ}C$	Ι _D	54	А
Power Dissipation		PD	156	W

THERMAL CHARACTERISTICS

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Note 3)	$R_{\theta JC}$	0.48	°C/W
Thermal Resistance, Junction-to-Ambient (Note 3)		43	°C/W

3. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Value	Unit
Operation Values of Gate-to-Source Voltage	V _{GSop}	-53/+18	V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise stated)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS					-	
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I_D = 1 mA	650			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	$\Delta V_{(BR)DSS}$ / ΔT_J	$I_D = 1 \text{ mA}$, Referenced to 25°C		89		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 650 \text{ V}, \text{ T}_{J} = 25^{\circ}\text{C}$			10	μΑ
		$V_{DS} = 650 \text{ V}, \text{ T}_{\text{J}} = 175^{\circ}\text{C} \text{ (Note 5)}$			500	μΑ
Gate-to-Source Leakage Current	I _{GSS}	$V_{GS} = -8/+22$ V, $V_{DS} = 0$ V			±1	μΑ
ON CHARACTERISTICS						
Drain-to-Source On Resistance	R _{DS(on)}	V_{GS} = 18 V, I_D = 20 A, T_J = 25°C		23	32.6	mΩ
		V_{GS} = 18 V, I_D = 20 A, T_J = 175°C		34		
		V_{GS} = 15 V, I_D = 20 A, T_J = 25°C		29		
		V_{GS} = 15 V, I_D = 20 A, T_J = 175°C		37		
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D = 10 \text{ mA}$	2		4	V
Forward Transconductance	g fs	V _{DS} = 10 V, I _D = 20 A		14		S
CHARGES, CAPACITANCES & GATE F	ESISTANCE					
Input Capacitance	C _{ISS}	$V_{GS} = 0 V, f = 1 MHz,$		1950		pF
Output Capacitance	C _{OSS}	$V_{\rm DS} = 400 \rm V (Note 5)$		152		
Reverse Transfer Capacitance	C _{RSS}			13		
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -3/18 \text{ V}, V_{DD} = 400 \text{ V},$		69		nC
Gate-to-Source Charge	Q _{GS}	I _D = 20 A (Note 5)		19		
Gate-to-Drain Charge	Q _{GD}			18		
Gate-Resistance	R _G	f = 1 MHz		4.0		Ω
SWITCHING CHARACTERISTICS						
Turn–On Delay Time	t _{d(ON)}	$\label{eq:VGS} \begin{array}{l} V_{GS} = -3/18 \ \text{V}, V_{DD} = 400 \ \text{V}, \\ I_D = 20 \ \text{A}, \ \text{R}_G = 4.7 \ \Omega, \\ T_J = 25^\circ\text{C}, \ (\text{Notes} \ 4, \ 5) \end{array}$		11		ns
Turn-Off Delay Time	t _{d(OFF)}			35		1
Rise Time	t _r			15		
Fall Time	t _f			9.6		
Turn–On Switching Loss	E _{ON}			51		μJ
Turn–Off Switching Loss	E _{OFF}			29		
Total Switching Loss	E		•	•	•	

ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise stated)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{d(ON)}	$V_{GS} = -3/18 \text{ V}, V_{DS} = 400 \text{ V},$		9.6		ns
Turn-Off Delay Time	t _{d(OFF)}	l _D = 20 A, R _G = 4.7 Ω, T _J = 175°C, (Notes 4, 5)		41		
Rise Time	t _r			14		
Fall Time	t _f			12		
Turn–On Switching Loss	E _{ON}			51		μJ
Turn–Off Switching Loss	E _{OFF}			45		
Total Switching Loss	E _{TOT}			96		
SOURCE-TO-DRAIN DIODE CHA	RACTERISTICS					
Forward Diode Voltage	V _{SD}	V_{GS} = -3 V, I_{SD} = 20 A, T_J = 25°C		4.5	6.0	V

Forward Diode Voltage	V _{SD}	V_{GS} = -3 V, I_{SD} = 20 A, T_{J} = 25°C	4.5	6.0	V
		$V_{GS} = -3 \text{ V}, \text{ I}_{SD} = 20 \text{ A}, \text{ T}_{J} = 175^{\circ}\text{C}$	4.2		
Reverse Recovery Time	t _{RR}	V _{GS} = -3 V, I _S = 20 A, dl/dt = 1000 A/µs, V _{DS} = 400 V	19		ns
Charge Time	ta	$di/dt = 1000 A/\mu s, v_{DS} = 400 V$ (Note 5)	11		
Discharge Time	t _b		8		
Reverse Recovery Charge	Q _{RR}		97		nC
Reverse Recovery Energy	E _{REC}		8.7		μJ
Peak Reverse Recovery Current	I _{RRM}		11		А

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.
4. E_{ON}/E_{OFF} result is with body diode.
5. Defined by design, not subject to production test.





TYPICAL CHARACTERISTICS

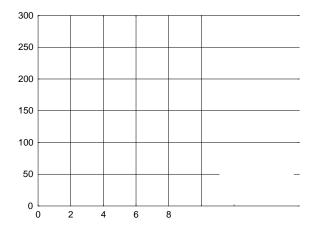


Figure 1. On–Region Characteristics

Figure 2. Output Characteristics

Figure 3. Transfer Characteristics

Figure 4. On-Resistance vs. Gate Voltage

Figure 5. On-Resistance vs. Drain Current

Figure 6. On–Resistance vs. Junction Temperature





TYPICAL CHARACTERISTICS

TYPICAL CHARACTERISTICS

Figure 19. Switching Loss vs. Gate Resistance

Figure 20. Thermal Response Characteristics





H-PSOF8L 9.90x10.38x2.30, 1.20P CASE 100DC ISSUE D

DATE 30 JUL 2024

____ D4 (2x)

- H/2

= Year

A Y

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ZZ

XXXX = Specific Device Code A = Assembly Location

= Assembly Lot Code

= Work Week

GENERIC MARKING DIAGRAM*

😡 AYWWZZ

NEW AND A

LAND PATTERN RECOMMENDATION

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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