

[Silicon Carbide \(SiC\)](#)
[MOSFET](#) - EliteSiC,
960 mohm, 1700 V, M1,
TO-247-3L

NTHL1000N170M1

- Typ. $R_{DS(on)} = 960 \text{ m}\Omega$
- Ultra Low Gate Charge (typ. $Q_{G(tot)} = 14 \text{ nC}$)
- Low Effective Output Capacitance (typ. $C_{oss} = 11 \text{ pF}$)
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Junction-to-Case - Steady State (Note 1)	$R_{\theta JC}$	3.1	$^{\circ}C/W$
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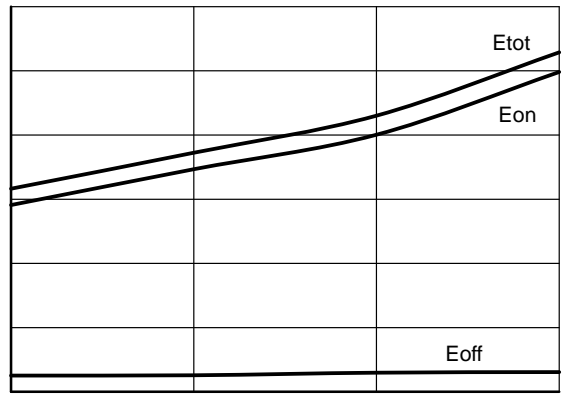
($T_J = 25^{\circ}C$ unless otherwise specified)

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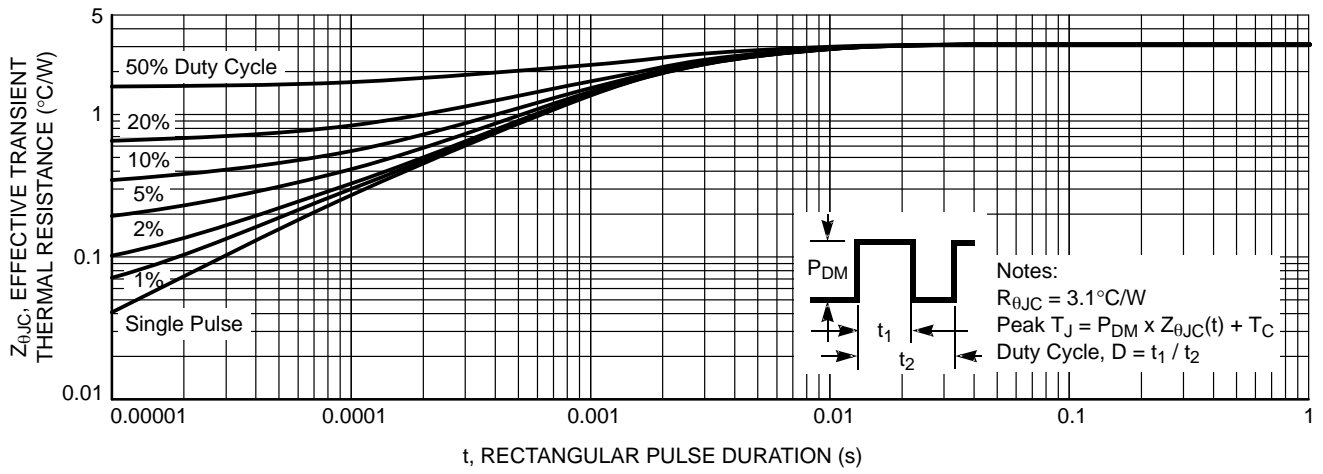
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0 V, I_D = 1 mA$	1700			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	$V_{(BR)DSS}/T_J$	$I_D = 1 mA, \text{referenced to } 25^{\circ}C$	1700			





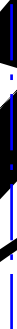
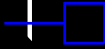
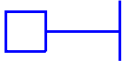






ESD-HBM	0B (125 V to <250 V)	ANSI/ESDA/JEDEC JS-001
ESD-CDM	C3 (>1000 V)	ANSI/ESDA/JEDEC JS-002

B. AD



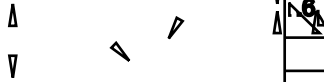
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Z

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T.

1.50 1.60

D 3



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