

# Automotive 750 V, 800 A Dual Side Cooling Half-Bridge Power Module

## VE-Trac™ Dual Gen II NVG800A75L4DSC2

### Product Description

The NVG800A75L4DSC2 is part of a family of power modules with dual side cooling and compact footprints for Hybrid (HEV) and Electric Vehicle (EV) traction inverter application.

The module consists of two narrow mesa Field Stop (FS4) IGBTs in a half-bridge configuration. The chipset utilizes the new narrow mesa IGBT technology in providing high current density and robust short circuit protection with higher blocking voltage to deliver outstanding performance in EV traction applications.

Liquid cooling heatsink reference design, loss models and CAD models are available to support customers in inverter designs.

### Features

- Dual-Side Cooling
- Integrated Chip Level Temperature and Current Sensor
- $T_{vj\ max} = 175^{\circ}\text{C}$  for Continuous Operation
- Low-stray Inductance
- Low Conduction and Switching Losses
- Automotive Grade
- 4.2 kV Isolated DBC Substrate
- AEC Qualified and PPAP Capable
- This Device is Pb-Free and is RoHS Compliant

### Typical Applications

- Hybrid and Electric Vehicle Traction Inverter
- High Power DC-DC Converter

See detailed ordering and shipping information on page 5 of

VE-



# VE-Trac™ Dual Gen II NVG800A75L4DSC2

## CHARACTERISTICS OF IGBT (Tvj = 25°C, Unless Otherwise Specified)

Parameters	Conditions	Min	Typ	Max	Unit
V <sub>CESAT</sub>					

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## CHARACTERISTICS OF INVERSE DIODE (T<sub>VJ</sub> = 25°C, Unless Otherwise Specified)

Parameters		Conditions		Min	Typ	Max	Unit
V <sub>F</sub>	Diode Forward Voltage (Terminal)	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 600 A,	T <sub>VJ</sub> = 25°C	-	1.39	1.80	V
			T <sub>VJ</sub> = 150°C		1.36		
			T <sub>VJ</sub> = 175°C		1.34		
		V <sub>GE</sub> = 0 V, I <sub>C</sub> = 800 A,	T <sub>VJ</sub> = 25°C	1.49			
			T <sub>VJ</sub> = 150°C	1.48			
			T <sub>VJ</sub> = 175°C	1.47			

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## TYPICAL CHARACTERISTICS

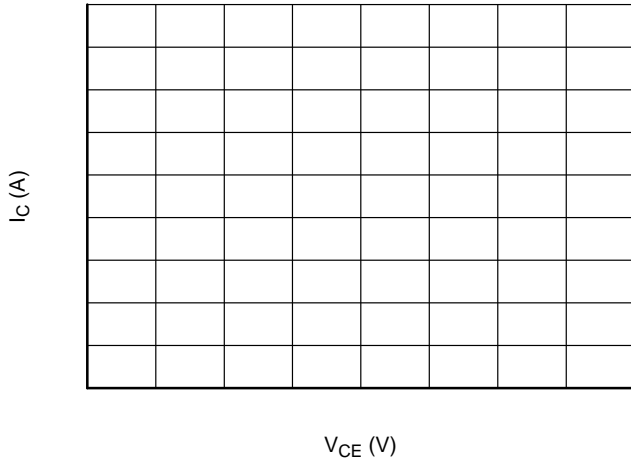


Figure 13. Reverse Bias Safe Operating Area

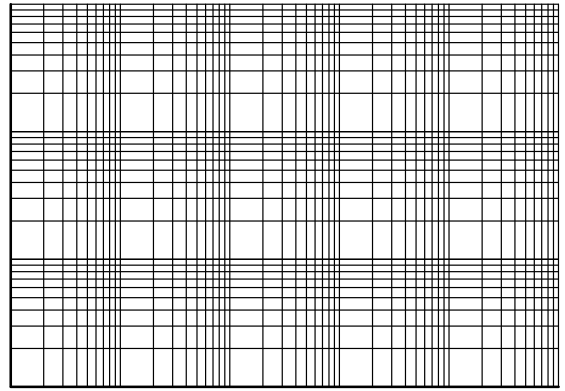


Figure 14. IGBT Transient Thermal Impedance

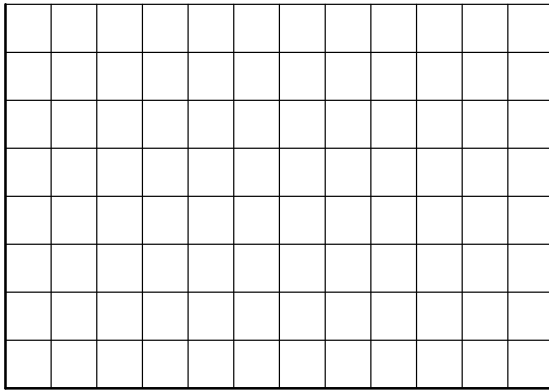


Figure 15. Diode Forward Characteristic

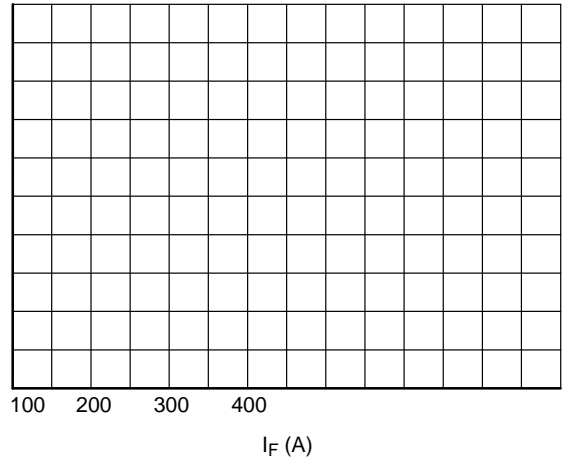


Figure 16. Diode Switching Losses vs.  $I_F$

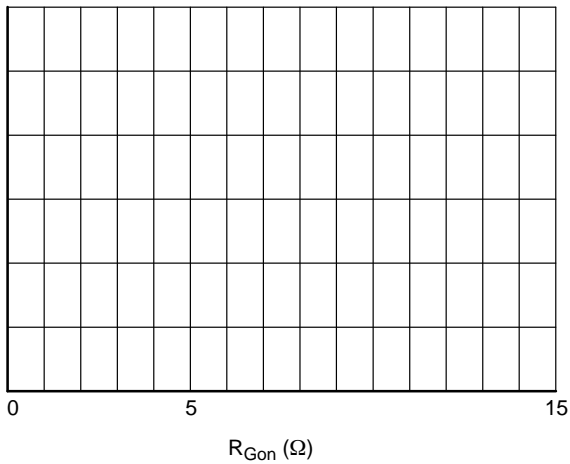


Figure 17. Diode Switching Losses vs.  $R_{Gon}$

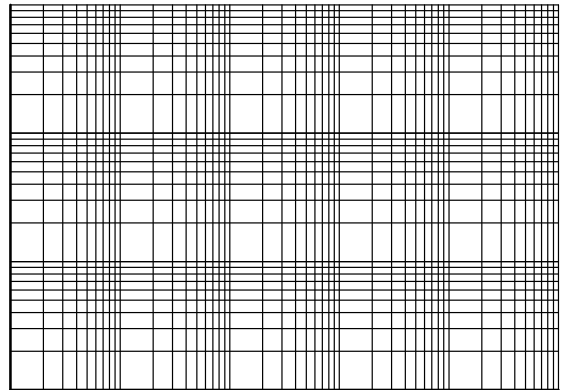


Figure 18. Diode Transient Thermal Impedance



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## TYPICAL CHARACTERISTICS

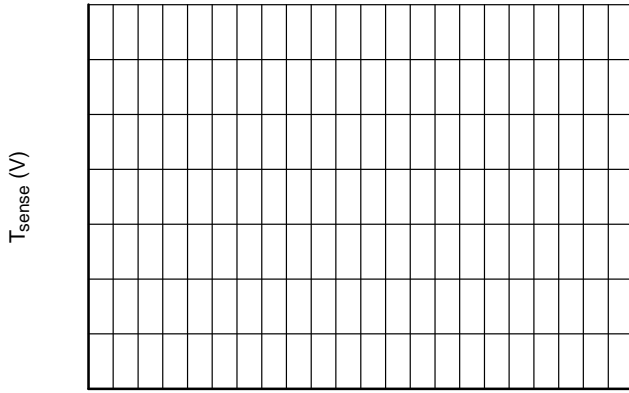


Figure 19. Temperature Sensor Characteristics

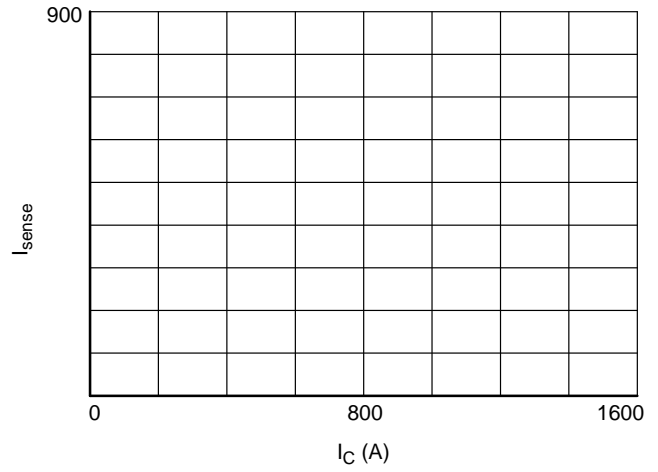
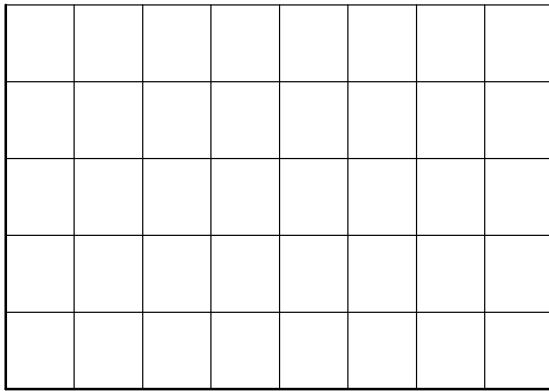
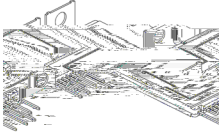


Figure 20. Current Sensor Characteristic



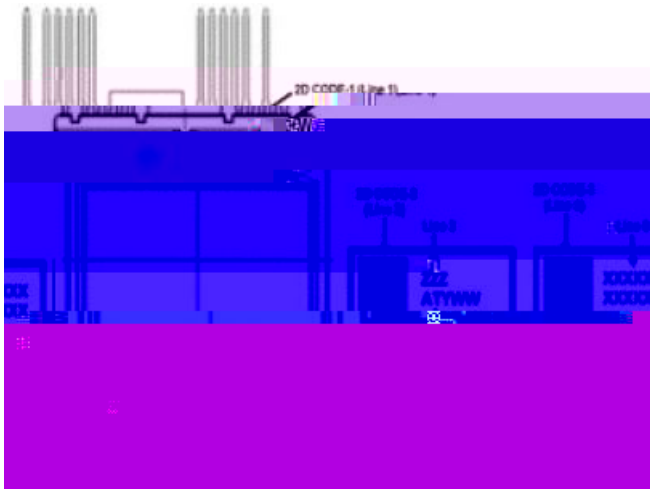


## AHPM15 55x55

4. DIMENSIONS  $b, b_1, b_2$  .□

AHPM15 55x55

GENERIC  
MARKING DIAGRAM\*



DOCUMENT NUMBER:	98AON32090H	
DESCRIPTION:	AHPM15 55x55	PAGE 2 OF 2

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